

## 4A, 700V N-CHANNEL POWER MOSFET

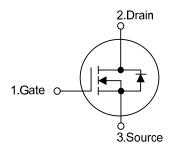
### DESCRIPTION

The UTC **4N70K-TC** is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche. This high speed switching power MOSFET is usually used in power supplies, PWM motor controls, high efficient AC to DC converters and bridge circuits.

### FEATURES

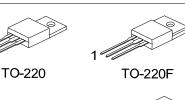
- \*  $R_{DS(ON)} \le 3.3 \Omega$  @  $V_{GS}$ =10V,  $I_D$ =2.0A
- \* Fast Switching Capability
- \* Avalanche Energy Specified
- \* Improved dv/dt Capability, High Ruggedness

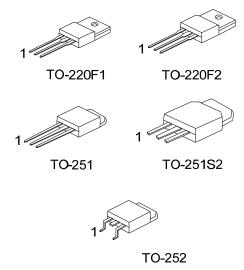
#### SYMBOL



### ORDERING INFORMATION

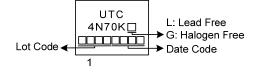
Ordering Number		Package	Pin Assignment			Dealving	
Lead Free	Lead Free Halogen Free		1	2	3	Packing	
4N70KL-TA3-T	4N70KL-TA3-T 4N70KG-TA3-T		G	D	S	Tube	
4N70KL-TF1-T	4N70KL-TF1-T 4N70KG-TF1-T		G	D	S	Tube	
4N70KL-TF2-T	4N70KL-TF2-T 4N70KG-TF2-T		G	D	S	Tube	
4N70KL-TF3-T	4N70KL-TF3-T 4N70KG-TF3-T		G	D	S	Tube	
4N70KL-TM3-T	4N70KL-TM3-T 4N70KG-TM3-T		G	D	S	Tube	
4N70KL-TMS2-T	4N70KL-TMS2-T 4N70KG-TMS2-T		G	D	S	Tube	
4N70KL-TN3-R 4N70KG-TN3-R		TO-252	G	D	S	Tape Reel	
Note: Pin Assignment: G: Gate D: Drain S: Source							
4N70KG-TA3-T (1)Packing Type   (2)Package Type (2)Package Type   (3)Green Package (3)Green Package					FO-251S2,		





### Power MOSFET

### MARKING





PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V <sub>DSS</sub>	700	V
Gate-Source Voltage		V <sub>GSS</sub>	±30	V
Drain Current	Continuous	ID	4	А
	Pulsed (Note 2)	I <sub>DM</sub>	8	А
Avalanche Energy	Single Pulsed (Note 3)	E <sub>AS</sub>	86	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	3.3	V/ns
Power Dissipation	TO-220		106	W
	TO-220F/TO-220F1 TO-220F2	P <sub>D</sub>	32	W
	TO-251/TO-251S2 TO-252	-	49	W
Junction Temperature		TJ	+150	°C
Storage Temperature		T <sub>STG</sub>	-55 ~ +150	°C

#### ■ ABSOLUTE MAXIMUM RATINGS (T<sub>c</sub> = 25°C, unless otherwise specified)

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature.

3. L = 30mH,  $I_{AS}$  = 2.4A,  $V_{DD}$  = 50V,  $R_G$  = 25  $\Omega$ , Starting  $T_J$  = 25°C

4. I<sub>SD</sub>≤ 4.0A, di/dt ≤200A/µs, V<sub>DD</sub>≤ BV<sub>DSS</sub>, Starting T<sub>J</sub> =  $25^{\circ}$ C

#### THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction-to-Ambient	TO-220/TO-220F TO-220F1/TO-220F2	0	62.5	°C/W
	TO-251/TO-251S2 TO-252	θ <sub>JA</sub>	110	°C/W
Junction-to-Case	TO-220		1.18	°C/W
	TO-220F/TO-220F1 TO-220F2	θ <sub>JC</sub>	3.9	°C/W
	TO-251/TO-251S2 TO-252		2.55 (Note)	°C/W

Note: Device mounted on FR-4 substrate P<sub>C</sub> board, 2oz copper, with 1inch square copper plate.



### ■ **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub>=25°C, unless otherwise specified)

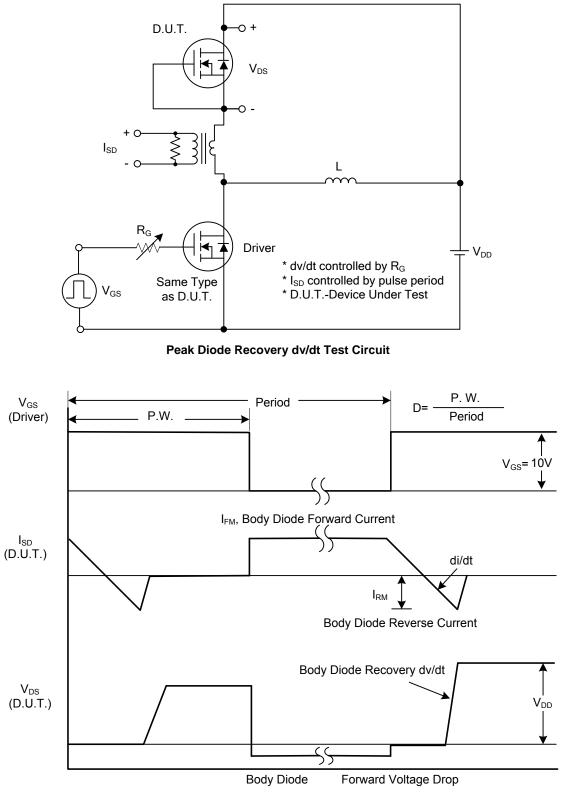
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250µA	700			V
Drain-Source Leakage Current		I <sub>DSS</sub>	V <sub>DS</sub> =700V, V <sub>GS</sub> =0V			10	μA
Gate-Source Leakage Current	Forward	I <sub>GSS</sub>	V <sub>GS</sub> =30V, V <sub>DS</sub> =0V			100	nA
	Reverse		V <sub>GS</sub> =-30V, V <sub>DS</sub> =0V			-100	IIA
ON CHARACTERISTICS					-		-
Gate Threshold Voltage		V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250µA	2.0		4.0	V
Static Drain-Source On-State Resistance		R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =2.0A			3.3	Ω
DYNAMIC CHARACTERISTICS					-		-
Input Capacitance		C <sub>ISS</sub>			515		рF
Output Capacitance		Coss	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1MHz		52		pF
Reverse Transfer Capacitance		C <sub>RSS</sub>			4.8		рF
SWITCHING CHARACTERISTIC	S						
Total Gate Charge (Note 1)		$Q_{G}$			16.2		nC
Gate to Source Charge		$Q_{GS}$	V <sub>DS</sub> =560V, V <sub>GS</sub> =10V, I <sub>D</sub> =4.0A I <sub>G</sub> = 1mA (Note1, 2)		5		nC
Gate to Drain Charge		$Q_{GD}$	$I_G = IIIIA (Note I, 2)$		2.6		nC
Turn-on Delay Time (Note 1)		t <sub>D(ON)</sub>			7		ns
Rise Time		t <sub>R</sub>	$V_{DS}$ =100V, $V_{GS}$ =10V, $I_{D}$ =4.0A, $R_{G}$ =25 $\Omega$ (Note1, 2)		16		ns
Turn-off Delay Time		t <sub>D(OFF)</sub>			39		ns
Fall-Time		t⊢			29		ns
SOURCE- DRAIN DIODE RATIN	GS AND CH	ARACTERIS	TICS		-		-
Maximum Body-Diode Continuous Current		ls				4	Α
Maximum Body-Diode Pulsed Current		I <sub>SM</sub>				8	Α
Drain-Source Diode Forward Voltage (Note 1)		$V_{SD}$	V <sub>GS</sub> =0V, I <sub>S</sub> =4.0A			1.4	V
Reverse Recovery Time (Note 1)		t <sub>rr</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =4.0A,		248		nS
Reverse Recovery Charge		Qrr	dI <sub>F</sub> /dt=100A/µs		3.3		μC

Notes: 1. Pulse Test: Pulse width  $\leq$  300µs, Duty cycle  $\leq$  2%.

2. Essentially independent of operating temperature.



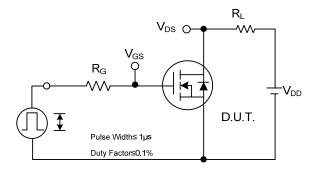
### TEST CIRCUITS AND WAVEFORMS



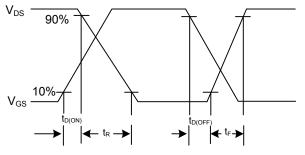
Peak Diode Recovery dv/dt Waveforms



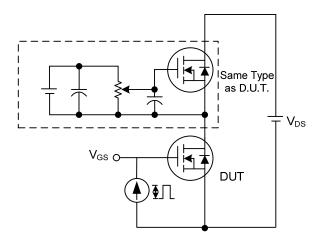
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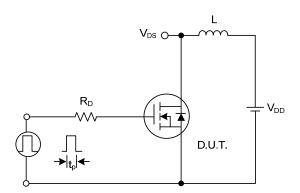




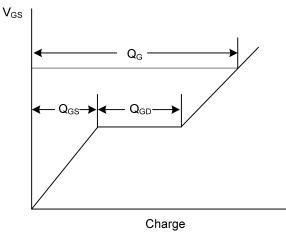




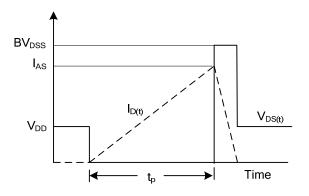
Gate Charge Test Circuit



**Unclamped Inductive Switching Test Circuit** 











5

4

3

2

1

0

0

Drain Current, I<sub>D</sub> (A)

Note:

1.T<sub>A</sub>=25°C

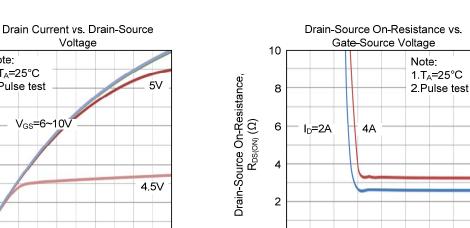
2.Pulse test

4

V<sub>GS</sub>=6~10V

### **TYPICAL CHARACTERISTICS**

Voltage



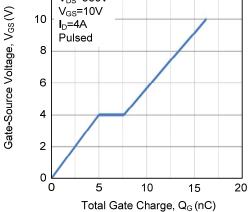
Drain-Source Voltage, V<sub>DS</sub>(V) Gate Charge Characteristics 12 V<sub>DS</sub>=560V V<sub>GS</sub>=10V 10 I<sub>D</sub>=4A Pulsed 8 6 4

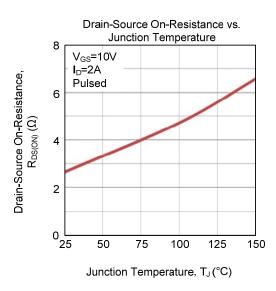
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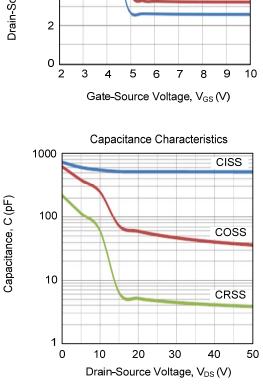
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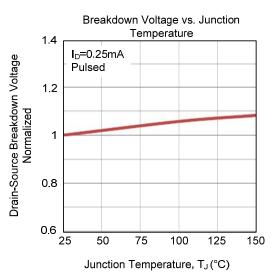
16

20



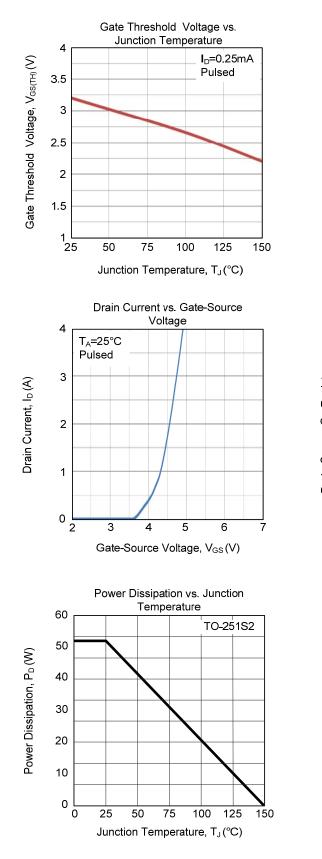


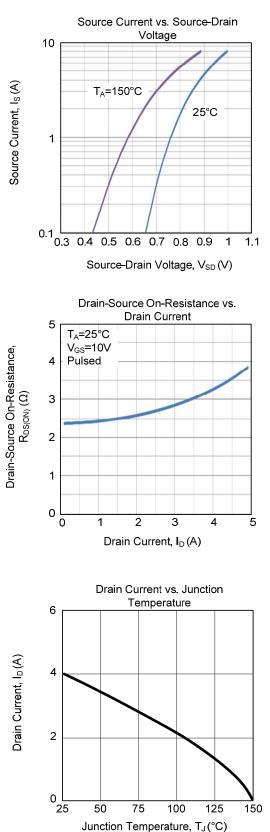






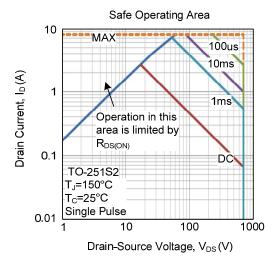
## ■ TYPICAL CHARACTERISTICS (Cont.)





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## TYPICAL CHARACTERISTICS (Cont.)



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