

UNISONIC TECHNOLOGIES CO., LTD

MGBR10L150 Preliminary DIODE

MOS GATED BARRIER RECTIFIER

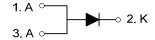
■ DESCRIPTION

The UTC MGBR10L150 is a surface mount mos gated barrier rectifier, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed, etc.

■ FEATURES

- * Low forward voltage drop
- * High switching speed

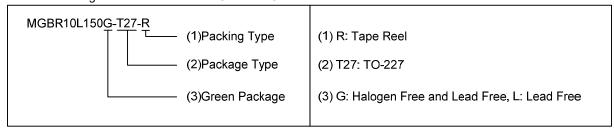
■ SYMBOL



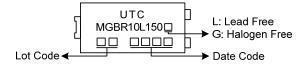
■ ORDERING INFORMATION

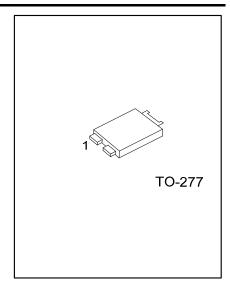
Ordering Number		Dealsons	Pin Assignment			Dealine	
Lead Free	Halogen Free	Package	1	2	3	Packing	
MGBR10L150L-T27-R	MGBR10L150G-T27-R	TO-277	Α	K	Α	Tape Reel	

Note: Pin Assignment: A: Anode K: Common Cathode



MARKING





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■ **ABSOLUTE MAXIMUM RATINGS** (T_A=25°C unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

PARAMETER	SYMBOL	RATINGS	UNIT
DC Blocking Voltage	V_{RM}	150	V
Working Peak Reverse Voltage	V_{RWM}	150	V
Peak Repetitive Reverse Voltage	V_{RRM}	150	V
Average Rectified Output Current T _C =125°C	Io	10	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	150	Α
Operating Junction Temperature	TJ	-65 ~ +150	Ŝ
Storage Temperature	T _{STG}	-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θ_{JA}	73	°C/W	
Junction to Case	θ_{JC}	13	°C/W	

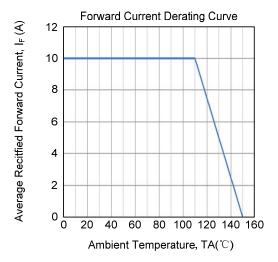
■ **ELECTRICAL CHARACTERISTICS** (T_A =25°C unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	$V_{(BR)R}$	I _R =0.50mA	150			٧
Forward Voltage Drop	V _{FM}	I _F =10A, T _J =25°C		0.8	0.9	V
		I _F =10A, T _J =125°C			0.8	V
Leakage Current (Note 1)	I _{RM}	V _R =150V, T _J =25°C		25	200	μΑ
		V _R =150V, T _J =125°C			50	mΑ

Notes: 1. Short duration pulse test used to minimize self-heating effect.

2. Thermal resistance junction to case mounted on heatsink.

■ TYPICAL CHARACTERISTICS



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