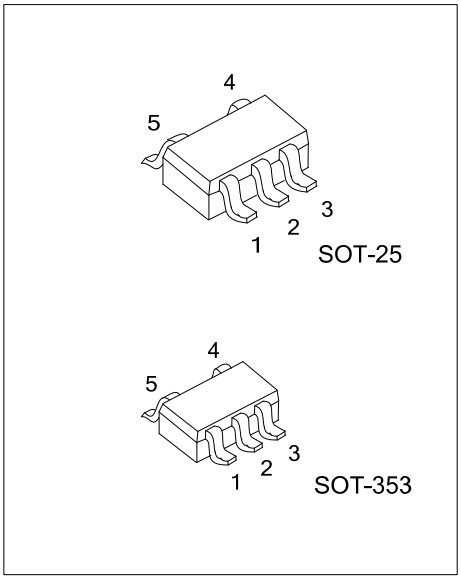
U74AUC1G126

CMOS IC

SINGLE BUS BUFFER GATE WITH 3-STATE OUTPUT



■ DESCRIPTION

The **U74AUC1G126** is single bus buffer gate with 3-state output. The output is disabled When the output enable (OE) is low. When OE is high, true data is passed from A input to the Y output.

This device has power-down protective circuit, preventing device destruction when it is powered down.

■ FEATURES

* Operate from 0.9V to 2.7V
* Low power dissipation: I_{CC}=10μA (Max.)
* ±8mA Output Driver : V_{CC}=1.8V
* I_{off} Supports partial-Power-Down Mode Operation

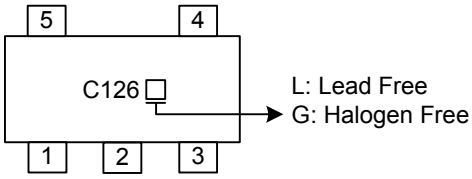
■ ORDERING INFORMATION

Ordering Number		Package	Packing
Lead Free	Halogen Free		
U74AUC1G126L-AF5-R	U74AUC1G126G-AF5-R	SOT-25	Tape Reel
U74AUC1G126L-AL5-R	U74AUC1G126G-AL5-R	SOT-353	Tape Reel

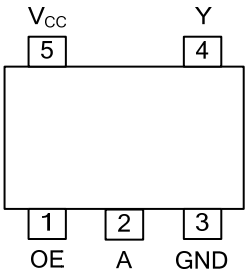
U74AUC1G126G-AF5-R
(1)Packing Type
(2)Packing Type
(3)Green Package

(1) R: Tape Reel
(2) AF5: SOT-25, AL5: SOT-353
(3) G: Halogen Free and Lead Free

■ MARKING



■ PIN CONFIGURATION

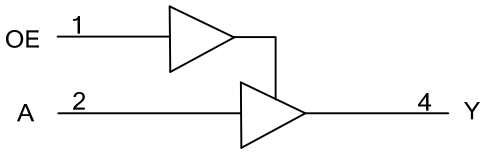


■ FUNCTION TABLE

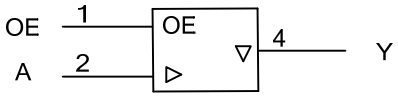
INPUT(OE)	INPUT(A)	OUTPUT(Y)
H	H	H
H	L	L
L	X	Z

Note: H: HIGH voltage level; L: LOW voltage level; X=don't care; Z=high-impedance OFF-state.

■ LOGIC DIAGRAM (positive logic)



Logic symbol



IEC logic symbol

■ ABSOLUTE MAXIMUM RATING

PARAMETER	SYMBOL	TEST CONDITIONS	RATINGS	UNIT
Supply Voltage	V_{CC}		-0.5 ~ +3.6	V
Input Voltage	V_{IN}		-0.5 ~ +3.6	V
Output Voltage	V_{OUT}	Enable mode	-0.5 ~ V_{CC} + 0.5	V
		Disable mode	-0.5 ~ +3.6	V
		Power-down mode	-0.5 ~ +3.6	V
V_{CC} or GND Current	I_{CC}		±100	mA
Continuous Output Current	I_{OUT}	V_{OUT}=0 ~ V_{CC}	±20	mA
Input Clamp Current	I_{IK}	V_{IN}<0	-50	mA
Output Clamp Current	I_{OK}	V_{OUT}>V_{CC} or V_{OUT}<0	-50	mA
Storage Temperature Range	T_{STG}		-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage	V_{CC}	Operating	0.9		2.7	V
Input Voltage	V_{IN}		0		3.6	V
Output Voltage	V_{OUT}	High or low state	0		V_{CC}	V
Input Transition Rise or Fall Rate	Δt/Δv	V_{CC}=0.9V ~ 1.6V			20	ns/V
		V_{CC}=1.65V ~ 1.95V			10	ns/V
		V_{CC}=2.3V ~ 2.7V			3	ns/V
Operating Temperature	T_A		-40		+125	°C

■ ELECTRICAL CHARACTERISTICS (T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
High-level Input Voltage	V_{IH}	V_{CC}=0.9V	V_{CC}			V
		V_{CC}=1.1V ~ 1.95V	0.65×V_{CC}			V
		V_{CC}=2.3V ~ 2.7V	1.7			V
Low-level Input Voltage	V_{IL}	V_{CC}=0.9V			0	V
		V_{CC}=1.1V ~ 1.95V			0.35×V_{CC}	V
		V_{CC}=2.3V ~ 2.7V			0.7	V
High-Level Output Voltage	V_{OH}	V_{CC}=0.9V ~ 2.7V, I_{OH}=-100μA	V_{CC}-0.1			V
		V_{CC}=0.9V, I_{OH}=-0.7mA		0.45		V
		V_{CC}=1.1V, I_{OH}=-3mA	0.8			V
		V_{CC}=1.4V,I_{OH}=-5mA	1			V
		V_{CC}=1.65V, I_{OH}=-8mA	1.2			V
		V_{CC}=2.3V, I_{OH}=-9mA	1.8			V
Low-Level Output Voltage	V_{OL}	V_{CC}=0.9V ~ 2.7V, I_{OL}=100μA			0.2	V
		V_{CC}=0.9V, I_{OL}=0.7mA		0.25		V
		V_{CC}=1.1V, I_{OL}=3mA			0.3	V
		V_{CC}=1.4V, I_{OL}=5mA			0.4	V
		V_{CC}=1.65V, I_{OL}=8mA			0.45	V
		V_{CC}=2.3V, I_{OL}=9mA			0.6	V
Input Leakage Current	$I_{I(LEAK)}$	V_{CC}=2.7V, V_{IN}= V_{CC} or GND			±5	μA
Power OFF Leakage Current	I_{off}	V_{CC}=0V, V_{IN} or V_{OUT}=2.7V			±10	μA
3-state Output OFF-state Current	I_{OZ}	V_{CC} =2.7V, V_{OUT}=V_{CC} or GND			±10	μA
Quiescent Supply Current	I_{CC}	V_{CC}=0.9V to 2.7V, V_{IN}=V_{CC} or GND, I_{OUT}=0			10	μA
Input Capacitance	C_I	V_{CC}=2.5V, V_{IN}=V_{CC} or GND		5		pF
output Capacitance	C_O	V_{CC}=2.5V, V_{IN}=V_{CC} or GND		7.6		pF

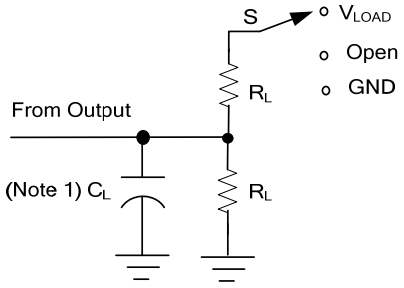
■ SWITCHING CHARACTERISTICS (T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Propagation delay from input A to output Y	t_{PLH} / t_{PHL}	C_L=15pF, R_L=2kΩ	V_{CC}=0.9V		25		ns
			V_{CC}=1.2±0.1V	6.1		10.5	ns
			V_{CC}=1.5±0.1V	4.5		8.5	ns
			V_{CC}=1.8±0.15V	2.5		5.5	ns
			V_{CC}=2.5±0.2V	1		3.1	ns
		C_L=30pF, R_L=1kΩ	V_{CC}=1.8±0.15V	2.5		5.5	ns
		C_L=30pF, R_L=500Ω	V_{CC}=2.5±0.2V	1.5		3.5	ns
3-state output enable time from input $\overline{OE}$ to output Y	t_{PZH} / t_{PZL}	C_L=15pF, R_L=2kΩ	V_{CC}=0.9V		25		ns
			V_{CC}=1.2±0.1V	8.5		13	ns
			V_{CC}=1.5±0.1V	6		10	ns
			V_{CC}=1.8±0.15V	4		7	ns
			V_{CC}=2.5±0.2V	0.5		3.3	ns
		C_L=30pF, R_L=1kΩ	V_{CC}=1.8±0.15V	4.5		7.5	ns
		C_L=30pF, R_L=500Ω	V_{CC}=2.5±0.2V	0.8		4	ns
3-state output disable time from input $\overline{OE}$ to output Y	t_{PLZ} / t_{PLH}	C_L=15pF, R_L=2kΩ	V_{CC}=0.9V		25		ns
			V_{CC}=1.2±0.1V	3.8		9.5	ns
			V_{CC}=1.5±0.1V	2.3		8.6	ns
			V_{CC}=1.8±0.15V	1.5		7.2	ns
			V_{CC}=2.5±0.2V	0.9		5	ns
		C_L=30pF, R_L=1kΩ	V_{CC}=1.8±0.15V	1.8		7.2	ns
		C_L=30pF, R_L=500Ω	V_{CC}=2.5±0.2V	0.8		4	ns

■ OPERATING CHARACTERISTICS (f=10MHz, T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Power Dissipation Capacitance (Outputs enabled)	C_{PD}	V_{CC}=0.9V		14		pF
		V_{CC}=1.2V		14		pF
		V_{CC}=1.5V		14		pF
		V_{CC}=1.8V		15		pF
		V_{CC}=2.5V		16		pF
Power Dissipation Capacitance (Outputs Disabled)	C_{PD}	V_{CC}=0.9V		1.5		pF
		V_{CC}=1.2V		1.5		pF
		V_{CC}=1.5V		1.5		pF
		V_{CC}=1.8V		2		pF
		V_{CC}=2.5V		2.5		pF

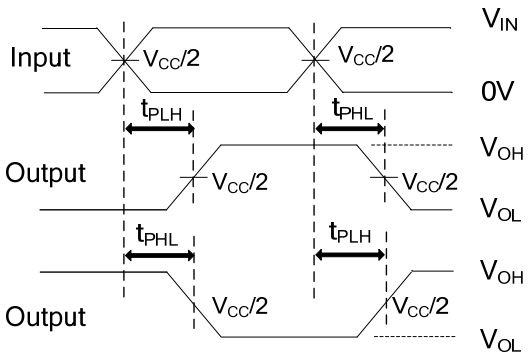
■ TEST CIRCUIT AND WAVEFORMS



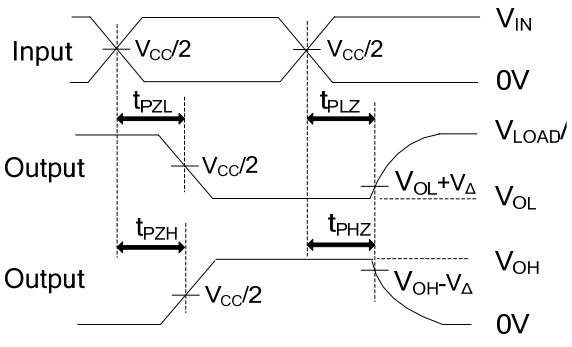
TEST	S
t_{PLH}/t_{PHL}	Open
t_{PHZ}/t_{PZH}	GND
t_{PLZ}/t_{PZL}	$2\times V_{CC}$

TEST CIRCUIT

V_{CC}	C_L	R_L	V_Δ
0.9V	15pF	2kΩ	0.1V
1.2V±0.1V	15pF	2kΩ	0.1V
1.5V±0.1V	15pF	2kΩ	0.1V
1.8V±0.15V	15pF	2kΩ	0.15V
2.5V±0.2V	15pF	2kΩ	0.15V
1.8V±0.15V	30pF	1kΩ	0.15V
2.5V±0.2V	30pF	500Ω	0.15V



PROPAGATION DELAY TIMES



ENABLE AND DISABLE TIMES

Notes: 1. C_L includes probe and jig capacitance.
2. All input pulses are supplied by generators having the following characteristics: PRR ≤10MHz, Z_O = 50Ω.