UNISONIC TECHNOLOGIES CO., LTD

8N60K-MTQ Power MOSFET

8A, 600V N-CHANNEL POWER MOSFET

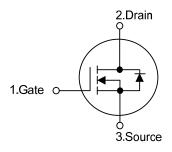
■ DESCRIPTION

The UTC **8N60K-MTQ** is a high voltage and high current power MOSFET, designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient AC to DC converters and bridge circuits.

■ FEATURES

- * $R_{DS(ON)} \le 1.4\Omega$ @ V_{GS} =10V, I_D =4.0A
- * Fast switching capability
- * Avalanche energy specified
- * Improved dv/dt capability, high ruggedness

■ SYMBOL

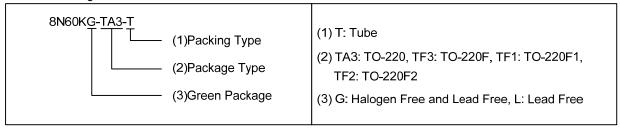


TO-220 TO-220F TO-220F TO-220F1 TO-220F2

■ ORDERING INFORMATION

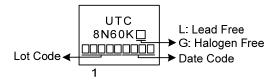
Ordering Number		Dookogo	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
8N60KL-TA3-T	8N60KG-TA3-T	TO-220	G	D	S	Tube	
8N60KL-TF1-T	8N60KG-TF1-T	TO-220F1	G	D	S	Tube	
8N60KL-TF2-T	8N60KG-TF2-T	TO-220F2	G	D	S	Tube	
8N60KL-TF3-T	8N60KG-TF3-T	TO-220F	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source



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■ MARKING



8N60K-MTQ Power MOSFET

■ **ABSOLUTE MAXIMUM RATINGS** (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V _{DSS}	600	V
Gate-Source Voltage		V_{GSS}	±30	V
Drain Current	Continuous	I _D	8	Α
	Pulsed (Note 2)	I _{DM}	32	Α
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	306	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Power Dissipation	TO-220		147	W
	TO-220F/TO-220F1 TO-220F2	P _D	48	W
lunction Temperature		TJ	+150	°C
Operating Temperature		T _{OPR}	-55 ~ +150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. L = 20mH, I_{AS} = 5.6A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25°C
- 4. $I_{SD} \le 7.5 A$, di/dt $\le 200 A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25 ^{\circ}C$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient		θ_{JA}	62.5	°C/W
	TO-220		0.85	°C/W
Junction to Case	TO-220F/TO-220F1 TO-220F2	θ _{JC}	2.6	°C/W

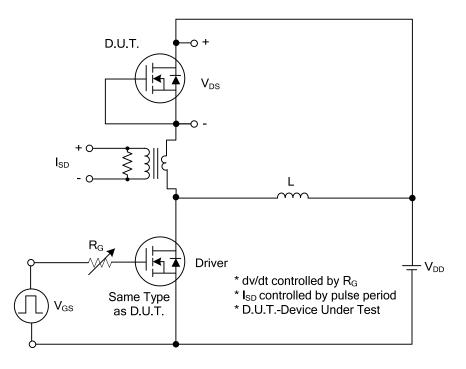
■ **ELECTRICAL CHARACTERISTICS** (T_C =25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} = 0 V, I _D = 250 μA	600			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} = 600 V, V _{GS} = 0V			10	μΑ
Gate-Source Leakage Current	Forward	1000	$V_{GS} = 30 \text{ V}, V_{DS} = 0 \text{V}$			100	nA
	Reverse		$V_{GS} = -30 \text{ V}, V_{DS} = 0 \text{ V}$			-100	nA
Breakdown Voltage Temperature Coefficient		$\triangle BV_{DSS}/\triangle T_J$	I _D =250μA, Referenced to 25°C		0.7		V/°C
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	$V_{GS} = 10V, I_D = 4A$			1.4	Ω
DYNAMIC CHARACTERISTICS		_					
Input Capacitance	ut Capacitance				540		pF
Output Capacitance		C _{ISS}	V _{DS} = 25V, V _{GS} = 0V, f = 1MHz		95		pF
Reverse Transfer Capacitance		C_{RSS}			10		pF
SWITCHING CHARACTERISTIC	S						
Total Gate Charge		Q_G	V _{DS} =50V, V _{GS} =10V, I _D =1.3A		22.8		nC
Gate-Source Charge		Q_GS	I _G =100µA (Note 1, 2)		6.6		nC
Gate-Drain Charge		Q_GD	IG-100μΑ (Note 1, 2)		5.7		nC
Turn-On Delay Time		$t_{D(ON)}$			56		ns
Turn-On Rise Time		t_R	$V_{DD} = 30V, V_{GS} = 10V, I_D = 0.5A,$		65		ns
Turn-Off Delay Time		$t_{D(OFF)}$	R _G =25Ω (Note 1, 2)		118.5		ns
Turn-Off Fall Time		t_{F}			62		ns
DRAIN-SOURCE DIODE CHARA	CTERISTIC	CS AND MAXII	MUM RATINGS				
Maximum Continuous Drain-Source	aximum Continuous Drain-Source Diode					8	Α
Forward Current		I _S				0	A
Maximum Pulsed Drain-Source Diode		I _{SM}				32	Α
Forward Current						32	^
Drain-Source Diode Forward Voltage		V_{SD}	$V_{GS} = 0V$, $I_S = 8A$			1.4	V

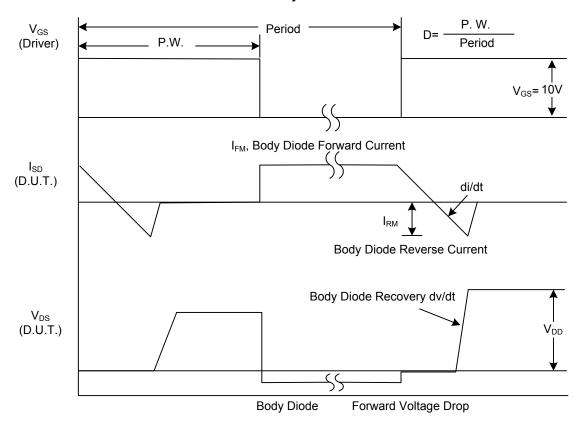
Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%.

^{2.} Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

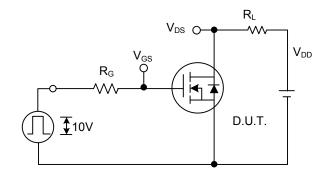


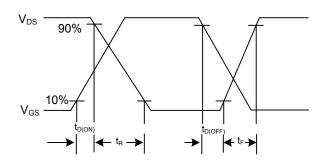
Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

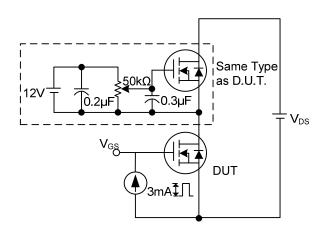
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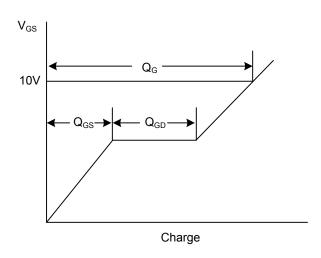




Switching Test Circuit

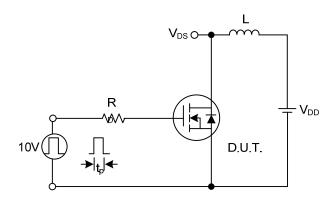
Switching Waveforms

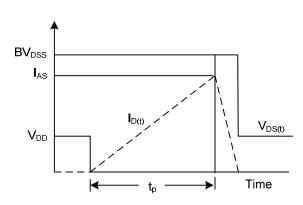




Gate Charge Test Circuit

Gate Charge Waveform





Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

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