

**3N60K-MK**

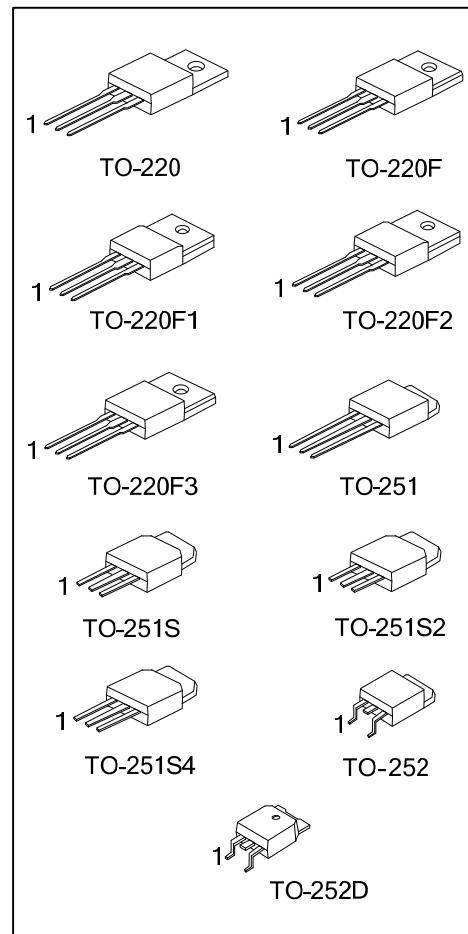
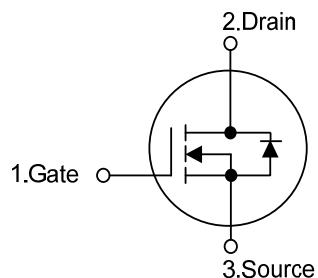
Preliminary

*Power MOSFET***3A, 600V N-CHANNEL  
POWER MOSFET****■ DESCRIPTION**

The UTC **3N60K-MK** is a high voltage and high current power MOSFET, designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

**■ FEATURES**

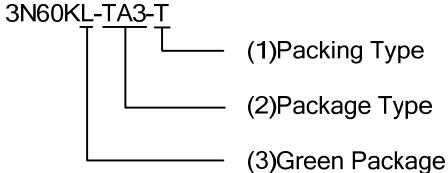
- \*  $R_{DS(ON)} < 3.6\Omega$  @  $V_{GS} = 10$  V,  $I_D = 1.5$  A
- \* Fast switching capability
- \* Avalanche energy specified
- \* Improved dv/dt capability, high ruggedness

**■ SYMBOL**

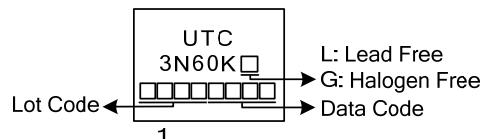
### ■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
3N60KL-TA3-T	3N60KG-TA3-T	TO-220	G	D	S	Tube
3N60KL-TF3-T	3N60KG-TF3-T	TO-220F	G	D	S	Tube
3N60KL-TF1-T	3N60KG-TF1-T	TO-220F1	G	D	S	Tube
3N60KL-TF2-T	3N60KG-TF2-T	TO-220F2	G	D	S	Tube
3N60KL-TF3-T	3N60KG-TF3-T	TO-220F3	G	D	S	Tube
3N60KL-TM3-T	3N60KG-TM3-T	TO-251	G	D	S	Tube
3N60KL-TMS-T	3N60KG-TMS-T	TO-251S	G	D	S	Tube
3N60KL-TMS2-T	3N60KG-TMS2-T	TO-251S2	G	D	S	Tube
3N60KL-TMS4-T	3N60KG-TMS4-T	TO-251S4	G	D	S	Tube
3N60KL-TN3-R	3N60KG-TN3-R	TO-252	G	D	S	Tape Reel
3N60KL-TND-R	3N60KG-TND-R	TO-252D	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

 (1)Packing Type (2)Package Type (3)Green Package	(1) T: Tube, R: Tape Reel (2) TA3: TO-220, TF3: TO-220F, TF1: TO-220F1, TF2: TO-220F2, TF3: TO-220F3, TM3: TO-251 TMS: TO-251S, TMS2: TO-251S2, TMS4: TO-251S4, TN3: TO-252, TND: TO-252D (3) L: Lead Free, G: Halogen Free and Lead Free
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### ■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ( $T_c = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	600	V
Gate-Source Voltage		$V_{GSS}$	$\pm 30$	V
Avalanche Current (Note 2)		$I_{AR}$	3.0	A
Continuous Drain Current		$I_D$	3.0	A
Pulsed Drain Current (Note 2)		$I_{DM}$	12	A
Avalanche Energy	Single Pulsed (Note 3)	$E_{AS}$	150	mJ
	Repetitive (Note 2)	$E_{AR}$	7.5	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Power Dissipation	TO-220	$P_D$	75	W
	TO-220F/TO-220F1		34	W
	TO-220F3		35	W
	TO-220F2		50	W
	TO-251/TO-251S		0.6	W/ $^\circ\text{C}$
	TO-251S2/TO-251S4		0.27	W/ $^\circ\text{C}$
Derate above 25°C	TO-252/TO-252D	$P_D$	0.28	W/ $^\circ\text{C}$
	TO-220		0.4	W/ $^\circ\text{C}$
	TO-220F/TO-220F1		35	W
	TO-220F3		0.28	W/ $^\circ\text{C}$
	TO-220F2		+150	$^\circ\text{C}$
	TO-251/TO-251S		-55 ~ +150	$^\circ\text{C}$
Power Dissipation	TO-251S2/TO-251S4	$P_D$	-55 ~ +150	$^\circ\text{C}$
	TO-252/TO-252D		Storage Temperature	$^\circ\text{C}$
Junction Temperature	$T_J$			
Operating Temperature	$T_{OPR}$			
Storage Temperature	$T_{STG}$			

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by  $T_J$ .

3.  $L=33\text{mH}$ ,  $I_{AS}=3\text{A}$ ,  $V_{DD}=50\text{V}$ ,  $R_G=25\ \Omega$ , Starting  $T_J = 25^\circ\text{C}$

4.  $I_{SD} \leq 3.0\text{A}$ ,  $di/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	TO-220/TO-220F TO-220F1/TO-220F2 TO-220F3	$\theta_{JA}$	62.5	$^\circ\text{C/W}$
	TO-251/TO-251S TO-251S2/TO-251S4 TO-252/TO-252D		110	
Junction to Case	TO-220	$\theta_{JC}$	1.67	$^\circ\text{C/W}$
	TO-220F/TO-220F1		3.68	
	TO-220F3		3.58	
	TO-220F2		2.5	
	TO-251/TO-251S TO-251S2/TO-251S4 TO-252/TO-252D			

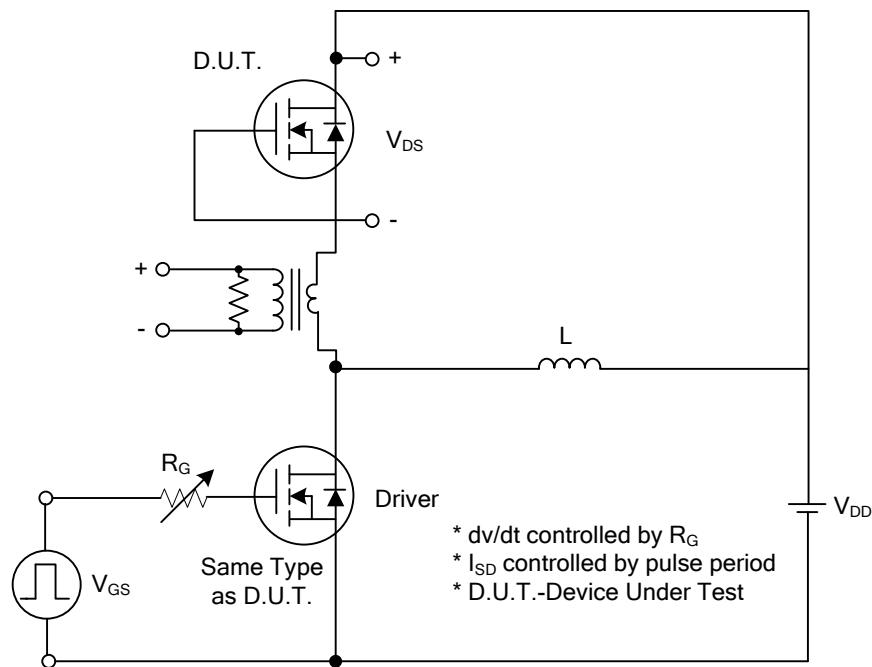
■ ELECTRICAL CHARACTERISTICS ( $T_c = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}} = 0 \text{ V}, I_{\text{D}} = 250 \mu\text{A}$	600			V
Drain-Source Leakage Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 600 \text{ V}, V_{\text{GS}} = 0 \text{ V}$		10		$\mu\text{A}$
Gate-Source Leakage Current	Forward	$V_{\text{GS}} = 30 \text{ V}, V_{\text{DS}} = 0 \text{ V}$		100		nA
	Reverse	$V_{\text{GS}} = -30 \text{ V}, V_{\text{DS}} = 0 \text{ V}$		-100		nA
Breakdown Voltage Temperature Coefficient	$\triangle \text{BV}_{\text{DSS}}/\triangle T_J$	$I_{\text{D}} = 250 \mu\text{A},$ Referenced to $25^\circ\text{C}$		0.6		$\text{V}/^\circ\text{C}$
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{\text{GS(TH)}}$	$V_{\text{DS}} = V_{\text{GS}}, I_{\text{D}} = 250 \mu\text{A}$	2.5		4.5	V
Static Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}} = 10 \text{ V}, I_{\text{D}} = 1.5 \text{ A}$			3.6	$\Omega$
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	$C_{\text{ISS}}$	$V_{\text{DS}} = 25 \text{ V}, V_{\text{GS}} = 0 \text{ V},$ $f = 1 \text{ MHz}$		460	560	pF
Output Capacitance	$C_{\text{OSS}}$			41	60	pF
Reverse Transfer Capacitance	$C_{\text{RSS}}$			5.88	12	pF
<b>SWITCHING CHARACTERISTICS</b>						
Turn-On Delay Time	$t_{\text{D(ON)}}$	$V_{\text{DD}} = 30 \text{ V}, I_{\text{D}} = 0.5 \text{ A}, R_{\text{G}} = 25 \Omega$ (Note 1, 2)		43		ns
Turn-On Rise Time	$t_{\text{R}}$			16		ns
Turn-Off Delay Time	$t_{\text{D(OFF)}}$			96		ns
Turn-Off Fall Time	$t_{\text{F}}$			17		ns
Total Gate Charge	$Q_{\text{G}}$	$V_{\text{DS}} = 50 \text{ V}, I_{\text{D}} = 1.3 \text{ A}, V_{\text{GS}} = 10 \text{ V}$ (Note 1, 2)		14	18	nC
Gate-Source Charge	$Q_{\text{GS}}$			4.4		nC
Gate-Drain Charge	$Q_{\text{GD}}$			1.4		nC
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Drain-Source Diode Forward Voltage	$V_{\text{SD}}$	$V_{\text{GS}} = 0 \text{ V}, I_{\text{S}} = 3.0 \text{ A}$			1.4	V
Maximum Continuous Drain-Source Diode Forward Current	$I_{\text{S}}$				3.0	A
Maximum Pulsed Drain-Source Diode Forward Current	$I_{\text{SM}}$				12	A

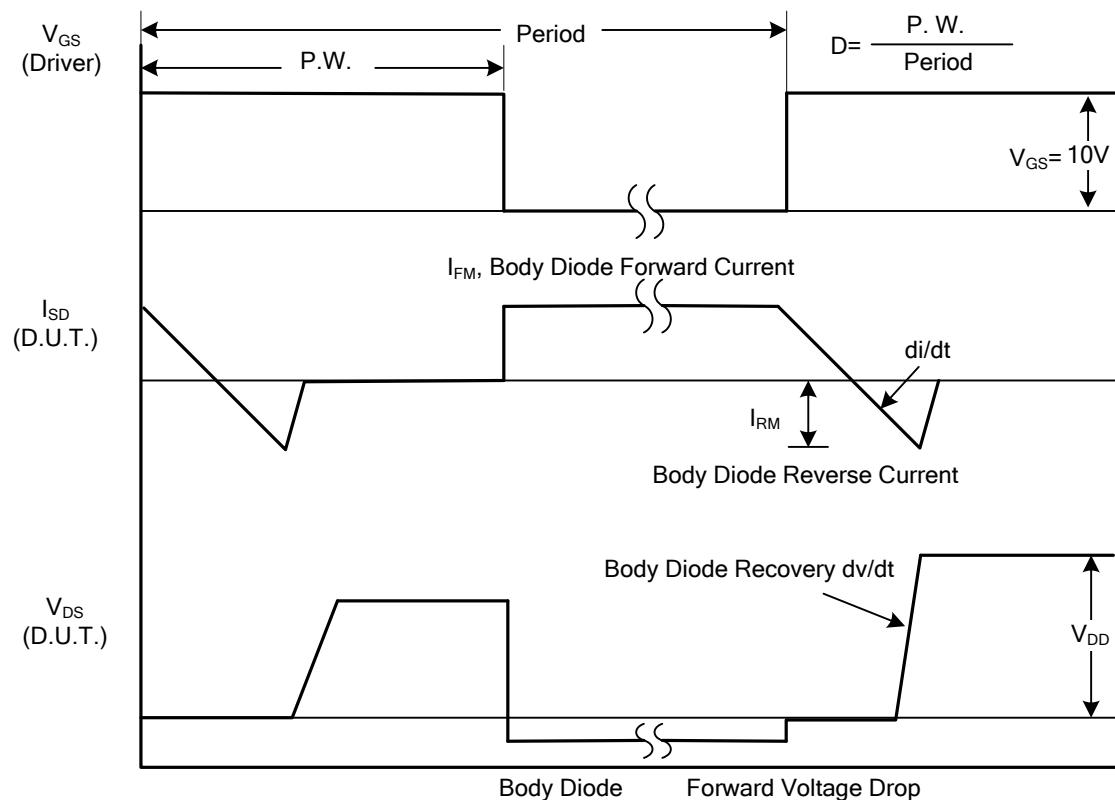
Notes: 1. Pulse Test: Pulse width  $\leq 300 \mu\text{s}$ , Duty cycle  $\leq 2\%$ 

2. Essentially independent of operating temperature

## ■ TEST CIRCUITS AND WAVEFORMS

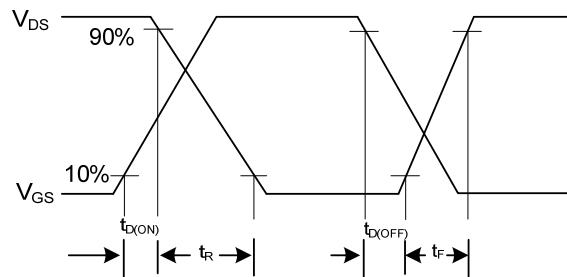
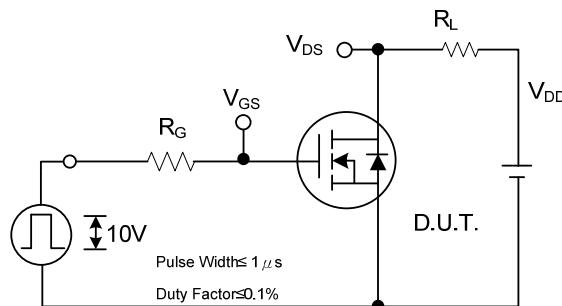


Peak Diode Recovery dv/dt Test Circuit



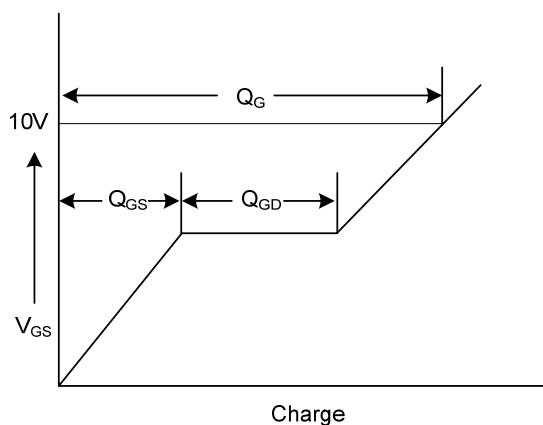
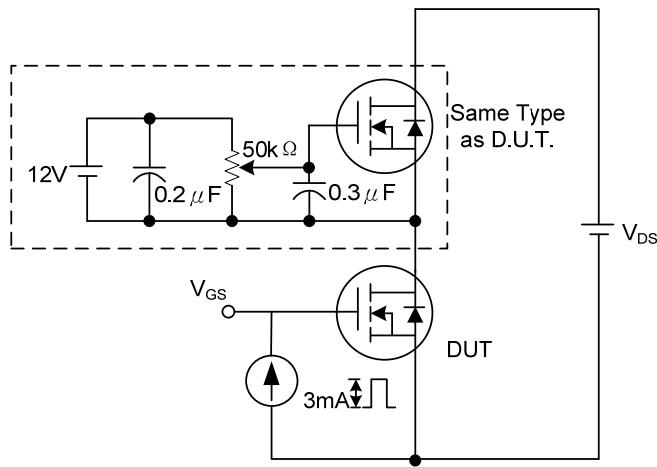
Peak Diode Recovery dv/dt Waveforms

■ TEST CIRCUITS AND WAVEFORMS (Cont.)



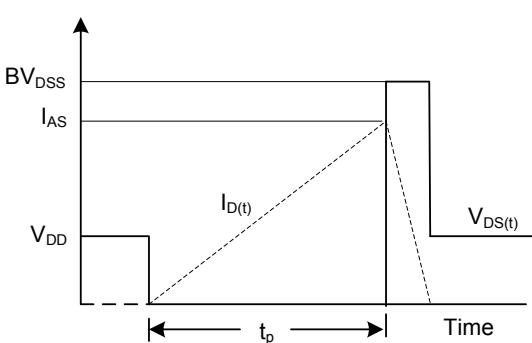
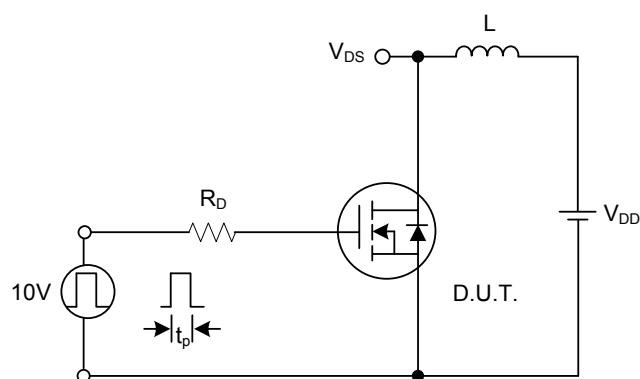
Switching Test Circuit

Switching Waveforms



Gate Charge Test Circuit

Gate Charge Waveform



Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

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