

## UNISONIC TECHNOLOGIES CO., LTD

11N50K-MT

## **Preliminary**

## **Power MOSFET**

# 11A, 500V N-CHANNEL POWER MOSFET

#### **■** DESCRIPTION

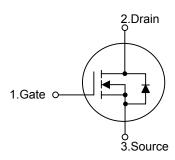
The **UTC 11N50K-MT** is a N-channel enhancement mode power MOSFET. It uses UTC advanced planar stripe, DMOS technology to provide customers perfect switching performance, minimal on-state resistance. It also can withstand high energy pulse in the avalanche and commutation mode.

The **UTC 11N50K-MT** is universally applied in electronic lamp ballasts based on half bridge topology, high efficiency switched mode power supplies, active power factor correction, etc.

#### **■ FEATURES**

- \*  $R_{DS(ON)}$  < 0.55 $\Omega$  @  $V_{GS}$  = 10 V,  $I_{D}$  = 5.5 A
- \* Fast Switching
- \* With 100% Avalanche Tested

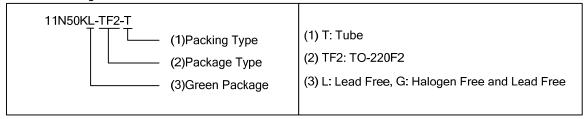
#### ■ SYMBOL



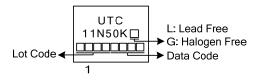
## **■ ORDERING INFORMATION**

Ordering Number		Daalaaaa	Pin Assignment			Daaldaa	
Lead Free	Halogen Free	Package	1	2	3	Packing	
11N50KL-TF2-T	11N50KG-TF2-T	TO-220F2	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source



## **■** MARKING



1 TO-220F2

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## ■ ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub> = 25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT	
Drain to Source Voltage	$V_{DSS}$	500	V	
Gate to Source Voltage	$V_{GSS}$	±30	V	
Continuous Prain Current	- I <sub>D</sub>	11 (Note 2)	Α	
Continuous Drain Current $T_c=100^{\circ}C$		7 (Note 2)	Α	
Pulsed Drain Current (Note 3)	I <sub>DM</sub>	44 (Note 2)	Α	
Single Pulsed Avalanche Energy(Note 4)	E <sub>AS</sub>	500	mJ	
Peak Diode Recovery dv/dt (Note 5)	dv/dt	4.5	V/ns	
Power Dissipation	P <sub>D</sub>	50	W	
Junction Temperature	TJ	+150	°C	
Storage Temperature	T <sub>STG</sub>	-55 ~ +150	°C	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Drain current limited by maximum junction temperature
- 3. Repetitive Rating: Pulse width limited by maximum junction temperature
- 4. L=8.26mH,  $I_{AS}$ =11A,  $V_{DD}$ = 50V,  $R_{G}$ =25 $\Omega$ , Starting  $T_{J}$ =25 $^{\circ}$ C
- 5.  $I_{SD} \le 11A$ , di/dt  $\le 200A/\mu s$ ,  $V_{DD} \le BV_{DSS}$ , Starting  $T_J = 25$ °C

#### ■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT
Junction to Ambient	$\theta_{JA}$	62.5	°C/W
Junction to Case	$\theta_{JC}$	2.5	°C/W

#### ■ ELECTRICAL CHARACTERISTICS (T<sub>C</sub>=25°C, unless otherwise specified)

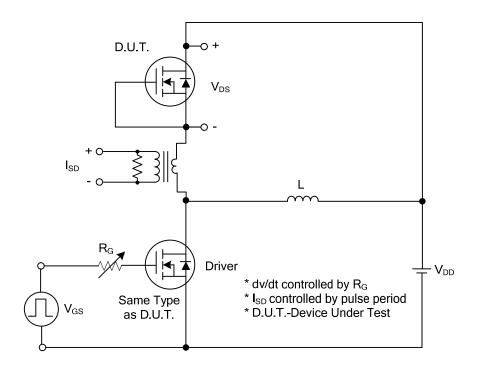
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	500			V	
Breakdown Voltage Temperature Coefficient	$\Delta BV_{DSS}/\Delta T_{J}$	I <sub>D</sub> =250μA,Referenced to 25°C		0.5		V/°C	
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =500V, V <sub>GS</sub> =0V			10	μΑ	
Dialii-Source Leakage Current		V <sub>DS</sub> =500V, T <sub>J</sub> =125°C			100	μΑ	
Gate-Source Leakage Current	I <sub>GSS</sub>	$V_{DS}$ =0V , $V_{GS}$ =±30V			±100	nA	
ON CHARACTERISTICS							
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	2.0		4.0	V	
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =5.5A		0.43	0.55	Ω	
DYNAMIC PARAMETERS							
Input Capacitance	C <sub>ISS</sub>			1100	1500	pF	
Output Capacitance	Coss	$V_{DS}$ =25V, $V_{GS}$ =0V,f=1.0MHz		150	200	pF	
Reverse Transfer Capacitance	C <sub>RSS</sub>			9	20	pF	
SWITCHING PARAMETERS							
Total Gate Charge	$Q_{G}$	V <sub>DS</sub> =50V, V <sub>GS</sub> =10V, I <sub>D</sub> =1.3A		33	45	nC	
Gate-Source Charge	$Q_{GS}$	(Note 1, 2)		9		nC	
Gate-Drain Charge	$Q_{GD}$	(NOIE 1, 2)		9		nC	
Turn-ON Delay Time	t <sub>D(ON)</sub>			65	80	ns	
Turn-ON Rise Time	t <sub>R</sub>	$V_{DD}$ =30V, $I_{D}$ =0.5A, $R_{G}$ =25 $\Omega$		100	150	ns	
Turn-OFF Delay Time	t <sub>D(OFF)</sub>	(Note 1, 2)		160	250	ns	
Turn-OFF Fall Time	$t_{F}$			100	160	ns	
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current	Is				11	Α	
Maximum Body-Diode Pulsed Current	I <sub>SM</sub>				44	Α	
Drain-Source Diode Forward Voltage	$V_{SD}$	I <sub>S</sub> =11A, V <sub>GS</sub> =0V			1.4	V	

Notes: 1. Pulse Test : Pulse width ≤ 300µs, Duty cycle ≤ 2%.

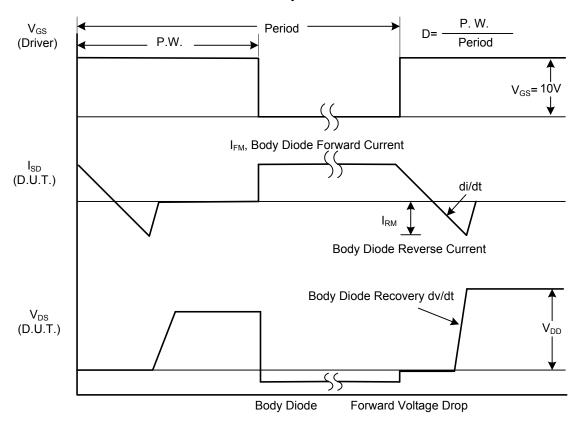
2. Essentially independent of operating temperature.



## ■ TEST CIRCUITS AND WAVEFORMS

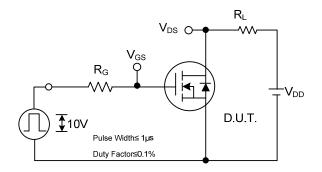


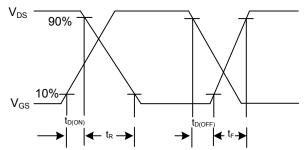
## Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

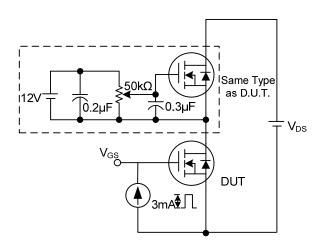
## ■ TEST CIRCUITS AND WAVEFORMS (Cont.)

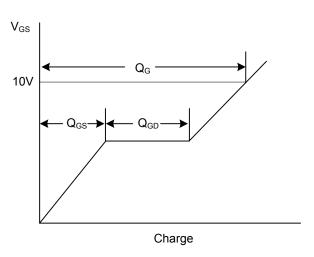




**Switching Test Circuit** 

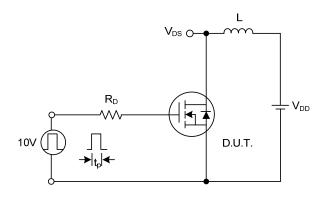
**Switching Waveforms** 

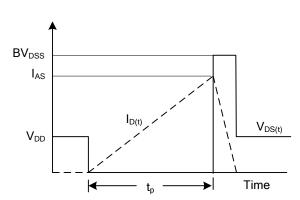




**Gate Charge Test Circuit** 

**Gate Charge Waveform** 





**Unclamped Inductive Switching Test Circuit** 

**Unclamped Inductive Switching Waveforms** 

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