

UTC UNISONIC TECHNOLOGIES CO., LTD

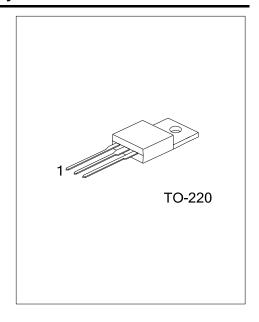
BTB310A Preliminary TRIAC

10A TRIACS

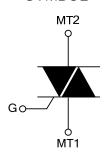
DESCRIPTION

The UTC BTB310A is a 10A triacs which can be operated in 3 quadrants, it uses UTC's advanced technology to provide customers with high commutation performances.

The UTC BTB310A is suitable for inductive load switching operations, also can be used in ON/OFF function applications such as induction motor starting circuits, heating regulation, static relays etc.



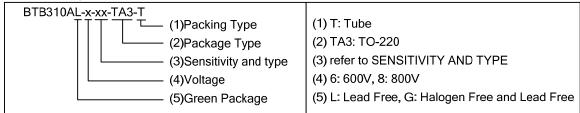
SYMBOL



ORDERING INFORMATION

Ordering Number		Packago	Pin <i>i</i>	Assignr	Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing
BTB310AL-x-xx-TA3-T	BTB310AG-x-xx-TA3-T	TO-220	MT1	MT2	G	Tube

Note: Pin Assignment: MT1: MT1 MT2: MT2 G: Gate

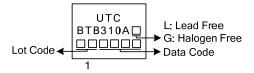


SENSITIVITY AND TYPE

	VOL	ΓAGE	SENSITIVITY	TYPE		
PART NUMBER	600V	800V	SENSITIVITY	ITPE		
BW	0	0	50mA	SNUBBERLESS		
CW	0	0	35mA	SNUBBERLESS		

O: Available

MARKING



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■ ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATINGS	UNIT		
RMS On-State Current (Full Sine Wave)	T _C =95°C		I _{T(RMS)}	10	Α
Non Repetitive Surge Peak On-State	F=50Hz	t=20ms	I _{TSM}	100	Α
Current (Full Cycle T _J initial=25°C)	F=60Hz	t=16.7ms	-13W	105	Α
I ² t Value for Fusing	t _P =10ms		I ² t	55	A^2s
Critical Rate of Rise of On-State Current: I _G =2xI _{GT} , tr≤100ns	F=120Hz	T _J =125°C	dI/dt	50	A/μs
Non Repetitive Surge Peak Off-State Voltage	t _P =10ms T _J =25°C		V _{DSM} /V _{RSM}	V _{DSM} /V _{RSM} +100	V
Peak Gate Current	t _P =20µs ⁻		I_{GM}	4	Α
Average Gate Power Dissipation T _J =125°C			$P_{G(AV)}$	1	W
Operating Junction Temperature			T_J	-40~+125	°C
Storage Junction Temperature	T_{STG}	-40~+150	ů		

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL RESISTANCES

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	60	°C/W
Junction to Case (AC)	θ_{JC}	1.5	°C/W

■ ELECTRICAL CHARACTERISTICS (T_J= 25°C, unless otherwise specified)

FOR SNUBBERLESS (3 QUADRANTS)

TON GROBBENEESS (5 QC	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	TEST CONDITIONS		CW			BW			
PARAMETER	SYMBOL			MIN	TYP	MAX	MIN	TYP	MAX	UNIT
Gate Trigger Current (Note 1)	I _{GT}	V _D =12V,	1-11-111			35			50	mA
Gate Trigger Voltage	V _{GT}	R _L =33Ω	1-11-111			1.3			1.3	V
Gate Non-Trigger Voltage	V_{GD}	$V_D=V_{DRM}$, $R_L=3.3k\Omega$, $T_J=125^{\circ}C$	1-11-111	0.2			0.2			V
Holding Current (Note 2)	l _Η	I _T =500mA				35			50	mA
Latching Current	IL	I _G =1.2I _{GT} I-III 50 60	I-III			50			70	mA
	ı.			80	mA					
Critical Rate of Rise of Off-State Voltage (Note 2)	dV/dt	V _D =67%V _{DRM} , Gate Open, T _J =125°C		500			1000			V/µs
Critical Rate of Rise of Off-State Voltage at Commutation (Note 2)	(dl/dt)c	Without Snubber, T _J =125°C		5.5			9.0			A/ms

Note: 1. Minimum I_{GT} is guaranteed at 5% of I_{GT} max.

2. For both polarities of MT2 referenced to MT1.

■ STATIC CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Peak On-State Voltage (Note 2)	V_{T}	I _{TM} =14A, t _P =380μs	T _J =25°C			1.55	V
Threshold Voltage (Note 2)	V_{TO}		T _J =125°C			0.85	V
Dynamic Resistance (Note 2)	R_D		T _J =125°C			40	mΩ
Repetitive Peak Off-State Current	I _{DRM}	\/ -\/	T _J =25°C			5	μΑ
	I _{RRM}	$V_{DRM}=V_{RRM}$	T _J =125°C			1	mA

Notes: 1. Minimum I_{GT} is guaranteed at 5% of I_{GT} max.

2. For both polarities of MT2 referenced to MT1.

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