MGBR12L45

TO-277

QW-R601-189.B

# MOS GATED BARRIER RECTIFIER

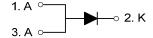
#### **■** DESCRIPTION

The UTC MGBR12L45 is a surface mount mos gated barrier rectifier, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed, etc.

#### ■ FEATURES

- \* Low forward voltage drop
- \* High switching speed

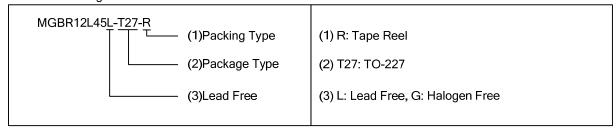
#### ■ SYMBOL



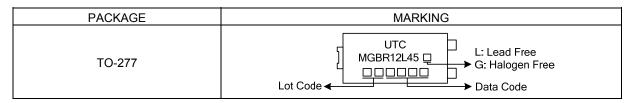
## **■ ORDERING INFORMATION**

Ordering Number		Deslesses	Pin Assignment			Dankina	
Lead Free	Halogen Free	Package	1	2	3	Packing	
MGBR12L45L-T27-R	MGBR12L45G-T27-R	TO-277	Α	K	Α	Tape Reel	

Note: Pin Assignment: A: Anode K: Common Cathode



#### MARKING INFORMATION



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# ■ **ABSOLUTE MAXIMUM RATINGS**(T<sub>A</sub>=25°C, unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

PARAMETER	SYMBOL	RATINGS	UNIT
DC Blocking Voltage	$V_{RM}$	45	V
WorkingPeak Reverse Voltage	$V_{RWM}$	45	V
Peak Repetitive Reverse Voltage	$V_{RRM}$	45	V
Average Rectified Output Current T <sub>C</sub> =140°C	Ιο	12	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	180	Α
Operating Junction Temperature	$T_J$	-65~+150	°C
Storage Temperature	$T_{STG}$	-65~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

# ■ THERMAL CHARACTERISTICS (Note 3)

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	$\theta_{JA}$	73	°C/W
Junction to Case	θ <sub>JC</sub>	13	°C/W

## ■ **ELECTRICAL CHARACTERISTICS**(T<sub>A</sub>=25°C,unless otherwise specified.)

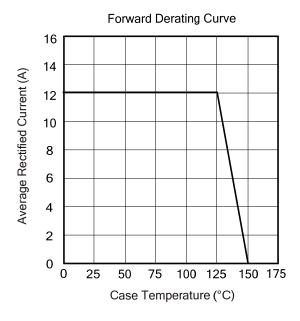
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	$V_{(BR)R}$	I <sub>R</sub> =0.5mA	45			V
Forward Voltage Drop	V <sub>EM</sub>	I <sub>F</sub> =12A, T <sub>J</sub> =25°C			0.60	V
		I <sub>F</sub> =12A, T <sub>J</sub> =125°C			0.55	V
Lookage Compant (Note 4)	I DM	V <sub>R</sub> =45V, T <sub>J</sub> =25°C		50	300	μΑ
Leakage Current (Note 1)		V <sub>R</sub> =45V, T <sub>J</sub> =125°C		17	75	mΑ

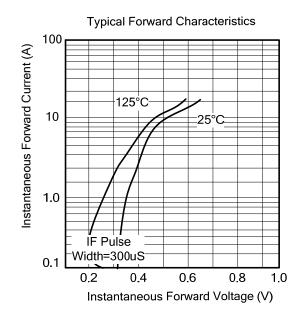
Notes: 1. Short duration pulse test used to minimize self-heating effect.

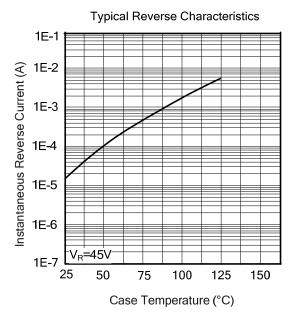
- 2. Thermal resistance junction to case mounted on heatsink.
- 3. Mounted on an FR4 PCB, single-sided copper, with 100cm<sup>2</sup> copper pad area.

MGBR12L45

## ■ TYPICAL CHARACTERISTICS







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