

UNISONIC TECHNOLOGIES CO., LTD

MGBR10S50C

Preliminary

DIODE

DUAL MOS GATED BARRIER RECTIFIER

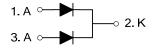
DESCRIPTION

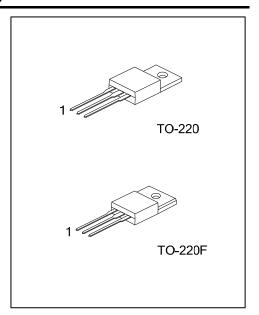
The UTC **MGBR10S50C** is a dual mos gated barrier rectifiers, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed, etc.

■ FEATURES

- * Super low forward voltage drop
- * High switching speed

■ SYMBOL

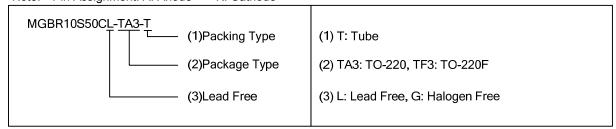




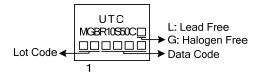
■ ORDERING INFORMATION

Ordering Number		Doolsono	Pin Assignment			Daakina	
Lead Free	Halogen Free	Package	1	2	3	Packing	
MGBR10S50CL-TA3-T	MGBR10S50CG-TA3-T	TO-220	Α	K	Α	Tube	
MGBR10S50CL-TF3-T	MGBR10S50CG-TF3-T	TO-220F	Α	K	Α	Tube	

Note: Pin Assignment: A: Anode K: Cathode



■ MARKING



www.unisonic.com.tw 1 of 3

■ ABSOLUTE MAXIMUM RATINGS (PER LEG) (T_A=25°C unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

PARAMETER		SYMBOL	RATINGS	UNIT
DC Blocking Voltage		V_{RM}	50	V
Working Peak Reverse Voltage		V_{RWM}	50	V
Peak Repetitive Reverse Voltage		V_{RRM}	50	V
Average Rectified Forward Current	Per Leg	l _o	5	Α
(Rated VR-20KHz Square Wave) – 50% duty cycle	Total		10	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load		I _{FSM}	150	Α
Peak Repetitive Reverse Surge Current (2µS-1kHz)		I _{RRM}	2	Α
Operating Junction Temperature		T_J	-65~+150	°C
Storage Temperature		T _{STG}	-65~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT	
Junction to Ambient		θ_{JA}	62.5	°C/W	
lunction to Coop	TO-220	0	2	°C/M	
Junction to Case	TO-220F	$\theta_{ m JC}$	3.31	°C/W	

■ **ELECTRICAL CHARACTERISTICS** (T_A =25°C, unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	$V_{(BR)R}$	I _R =0.50mA	50			V
Forward Voltage Drop	V_{FM}	I _F =5A, T _J =25°C			0.46	V
		I _F =5A, T _J =125°C			0.41	V
Lockers Comment (Note 1)	I IpM	V _R =50V, T _J =25°C		50	500	μΑ
Leakage Current (Note 1)		V _R =50V, T _J =125°C		15	50	mA

Notes: 1. Short duration pulse test used to minimize self-heating effect.

2. Thermal resistance junction to case mounted on heatsink.

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