

UNISONIC TECHNOLOGIES CO., LTD

MGBR10L45C

Preliminary

DIODE

DUAL MOS GATED BARRIER RECTIFIER

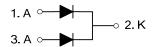
■ DESCRIPTION

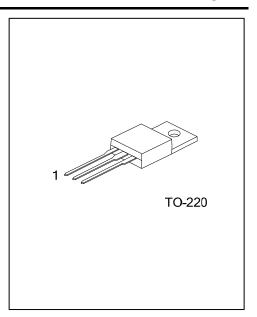
The UTC MGBR10L45C is a dual mos gated barrier rectifiers, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed, etc.

■ FEATURES

- * Low forward voltage drop
- * High switching speed

■ SYMBOL

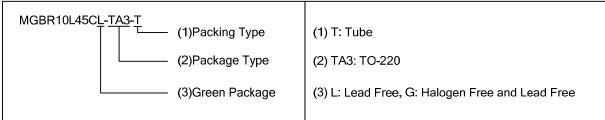




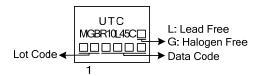
■ ORDERING INFORMATION

Ordering Number		Daakana	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
MGBR10L45CL-TA3-T	MGBR10L45CG-TA3-T	TO-220	Α	K	Α	Tube	

Note: Pin Assignment: A: Anode K: Cathode



■ MARKING



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■ **ABSOLUTE MAXIMUM RATINGS** (T_A=25°C, unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

PARAMETER		SYMBOL	RATINGS	UNIT	
DC Blocking Voltage		V_{RM}	45	V	
Working Peak Reverse Voltage		V_{RWM}	45	V	
Peak Repetitive Reverse Voltage		V_{RRM}	45	V	
RMS Reverse Voltage		$V_{R(RMS)}$	32	V	
Average Rectified Output Current	Per Leg		5	Α	
(T _C =140°C)	Total	I _O	10	Α	
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load		I _{FSM}	90	А	
Repetitive Peak Avalanche Power (1µs, 25°C)		P _{ARM}	5000	W	
Operating Junction Temperature		TJ	-65 ~ + 150	°C	
Storage Temperature		T _{STG}	-65 ~ +150	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	62.5	°C/W
Junction to Case	θις	2	°C/W

■ ELECTRICAL CHARACTERISTICS (Per Leg) (T_A =25°C, unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	$V_{(BR)R}$	I _R =0.45mA	45			V
Forward Voltage Drop	V _{FM}	I _F =5A, T _J =25°C			0.55	V
		I _F =5A, T _J =125°C			0.50	V
Leakage Current (Note 1)	PМ	V _R =45V, T _J =25°C		50	300	μΑ
		V _R =45V, T _J =125°C		12	30	mA

Notes: 1. Short duration pulse test used to minimize self-heating effect.

^{2.} Thermal resistance junction to case mounted on heatsink.

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