# UNISONIC TECHNOLOGIES CO., LTD

6N65-P Power MOSFET

# 6.2A, 650V N-CHANNEL **POWER MOSFET**

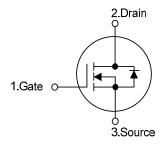
#### DESCRIPTION

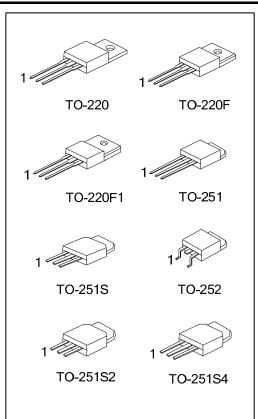
The UTC 6N65-P is a high voltage power MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications of switching power supplies and adaptors.

#### **FEATURES**

- \*  $R_{DS(ON)}$  < 2.00 @  $V_{GS}$  = 10V,  $I_D$  = 3.1A
- \* Fast switching capability
- \* Avalanche energy tested
- \* Improved dv/dt capability, high ruggedness

#### **SYMBOL**

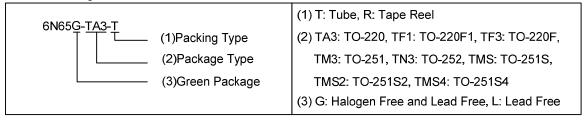




#### **ORDERING INFORMATION**

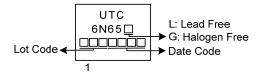
Ordering Number		Package	Pin Assignment			Packing	
Lead Free	Halogen Free	Fackage	1	2	3	Facking	
6N65L-TA3-T	6N65G-TA3-T	TO-220	G	D	S	Tube	
6N65L-TF1-T	6N65G-TF1-T	TO-220F1	G	D	S	Tube	
6N65L-TF3-T	6N65G-TF3-T	TO-220F	G	D	S	Tube	
6N65L-TM3-T	6N65G-TM3-T	TO-251	G	D	S	Tube	
6N65L-TMS-T	6N65G-TMS-T	TO-251S	G	D	S	Tube	
6N65L-TMS2-T	6N65G-TMS2-T	TO-251S2	G	D	S	Tube	
6N65L-TMS4-T	6N65G-TMS4-T	TO-251S4	G	D	S	Tube	
6N65L-TN3-R	6N65G-TN3-R	TO-252	G	D	S	Tape Reel	

Note: Pin Assignment: G: Gate D: Drain S: Source



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#### MARKING



#### ■ **ABSOLUTE MAXIMUM RATINGS** (T<sub>C</sub> = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	650	V
Gate-Source Voltage		$V_{GSS}$	±30	V
Avalanche Current (Note 2)		I <sub>AR</sub>	6.2	Α
Continuous Drain Current		$I_D$	6.2	Α
Pulsed Drain Current (Note 2)		$I_{DM}$	24.8	Α
Avalanche Energy	Single Pulsed (Note 3)	E <sub>AS</sub>	130	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Power Dissipation	TO-220	P <sub>D</sub>	125	W
	TO-220F/TO-220F1		40	W
	TO-251/TO-251S			
	TO-251S2/TO-251S4		55	W
	TO-252			
Junction Temperature		TJ	+150	°C
Operating Temperature		$T_OPR$	-55 ~ +150	°C
Storage Temperature		$T_{STG}$	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. L = 7.2mH,  $I_{AS}$  = 6A,  $V_{DD}$  = 50V,  $R_{G}$  = 25  $\Omega$ , Starting  $T_{J}$  = 25°C
- 4.  $I_{SD} \le 6.2A$ , di/dt  $\le 200A/\mu s$ ,  $V_{DD} \le BV_{DSS}$ , Starting  $T_J = 25^{\circ}C$

#### **■ THERMAL DATA**

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	TO-220/TO-220F TO-220F1		62.5	°C/W
	TO-251/TO-251S TO-251S2/TO-251S4 TO-252	θЈА	110	°C/W
Junction to Case	TO-220		1.0	°C/W
	TO-220F/TO-220F1		3.2	°C/W
	TO-251/TO-251S TO-251S2/TO-251S4 TO-252	θЈС	2.27	°C/W

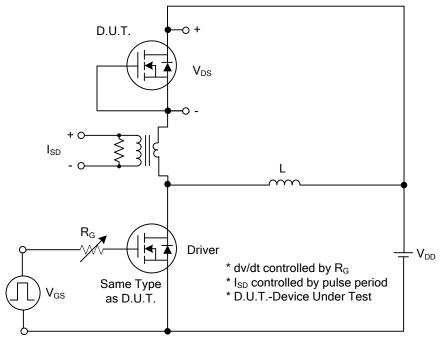
# ■ ELECTRICAL CHARACTERISTICS (T<sub>J</sub> =25°C, unless otherwise specified)

			•				
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNI T
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	650			V
Drain-Source Leakage Current		I <sub>DSS</sub>	$V_{DS} = 650V, V_{GS} = 0V$			10	μΑ
Gate- Source Leakage Current	Forward	I <sub>GSS</sub>	$V_{GS} = 30V, V_{DS} = 0V$			100	nA
	Reverse		$V_{GS} = -30V, V_{DS} = 0V$			-100	nA
Breakdown Voltage Temperature Coefficient		$\triangle BV_{DSS}/\triangle T_{J}$	I <sub>D</sub> =250μA, Referenced to 25°C		0.53		V/°C
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V
Static Drain-Source On-State Resistance		R <sub>DS(ON)</sub>	$V_{GS} = 10V, I_D = 3.1A$		1.6	2.0	Ω
DYNAMIC CHARACTERISTICS							
Input Capacitance		C <sub>ISS</sub>			920		pF
Output Capacitance		Coss	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1.0 MHz		80		pF
Reverse Transfer Capacitance		$C_{RSS}$			10		pF
SWITCHING CHARACTERISTIC	S						
Total Gate Charge		$Q_G$	\/ =200\/   =4A \/ =10\/		26		nC
Gate-Source Charge		$Q_GS$	V <sub>DS</sub> =200V, I <sub>D</sub> =4A, V <sub>GS</sub> =10V I <sub>G</sub> =3mA (Note 1, 2)		7		nC
Gate-Drain Charge		$Q_{GD}$	IG-SITIA (NOTE 1, 2)		8.4		nC
Turn-On Delay Time		$t_{D(ON)}$			40		ns
Turn-On Rise Time		$t_R$	$V_{DD}$ =30V, $I_{D}$ =0.5A, $V_{GS}$ =10V		40		ns
Turn-Off Delay Time Turn-Off Fall Time		t <sub>D(OFF)</sub>	$R_G = 25\Omega$ (Note 1, 2)		140		ns
		$t_{F}$			60		ns
DRAIN-SOURCE DIODE CHARA	CTERISTIC	CS AND MAXII	MUM RATINGS				
Maximum Continuous Drain-Source Diode Forward Current		Is				6.2	Α
						0.2	^
Maximum Pulsed Drain-Source Diode Forward Current		I <sub>SM</sub>				24.8	Α
						24.0	^
Drain-Source Diode Forward Voltage		$V_{SD}$	$V_{GS} = 0 \text{ V}, I_{S} = 6.2 \text{ A}$			1.4	V

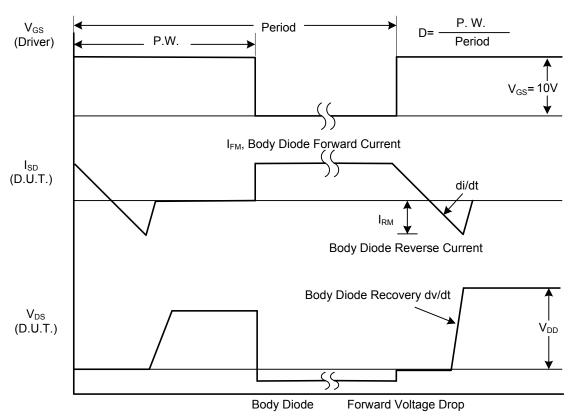
Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%

<sup>2.</sup> Essentially independent of operating temperature

# **■ TEST CIRCUITS AND WAVEFORMS**

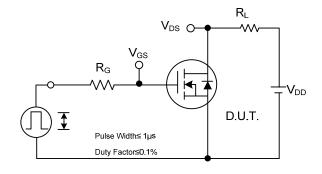


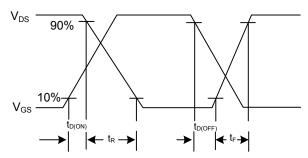
Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

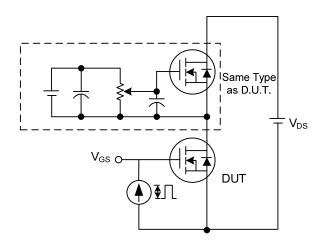
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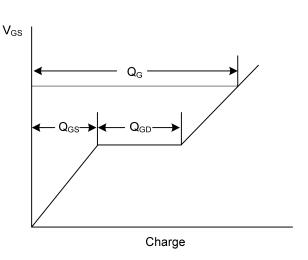




**Switching Test Circuit** 

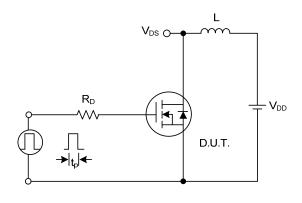
**Switching Waveforms** 

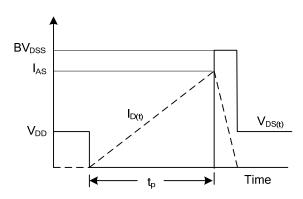




**Gate Charge Test Circuit** 

**Gate Charge Waveform** 

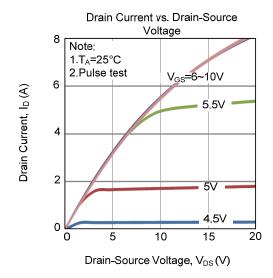


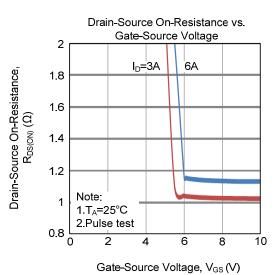


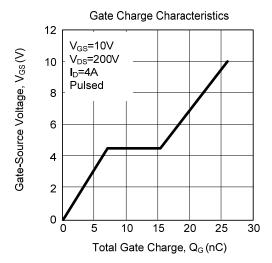
**Unclamped Inductive Switching Test Circuit** 

**Unclamped Inductive Switching Waveforms** 

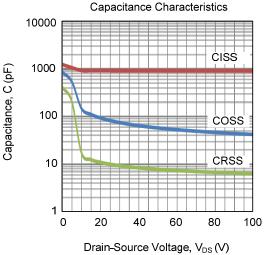
#### **■ TYPICAL CHARACTERISTICS**

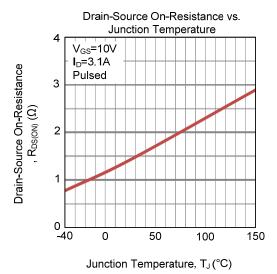


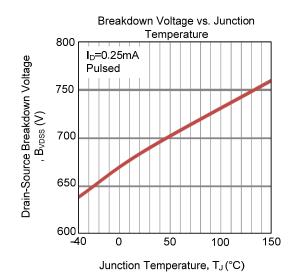




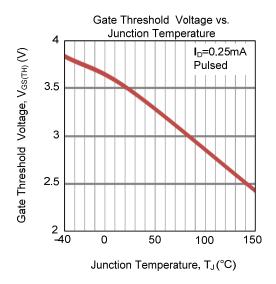


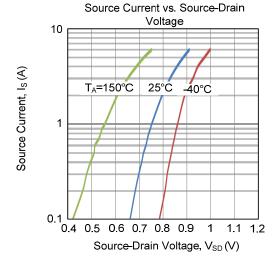


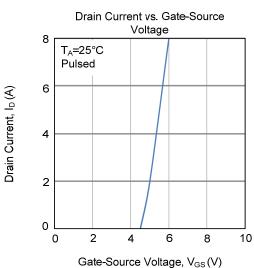


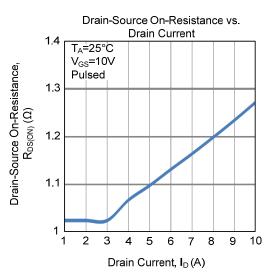


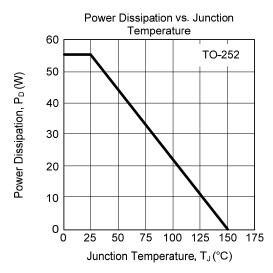
# **■ TYPICAL CHARACTERISTICS (Cont.)**

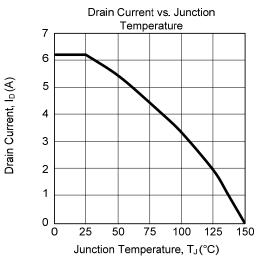




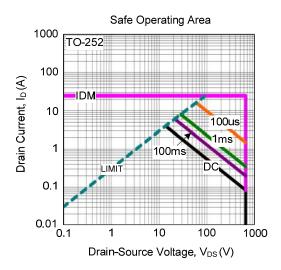








#### **■ TYPICAL CHARACTERISTICS (Cont.)**



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