

## UTT36N10

Power MOSFET

36A, 100V N-CHANNEL  
POWER MOSFET

## ■ DESCRIPTION

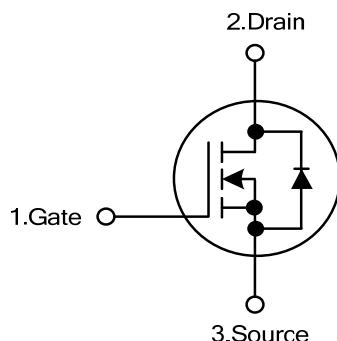
The UTC **UTT36N10** is a N-channel mode power MOSFET using UTC's advanced technology to provide customers with a minimum on-state resistance, low gate charge and high switching speed.

The UTC **UTT36N10** is suitable for high voltage synchronous rectifier and DC/DC converters, etc.

## ■ FEATURES

- \* High Switching Speed

## ■ SYMBOL



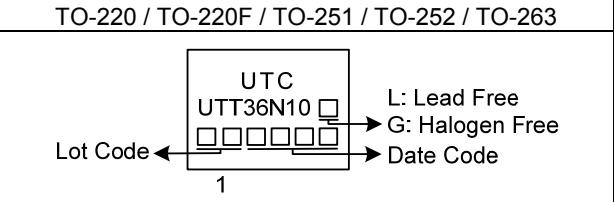
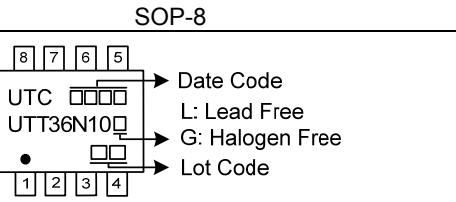
## ■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
UTT36N10L-TA3-T	UTT36N10G-TA3-T	TO-220	G	D	S	-	-	-	-	-	Tube
UTT36N10L-TF3-T	UTT36N10G-TF3-T	TO-220F	G	D	S						Tube
UTT36N10L-TM3-T	UTT36N10G-TM3-T	TO-251	G	D	S						Tube
UTT36N10L-TN3-R	UTT36N10G-TN3-R	TO-252	G	D	S	-	-	-	-	-	Tape Reel
UTT36N10L-TQ2-T	UTT36N10G-TQ2-T	TO-263	G	D	S	-	-	-	-	-	Tube
UTT36N10L-TQ2-R	UTT36N10G-TQ2-R	TO-263	G	D	S	-	-	-	-	-	Tape Reel
UTT36N10L-S08-R	UTT36N10G-S08-R	SOP-8	S	S	S	G	D	D	D	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

	(1)T: Tube, R: Tape Reel (2)TA3: TO-220, TF3: TO-220F, TM3: TO-251 TN3: TO-252, TQ2: TO-263, S08: SOP-8 (3)G: Halogen Free and Lead Free, L: Lead Free
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**■ MARKING**

TO-220 / TO-220F / TO-251 / TO-252 / TO-263	SOP-8
 <p>L: Lead Free G: Halogen Free</p> <p>Lot Code</p> <p>1</p>	 <p>Date Code L: Lead Free G: Halogen Free</p> <p>Lot Code</p>

■ ABSOLUTE MAXIMUM RATINGS ( $T_c=25^\circ\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	100	V
Gate-Source Voltage		$V_{GSS}$	$\pm 20$	V
Drain Current	Continuous ( $V_{GS}=10\text{V}$ ) $T_c=25^\circ\text{C}$	$I_D$	36	A
	Pulsed	$I_{DM}$	72	A
Single Pulsed Avalanche Energy		$E_{AS}$	12 (Note 3)	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Power Dissipation	TO-220/TO-263	$P_D$	90	W
	TO-220F		36	W
	TO-251/TO-252		44	W
	SOP-8		5	W
Junction Temperature		$T_J$	+150	$^\circ\text{C}$
Storage Temperature		$T_{STG}$	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3.  $L=0.1\text{mH}$ ,  $I_{AS}=16\text{A}$ ,  $V_{DD}=50\text{V}$ ,  $R_G=25\Omega$ , Starting  $T_J=25^\circ\text{C}$ .

4.  $I_{SD} \leq 30\text{A}$ ,  $di/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J=25^\circ\text{C}$ .

■ THERMAL DATA

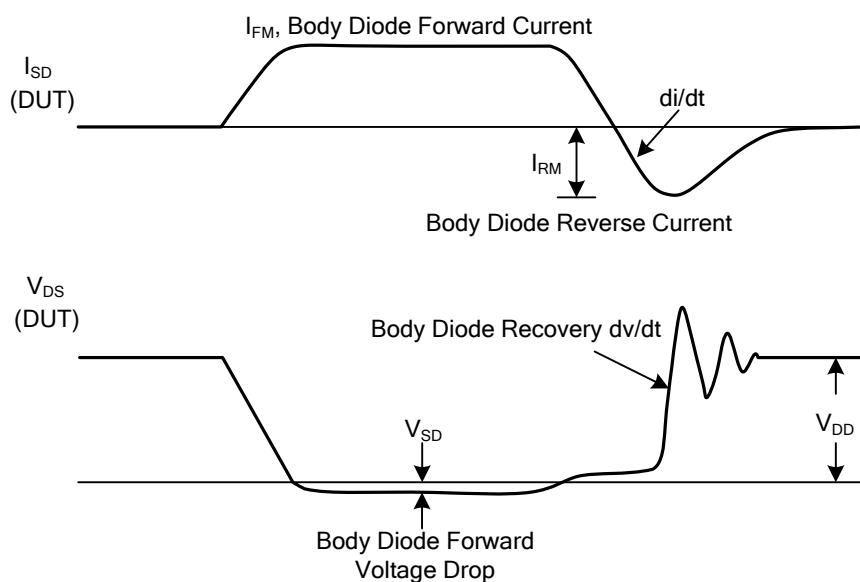
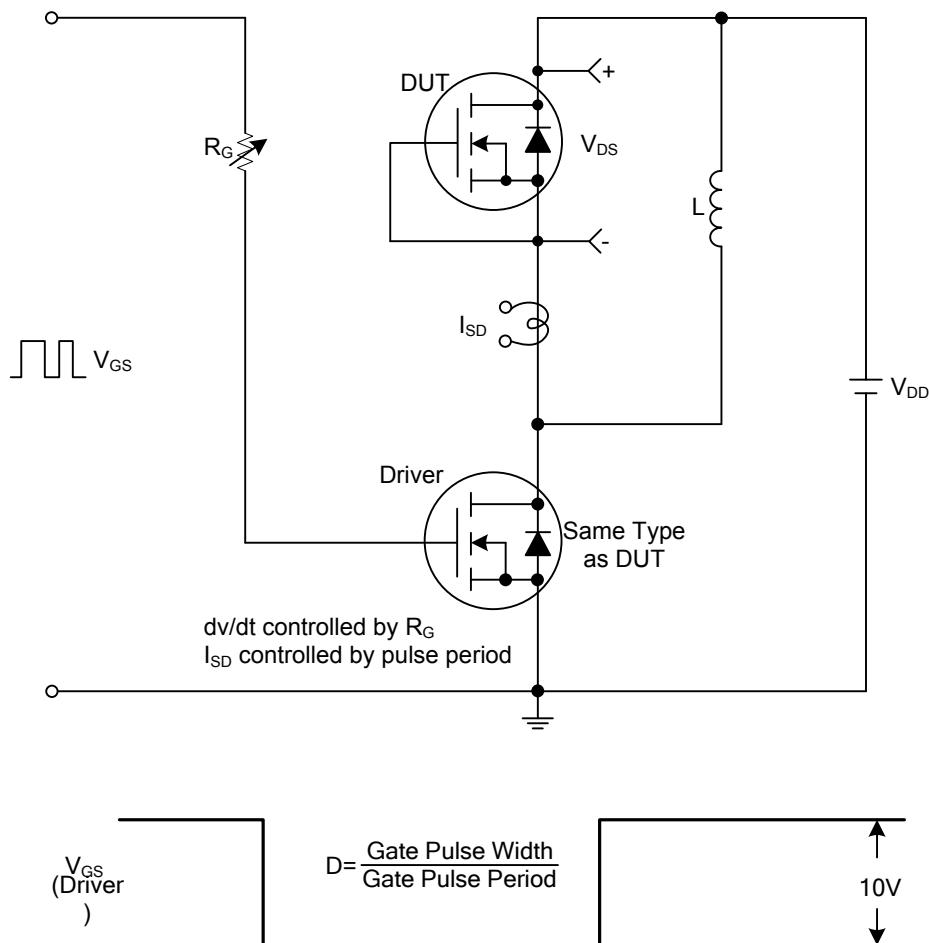
PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F	$\theta_{JA}$	62.5	$^\circ\text{C/W}$
	TO-263		110	$^\circ\text{C/W}$
	TO-251/TO-252		125 (Note)	$^\circ\text{C/W}$
	SOP-8			
Junction to Case	TO-220/TO-263	$\theta_{JC}$	1.38	$^\circ\text{C/W}$
	TO-220F		3.47	$^\circ\text{C/W}$
	TO-251/TO-252		2.85 (Note)	$^\circ\text{C/W}$
	SOP-8		25 (Note)	$^\circ\text{C/W}$

Note: The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.

■ ELECTRICAL CHARACTERISTICS ( $T_J=25^\circ\text{C}$ , unless otherwise specified)

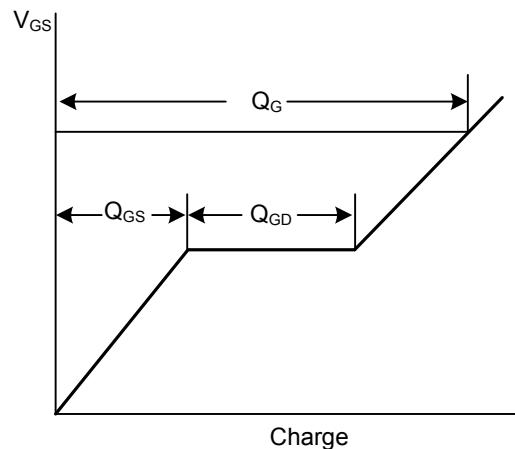
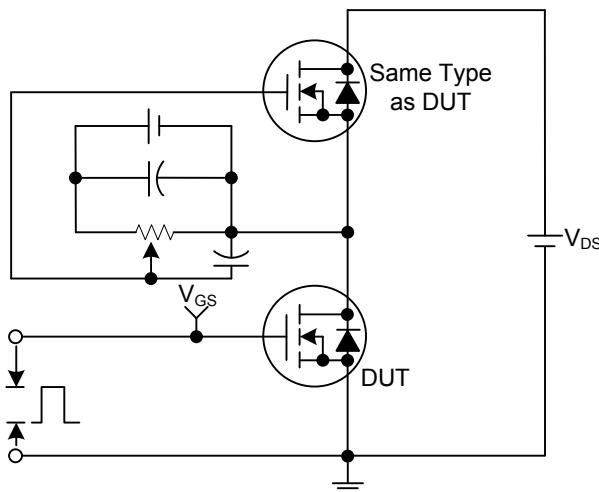
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$I_D=250\mu\text{A}, V_{GS}=0\text{V}$	100			V
Drain-Source Leakage Current	$I_{\text{DSS}}$	$V_{DS}=100\text{V}, V_{GS}=0\text{V}$		1		$\mu\text{A}$
Gate- Source Leakage Current	Forward	$V_{GS}=+20\text{V}, V_{DS}=0\text{V}$			+100	nA
	Reverse	$V_{GS}=-20\text{V}, V_{DS}=0\text{V}$			-100	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{GS(\text{TH})}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1.0		3.0	V
Static Drain-Source On-State Resistance	$R_{DS(\text{ON})}$	$V_{GS}=10\text{V}, I_D=30\text{A}$			44	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}, I_D=15\text{A}$			48	$\text{m}\Omega$
<b>DYNAMIC PARAMETERS</b>						
Input Capacitance	$C_{ISS}$	$V_{GS}=0\text{V}, V_{DS}=25\text{V}, f=1.0\text{MHz}$		2650		pF
Output Capacitance	$C_{OSS}$			145		pF
Reverse Transfer Capacitance	$C_{RSS}$			100		pF
<b>SWITCHING PARAMETERS</b>						
Total Gate Charge at 10V	$Q_G$	$V_{DD}=80\text{V}, I_D=36\text{A}, V_{GS}=10\text{V}$		68		nC
Gate to Source Charge	$Q_{GS}$			9		nC
Gate to Drain Charge	$Q_{GD}$			12		nC
Turn-ON Delay Time	$t_{D(\text{ON})}$			10		ns
Rise Time	$t_R$			18		ns
Turn-OFF Delay Time	$t_{D(\text{OFF})}$			46		ns
Fall-Time	$t_F$			20		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Drain-Source Diode Forward Voltage	$V_{SD}$	$I_{SD}=36\text{A}$			1.4	V
Body Diode Reverse Recovery Time (Note 1)	$t_{rr}$	$I_S=30\text{A}, V_{GS}=0\text{V}, dI_F/dt=100\text{A}/\mu\text{s}$		51		nS
Body Diode Reverse Recovery Charge	$Q_{rr}$			56		nC

## ■ TEST CIRCUITS AND WAVEFORMS

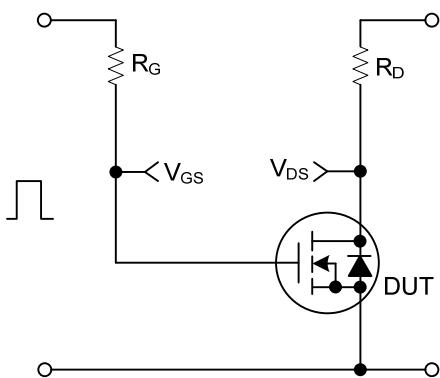


Peak Diode Recovery dv/dt Test Circuit and Waveforms

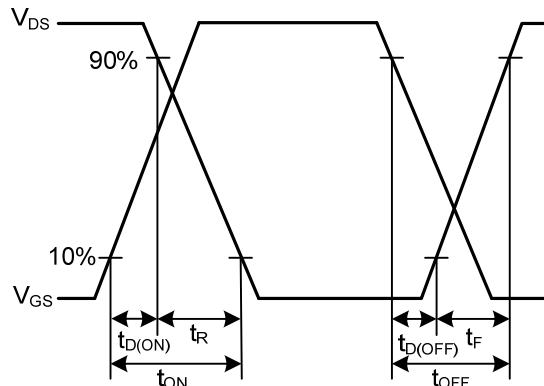
### ■ TEST CIRCUITS AND WAVEFORMS



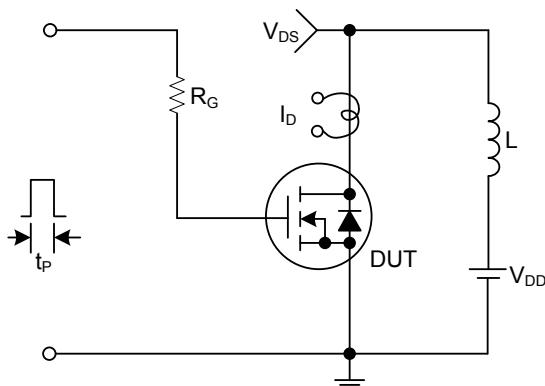
Gate Charge Test Circuit



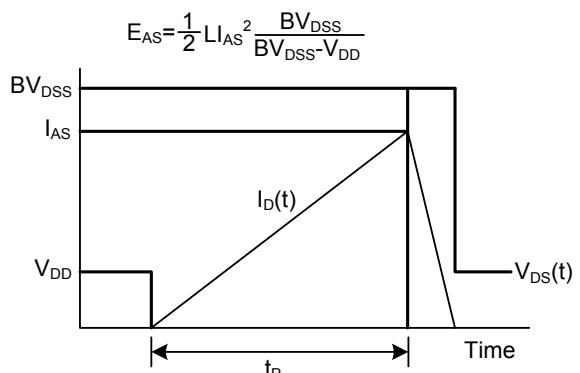
Gate Charge Waveforms



Resistive Switching Test Circuit



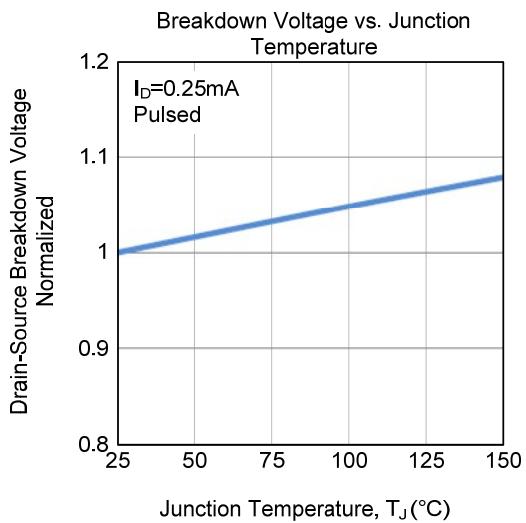
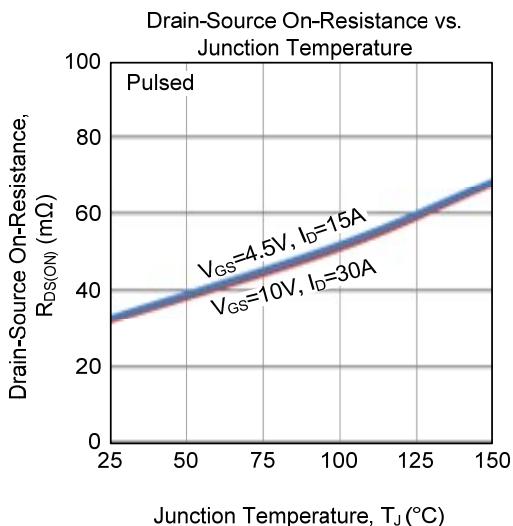
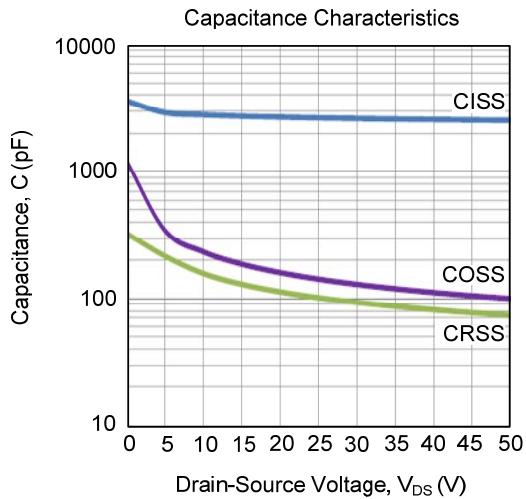
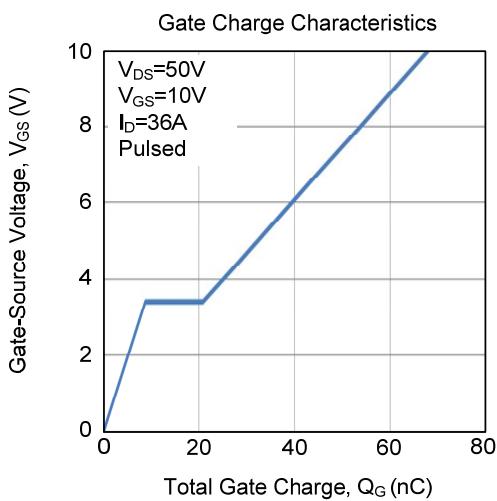
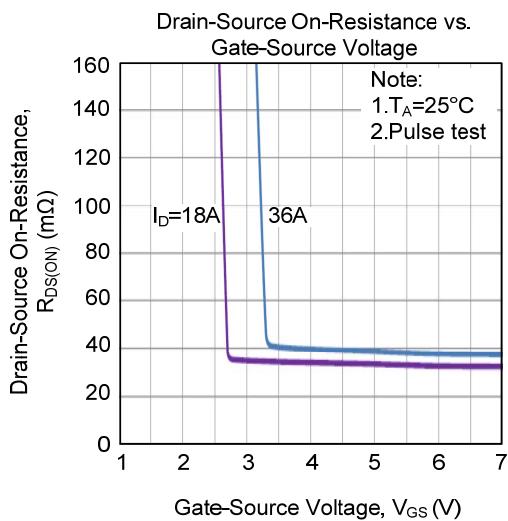
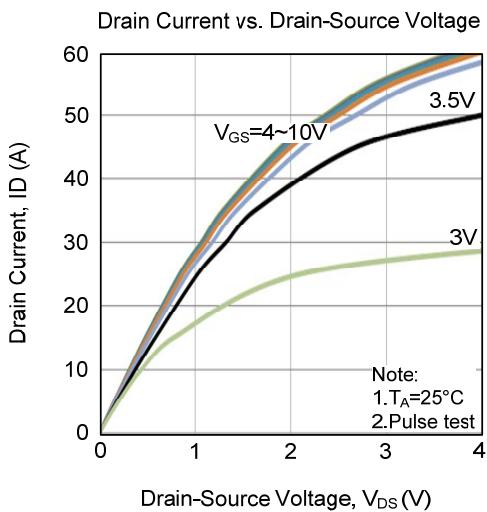
Resistive Switching Waveforms



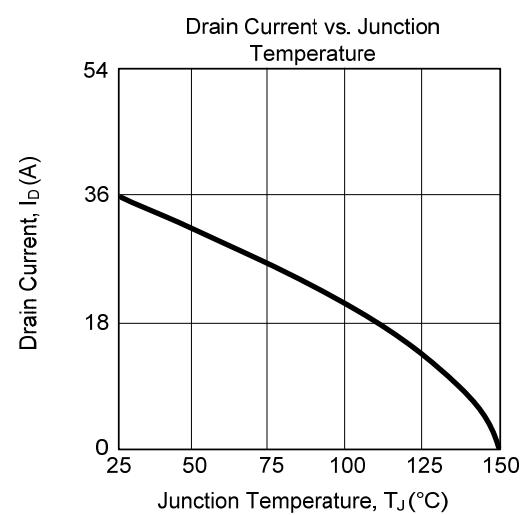
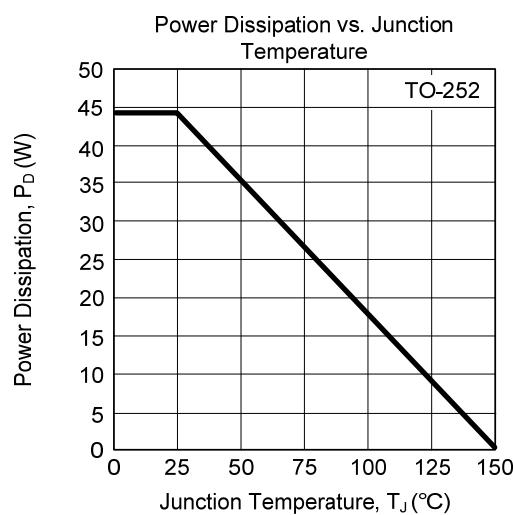
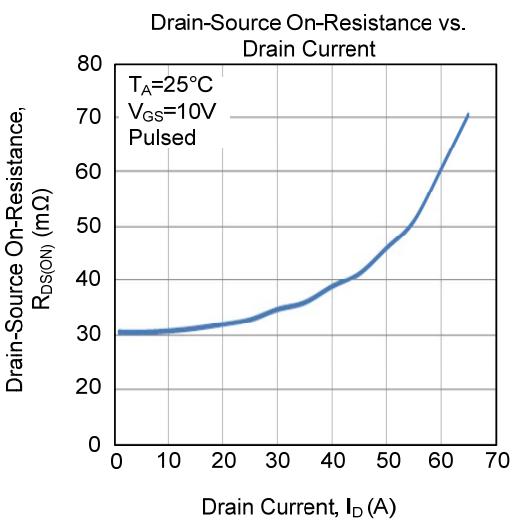
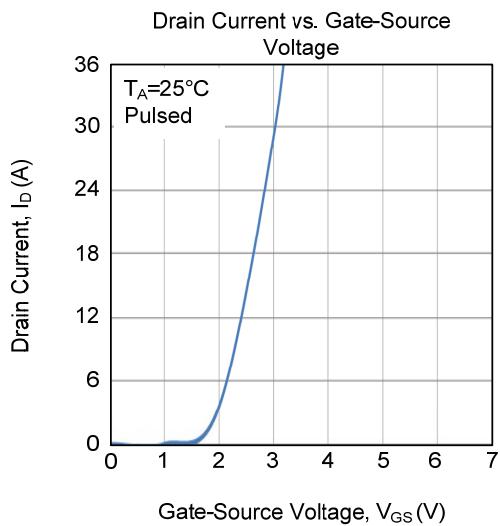
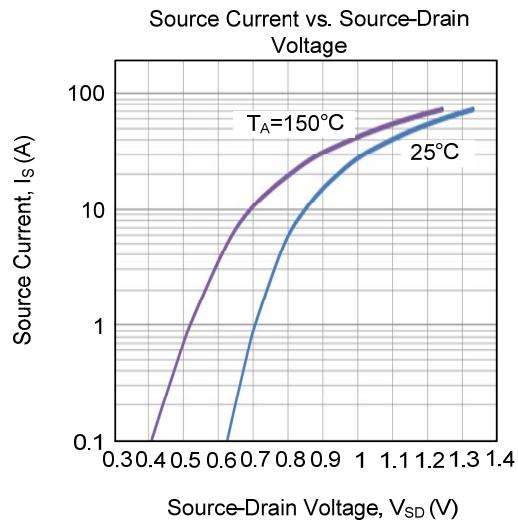
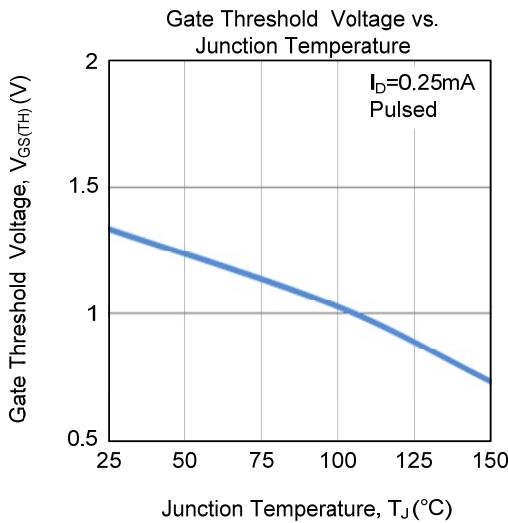
Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

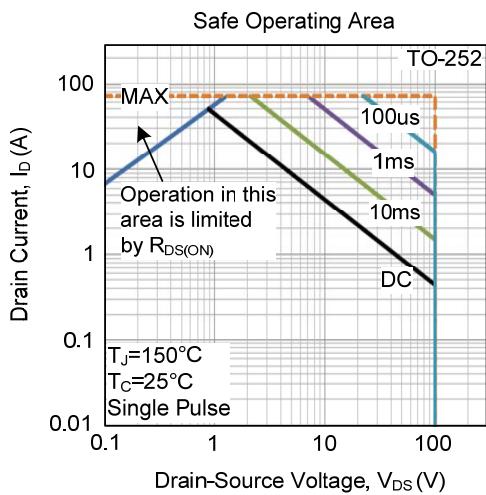
■ TYPICAL CHARACTERISTICS



## ■ TYPICAL CHARACTERISTICS (Cont.)



## ■ TYPICAL CHARACTERISTICS (Cont.)



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