



UZ5C056

Preliminary

DIODE

PROTECTION IN PORTABLE ELECTRONICS APPLICATIONS

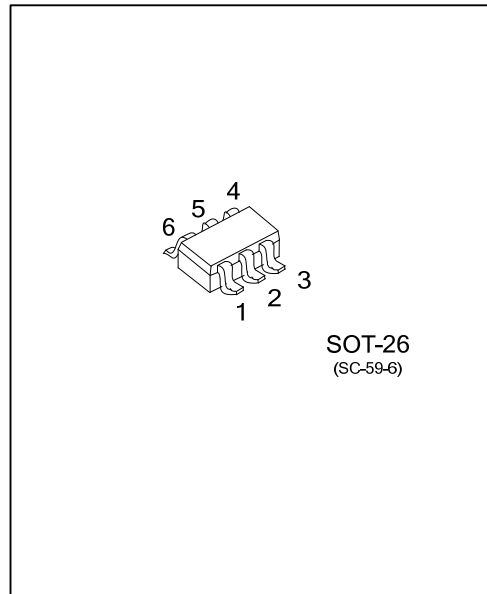
DESCRIPTION

The UTC **UZ5C056** is a protection in portable electronics applications, it uses UTC's advanced technology to provide customers low operating current, low leakage current and low clamping voltage.

The UTC **UZ5C056** is suitable for PDA, Cell phone, Cordless phones and Notebooks, etc.

FEATURES

- * Low operating current
- * Low leakage current
- * Low clamping voltage.

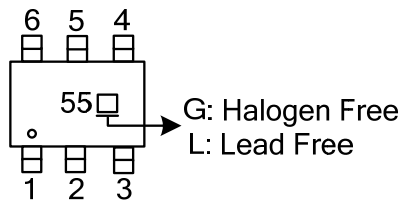


ORDERING INFORMATION

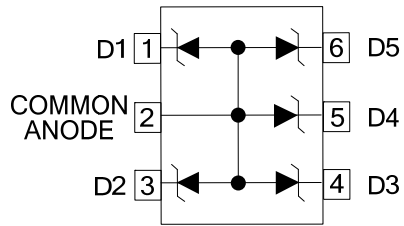
Ordering Number		Package	Packing
Lead Free	Halogen Free		
UZ5C056L-AG6-R	UZ5C056G-AG6-R	SOT-26	Tape Reel

UZ5C05L-AG6-R <ul style="list-style-type: none"> (1) Packing Type (2) Package Type (3) Halogen Free 	<ul style="list-style-type: none"> (1) R: Tape Reel (2) AG6: SOT-26 (3) G: Halogen Free, L: Lead Free
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MARKING



■ PIN CONFIGURATION



■ PIN DESCRIPTION

PIN NO.	PIN NAME	DESCRIPTION
1	D1	Terminal of Cathode 1
2	COMMON ANODE	Terminal of Common Anode
3	D2	Terminal of Cathode 2
4	D3	Terminal of Cathode 3
5	D4	Terminal of Cathode 4
6	D5	Terminal of Cathode 5

■ ABSOLUTE MAXIMUM RATING ($T_A=25^{\circ}\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Peak Pulse Power ($t_P=8/20\mu\text{s}$)	P_{PK}	150	W
Peak Pulse Current ($t_P=8/20\mu\text{s}$)	I_{PP}	10	A
Junction Temperature	T_J	-55~150	$^{\circ}\text{C}$
Storage Temperature	T_{STG}	-55~150	$^{\circ}\text{C}$

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.
Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS ($T_A=25^{\circ}\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Stand-Off Voltage	V_{RWM}				5	V
Reverse Breakdown Voltage	V_{BR}	$I_T=1\text{mA}$	6.3			V
Reverse Leakage Current	I_R	$V_{RWM}=5\text{V}$			1	μA
Clamping Voltage	V_C	$I_{PP}=10\text{A}$, $t_P=8/20\mu\text{s}$			14.5	V
Junction Capacitance	C_J	$V_R=0\text{V}$, $f=1\text{MHz}$ Between I/O Pins and GND			100	pF

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