

UTT50P06

Power MOSFET

**-50A, -60V P-CHANNEL (D-S)
POWER MOSFET**

■ DESCRIPTION

The UTC **UTT50P06** is a P-channel power MOSFET using UTC's advanced technology to provide the customers with high switching speed and a minimum on-state resistance, and it can also withstand high energy in the avalanche.

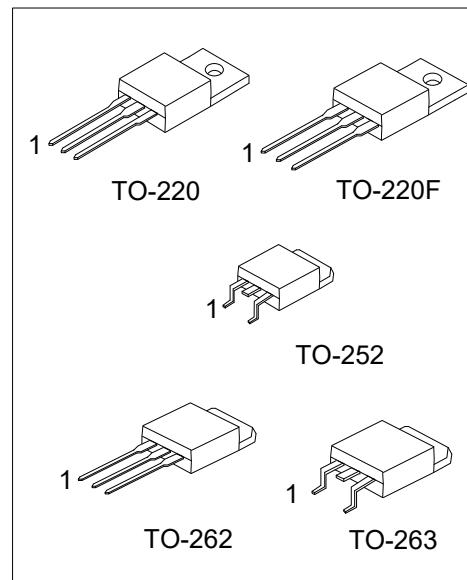
This UTC **UTT50P06** is suitable for load switch, etc.

■ FEATURES

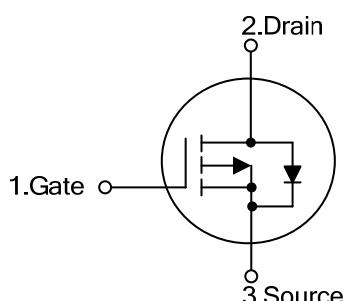
* $R_{DS(ON)} \leq 15 \text{ m}\Omega @ V_{GS}=-10V, I_D=-17A$

$R_{DS(ON)} \leq 20 \text{ m}\Omega @ V_{GS}=-4.5V, I_D=-14A$

* High Switching Speed



■ SYMBOL



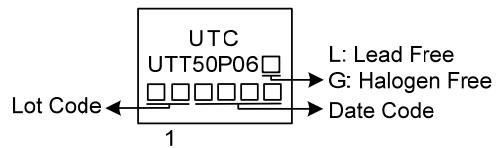
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UTT50P06L-TA3-T	UTT50P06G-TA3-T	TO-220	G	D	S	Tube
UTT50P06L-TF3-T	UTT50P06G-TF3-T	TO-220F	G	D	S	Tube
UTT50P06L-TN3-R	UTT50P06G-TN3-R	TO-252	G	D	S	Tape Reel
UTT50P06L-T2Q-T	UTT50P06G-T2Q-T	TO-262	G	D	S	Tube
UTT50P06L-TQ2-T	UTT50P06G-TQ2-T	TO-263	G	D	S	Tube
UTT50P06L-TQ2-R	UTT50P06G-TQ2-R	TO-263	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

UTT50P06G-TA3-T  <ul style="list-style-type: none"> (1)Packing Type (2)Package Type (3)Green Package 	<ul style="list-style-type: none"> (1) T: Tube, R:Tape Reel (2) TF1: TO-220F1, TF3: TO-220F, TN3: TO-252, T2Q: TO-262, TQ2: TO-263 (3) G: Halogen Free and Lead Free, L: Lead Free
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■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_c=25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	-60	V
Gate-Source Voltage		V_{GSS}	± 20	V
Drain Current	Continuous	$T_c=25^\circ\text{C}$	I_D	-50
		$T_c=125^\circ\text{C}$		-27.5
	Pulsed		I_{DM}	-100
Avalanche Current		I_{AR}	-50	A
Single Pulse Avalanche Energy (Note 2)	$L=0.1\text{mH}$	E_{AS}	145	mJ
Power Dissipation ($T_c=25^\circ\text{C}$)	TO-220	P_D	160	W
	TO-262			
	TO-263		46	W
	TO-220F		60	W
	TO-252			
Junction Temperature	T_J		-55 ~ +150	$^\circ\text{C}$
Storage Temperature	T_{STG}		-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L = 0.1\text{mH}$, $I_{AS} = 53.8\text{A}$, $V_{DD} = 30\text{V}$, $R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 30\text{A}$, $dI/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220	θ_{JA}	62.5	$^\circ\text{C/W}$
	TO-220F			
Junction to Case	TO-262	θ_{JC}	0.78	$^\circ\text{C/W}$
	TO-263			
	TO-220F			
	TO-252		2.71	$^\circ\text{C/W}$
			2.08 (Note)	$^\circ\text{C/W}$

Note: Device mounted on FR-4 substrate P_c board, 2oz copper, with 1inch square copper plate.

■ ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$, unless otherwise specified)

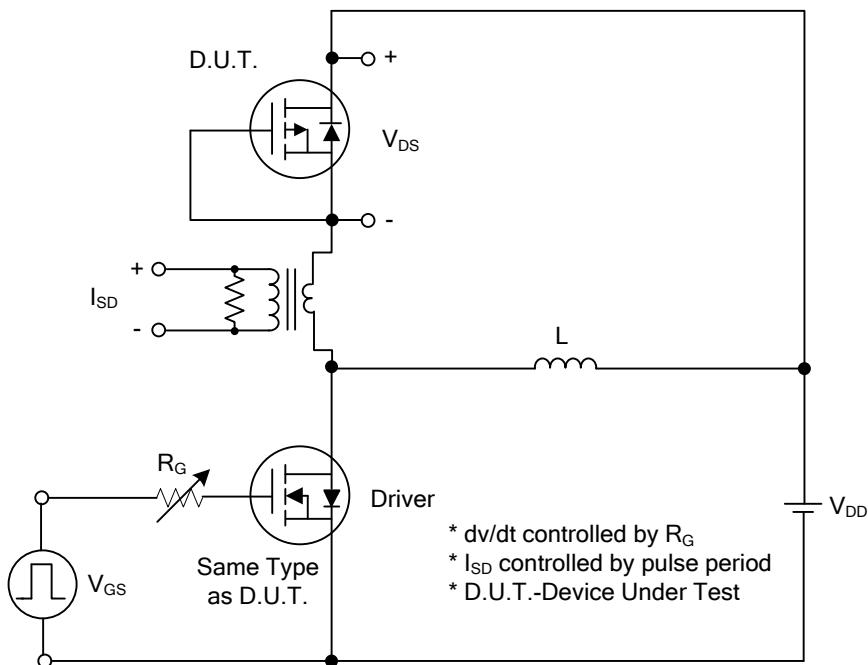
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-60			V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=-60\text{V}, V_{\text{GS}}=0\text{V}$			-1	μA
Gate Threshold Voltage	$V_{\text{GS}(\text{TH})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-1.0		-3.0	V
Gate- Source Leakage Current	Forward	$V_{\text{GS}}=+20\text{V}, V_{\text{DS}}=0\text{V}$			+100	nA
	Reverse	$V_{\text{GS}}=-20\text{V}, V_{\text{DS}}=0\text{V}$			-100	nA
ON CHARACTERISTICS						
Static Drain-Source On-State Resistance (Note 1)	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-17\text{A}$			15	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-14\text{A}$			20	$\text{m}\Omega$
DYNAMIC PARAMETERS (Note 2)						
Input Capacitance	C_{ISS}	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=-25\text{V}, f=1\text{MHz}$		5140		pF
Output Capacitance	C_{OSS}			390		pF
Reverse Transfer Capacitance	C_{RSS}			286		pF
SWITCHING PARAMETERS (Note 1,2,3)						
Total Gate Charge	Q_G	$V_{\text{DS}}=-48\text{V}, V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-50\text{A}$		96		nC
Gate to Source Charge	Q_{GS}			13		nC
Gate to Drain Charge	Q_{GD}			21		nC
Turn-ON Delay Time	$t_{\text{D}(\text{ON})}$	$V_{\text{DD}}=-30\text{V}, V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-50\text{A}, R_{\text{G}}=3.3\Omega$		12		ns
Rise Time	t_R			19		ns
Turn-OFF Delay Time	$t_{\text{D}(\text{OFF})}$			130		ns
Fall-Time	t_F			63		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_C=25^\circ\text{C}$) (Note 2)						
Maximum Body-Diode Continuous Current	I_S				-50	A
Maximum Body-Diode Pulsed Current	I_{SM}				-80	A
Drain-Source Diode Forward Voltage (Note 1)	V_{SD}	$I_F=-50\text{A}, V_{\text{GS}}=0\text{V}$			-1.6	V
Body Diode Reverse Recovery Time	t_{rr}	$I_F=-30\text{A}, dI/dt=100\text{A}/\mu\text{s}$		60		ns
Body Diode Reverse Recovery Charge	Q_{rr}			80		nC

Notes: 1. Pulse Test : Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

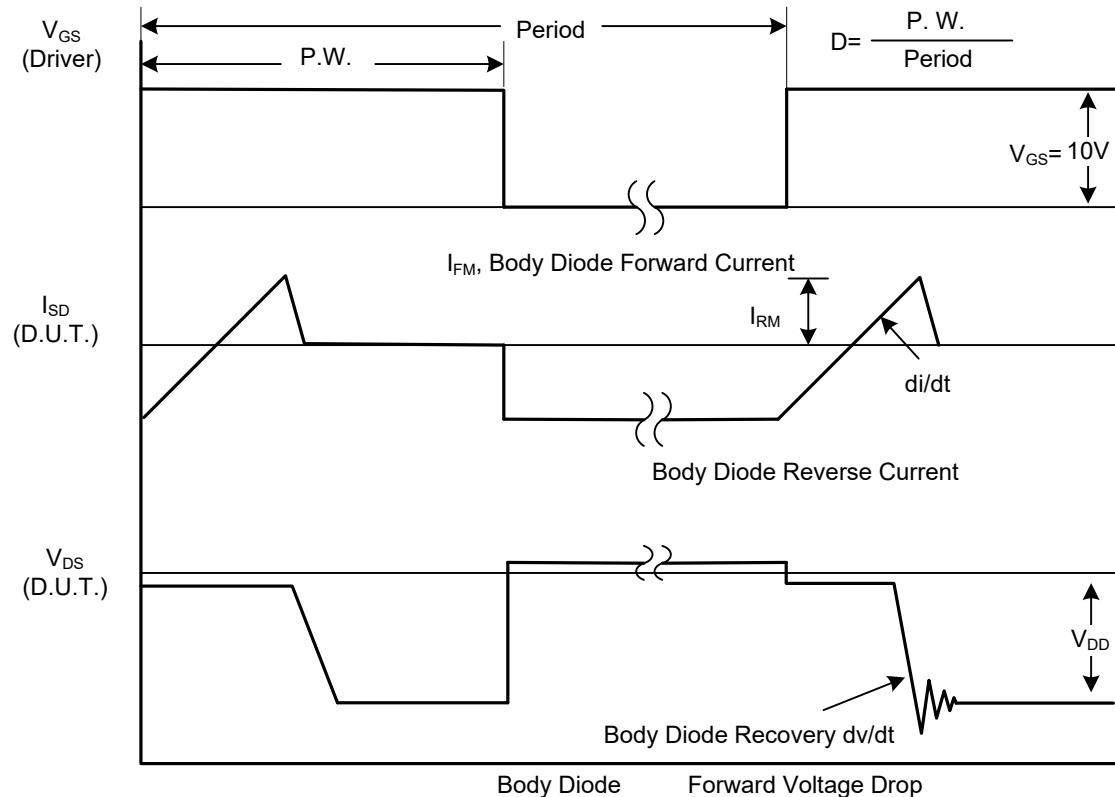
2. Guaranteed by design, not subject to production testing.

3. Independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

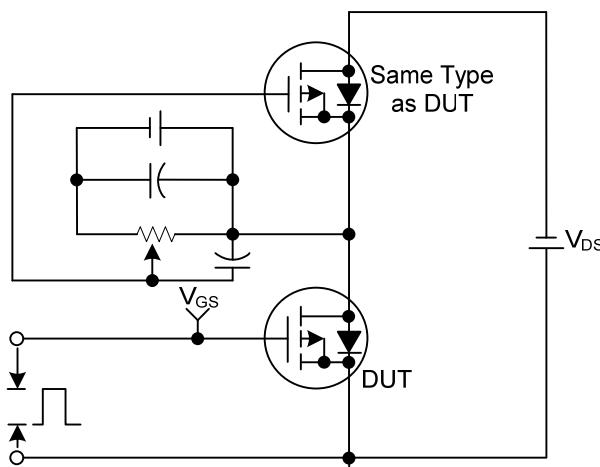


Peak Diode Recovery dv/dt Test Circuit

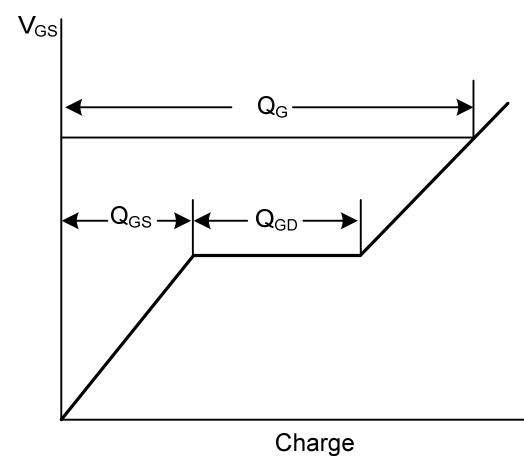


Peak Diode Recovery dv/dt Waveforms

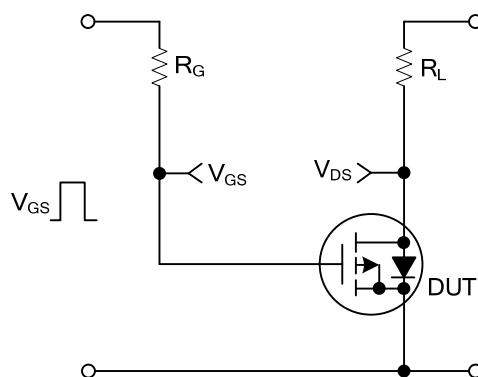
■ TEST CIRCUITS AND WAVEFORMS



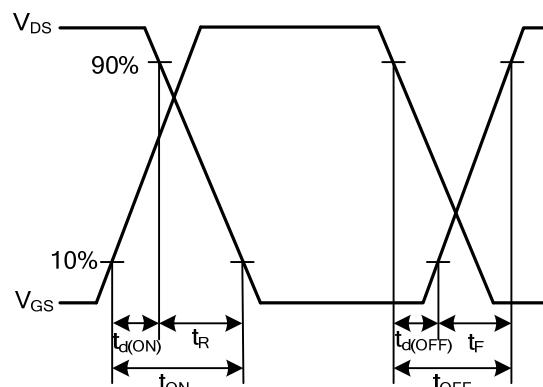
Gate Charge Test Circuit



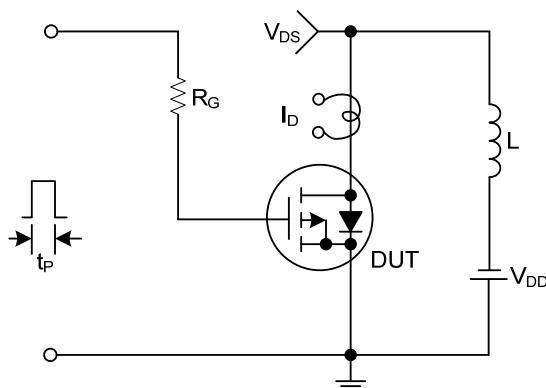
Gate Charge Waveforms



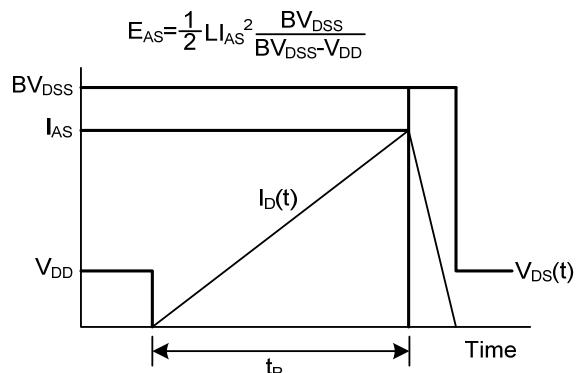
Resistive Switching Test Circuit



Resistive Switching Waveforms

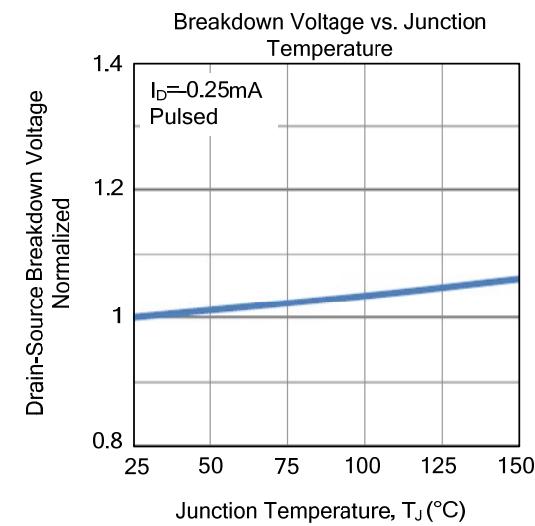
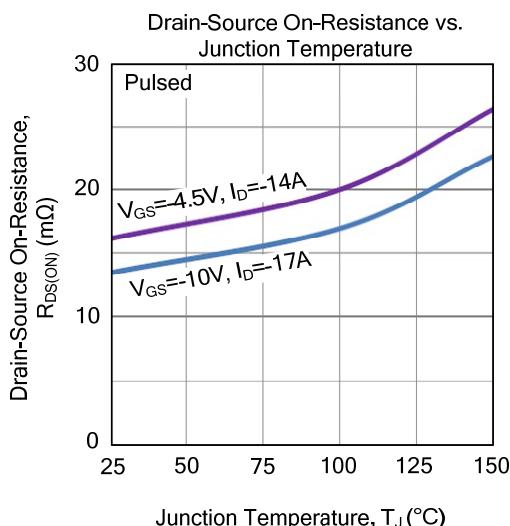
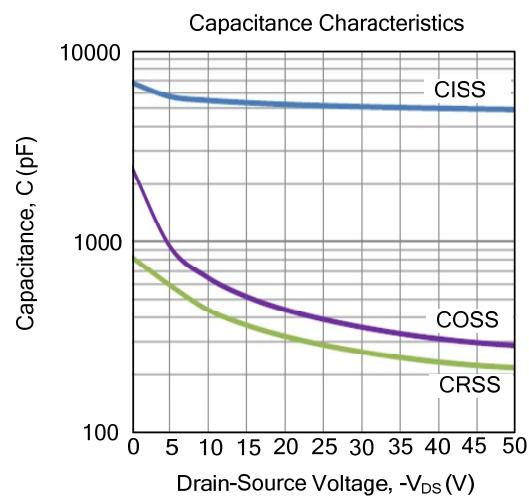
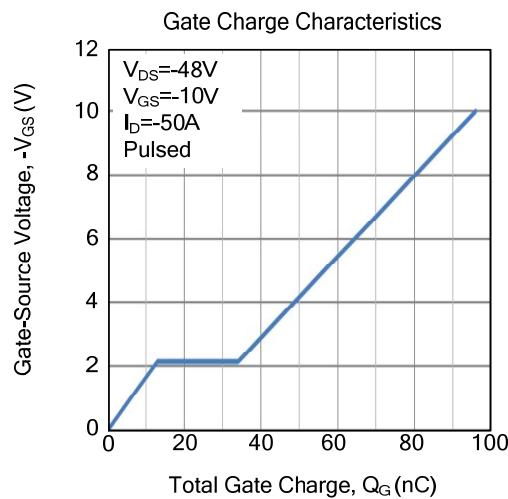
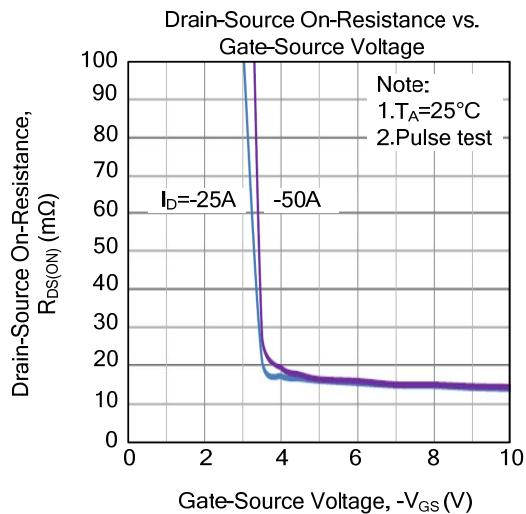
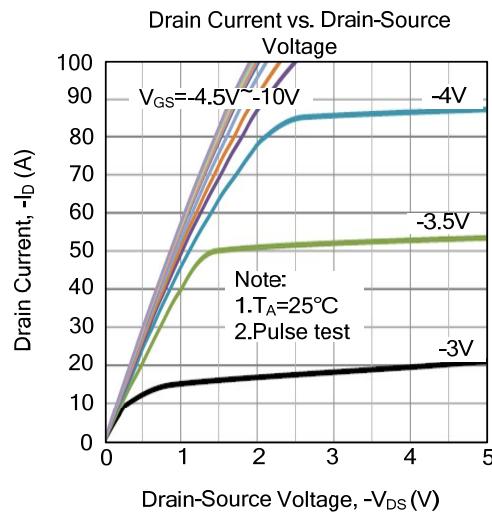


Unclamped Inductive Switching Test Circuit

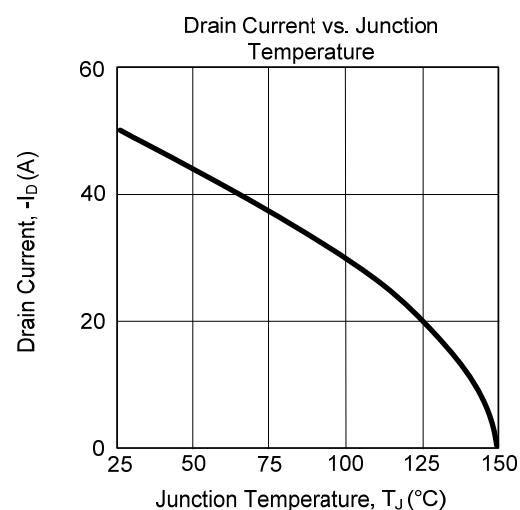
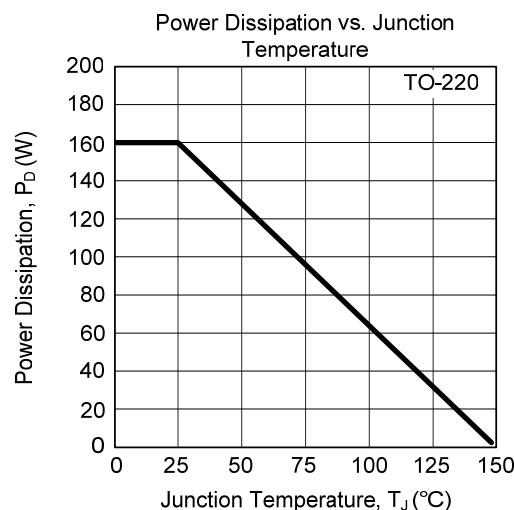
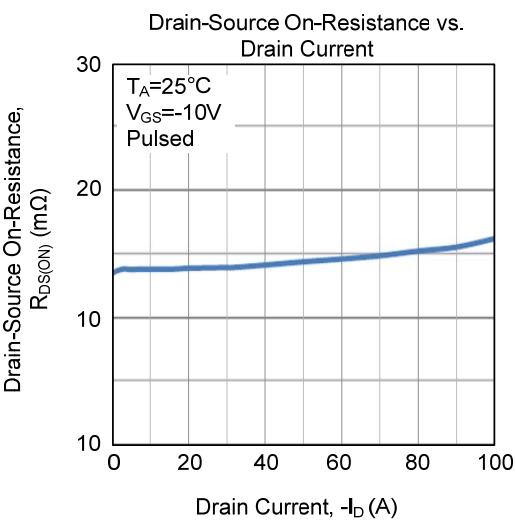
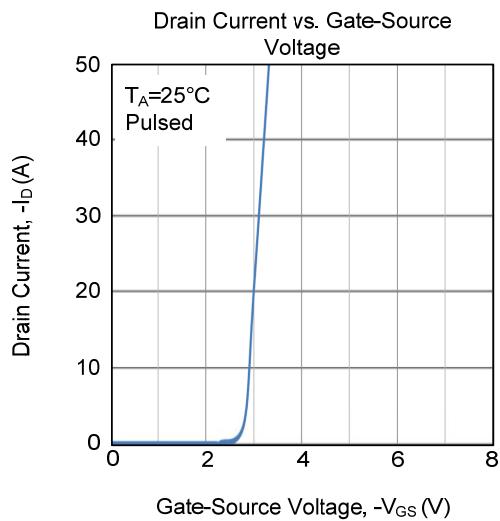
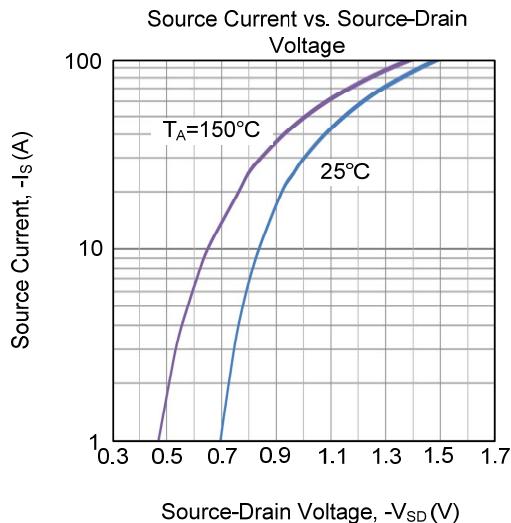
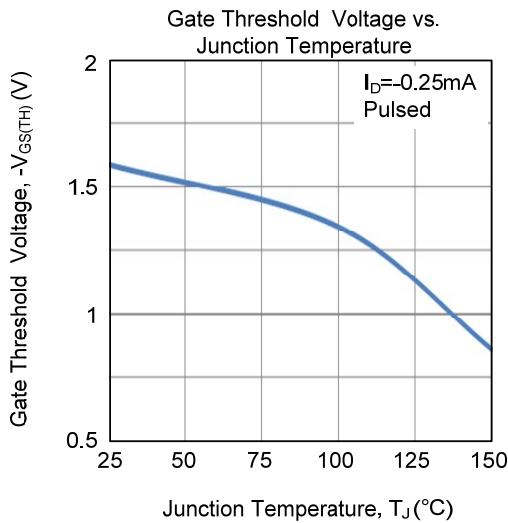


Unclamped Inductive Switching Waveforms

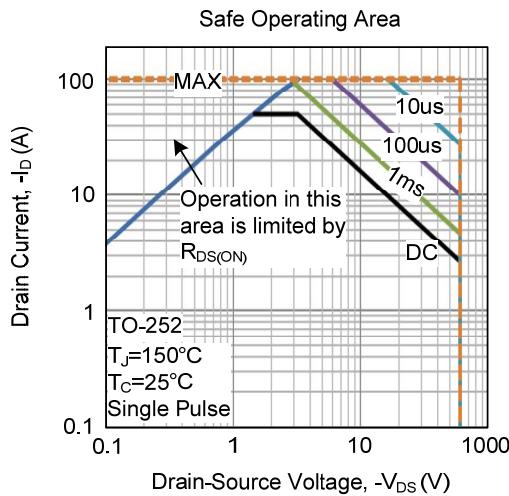
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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