

# **Power MOSFET**

# -18.3A, -60V P-CHANNEL POWER MOSFET

### DESCRIPTION

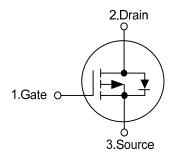
The UTC **UTT18P06** is a P-channel power MOSFET using UTC's advanced technology to provide the customers with high switching speed, cost-effectiveness and minimum on-state resistance. It can also withstand high energy in the avalanche.

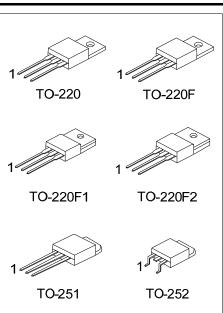
#### FEATURES

\*  $\mathsf{R}_{\mathsf{DS}(\mathsf{ON})} \leq 0.070~\Omega$  @  $\mathsf{V}_{\mathsf{GS}}\texttt{=}$  -10V,  $\mathsf{I}_{\mathsf{D}}\texttt{=}$  -18.3A

\* High Switching Speed

#### SYMBOL





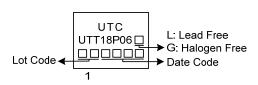
#### ORDERING INFORMATION

Ordering Number		Daakaga	Pin Assignment			Deeking
Lead Free	Halogen Free	Package	1	2	3	Packing
UTT18P06L-TA3-T	UTT18P06G-TA3-T	TO-220	G	D	S	Tube
UTT18P06L-TF1-T	UTT18P06G-TF1-T	TO-220F1	G	D	S	Tube
UTT18P06L-TF2-T	UTT18P06G-TF2-T	TO-220F2	G	D	S	Tube
UTT18P06L-TF3-T	UTT18P06G-TF3-T	TO-220F	G	D	S	Tube
UTT18P06L-TM3-T	UTT18P06G-TM3-T	TO-251	G	D	S	Tube
UTT18P06L-TN3-R	UTT18P06G-TN3-R	TO-252	G	D	S	Tape Reel
Note: Pin Assignment: C: C:	ate D: Drain S: Source					

Note: Pin Assignment: G: Gate D: Drain S: Source

UTT18P06G-TA3-T (1)Packing Type (2)Package Type (3)Green Package	<ul> <li>(1) T: Tube, R: Tape Reel</li> <li>(2) TA3: TO-220, TF1: TO-220F1, TF2: TO-220F2 TF3: TO-220F, TM3: TO-251, TN3: TO-252</li> <li>(3) G: Halogen Free and Lead Free, L: Lead Free</li> </ul>
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#### MARKING



#### ■ ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER			SYMBOL	RATINGS	UNIT	
Drain-Source Voltage			V <sub>DSS</sub>	-60	V	
Gate-Source Voltage			V <sub>GSS</sub>	±20	V	
Drain Current	Continuous	T <sub>C</sub> =25°C	I <sub>D</sub>	-18.3	А	
	Pulsed		I <sub>DM</sub>	-73.2	Α	
Single Pulsed Avalanche Current (L=0.1mH)			AS	-18.3	А	
Single Pulsed Avalanche Energy (L=0.1mH) (Note 3)			E <sub>AS</sub>	24.2	mJ	
Power Dissipation (Note 4)	T <sub>C</sub> =25°C	TO-220	P <sub>D</sub>	90	W	
		TO-220F TO-220F1 TO-220F2		39	w	
		TO-251 TO-252		41	w	
Junction Temperature			TJ	+150	°C	
Storage Temperature			T <sub>STG</sub>	-55 ~ +150	°C	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. Duty cycle  $\leq$  1 %.

4. See SOA curve for voltage derating.

#### THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT	
Junction to Ambient	TO-220/TO-220F TO-220F1/TO-220F2	θ <sub>JA</sub>	62.5	°C/W	
	TO-251/TO-252	-	110		
Junction to Case	TO-220		1.38		
	TO-220F/TO-220F1 TO-220F2	θ <sub>JC</sub>	3.19	°C/W	
	TO-251/TO-252	]	3.05 (Note 3)		

Note: Device mounted on FR-4 substrate P<sub>C</sub> board, 2oz copper, with 1inch square copper plate.



### ■ ELECTRICAL CHARACTERISTICS (TJ=25°C, unless otherwise specified)

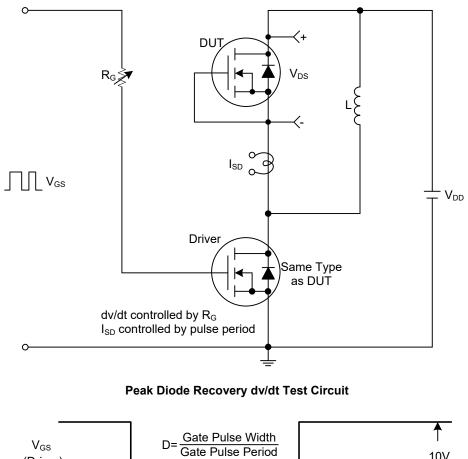
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PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS			·				
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	I <sub>D</sub> =-250μΑ, V <sub>GS</sub> =0V	-60			V
Drain-Source Leakage Current		I <sub>DSS</sub>	V <sub>DS</sub> =-60V, V <sub>GS</sub> =0V			-1	μA
Cata Source Leakage Current	Forward	I <sub>GSS</sub>	V <sub>GS</sub> =+20V, V <sub>DS</sub> =0V			+100	nA
Gate-Source Leakage Current	Reverse		V <sub>GS</sub> =-20V, V <sub>DS</sub> =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250µA	-1.0		-3.0	V
Static Drain-Source On-State Re	sistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-18.3A (Note 1)		0.055	0.070	Ω
On State Drain Current (Note 1)		I <sub>D(ON)</sub>	V <sub>GS</sub> =-10V, V <sub>DS</sub> =-5V	-30			Α
DYNAMIC PARAMETERS (Note	e 2)						
Input Capacitance		CISS	V <sub>GS</sub> =0V, V <sub>DS</sub> =-25V, f=1.0MHz		840	1310	pF
Output Capacitance		C <sub>OSS</sub>			95		рF
Reverse Transfer Capacitance		C <sub>RSS</sub>	(Note 2)		70		pF
SWITCHING PARAMETERS							
Total Gate Charge		$Q_{G}$	$y_{1} = 10y_{1}y_{2} = 50y_{1}$		35	40	nC
Gate to Source Charge		$Q_{GS}$	$V_{GS}$ =-10V, $V_{DS}$ =-50V,		6		nC
Gate to Drain Charge		$Q_{GD}$	I <sub>D</sub> =-1.3A, I <sub>G</sub> =100µA (Note 3)		7.0		nC
Turn-ON Delay Time		t <sub>D(ON)</sub>			50		ns
Rise Time		t <sub>R</sub>	$V_{DD}$ =-30V, $I_{D}$ =-0.5A, $R_{G}$ =2.5 $\Omega$		43		ns
Turn-OFF Delay Time	Turn-OFF Delay Time		(Note 3)		300		ns
Fall-Time		t <sub>F</sub>			95		ns
SOURCE- DRAIN DIODE RATIN	NGS AND CI	HARACTER	<b>ISTICS</b> (T <sub>C</sub> =25°C) (Note 2)				
Maximum Body-Diode Continuous Current		Is				-18.3	Α
Maximum Body-Diode Pulsed Cu	urrent	I <sub>SM</sub>				-73.2	Α
Drain-Source Diode Forward Voltage		V <sub>SD</sub>	I <sub>F</sub> =-18.3A, V <sub>GS</sub> =0V (Note 1)		-1.0	-1.5	V
Body Diode Reverse Recovery T	ïme	t <sub>rr</sub>	I <sub>F</sub> =-18.3A, dI <sub>F</sub> /dt=100A/μs		14	61	ns
Noton 1 Dulas testi pulas width			0/				

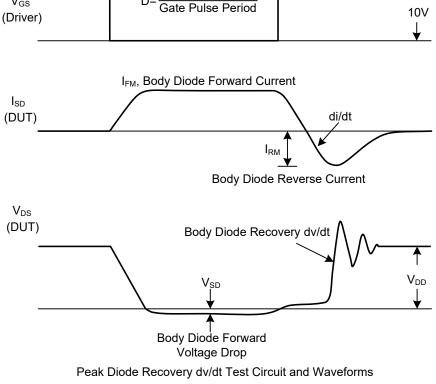
Notes: 1. Pulse test; pulse width ≤ 300  $\mu$ s, duty cycle ≤ 2 %

2. Guaranteed by design, not subject to production testing

3. Independent of operating temperature

### TEST CIRCUITS AND WAVEFORMS

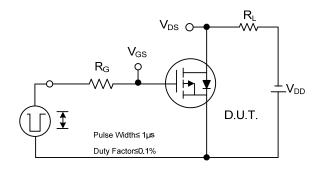


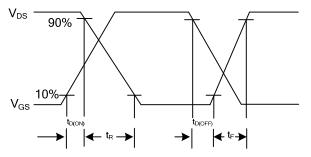


#### Peak Diode Recovery dv/dt Waveforms

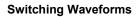


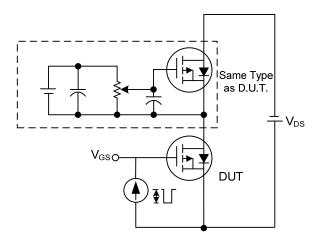
### TEST CIRCUITS AND WAVEFORMS



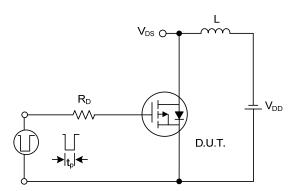




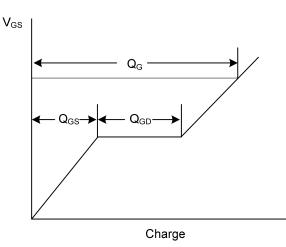




Gate Charge Test Circuit

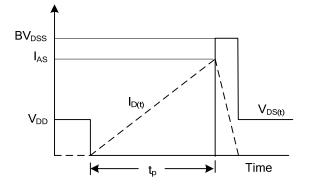


Unclamped Inductive Switching Test Circuit





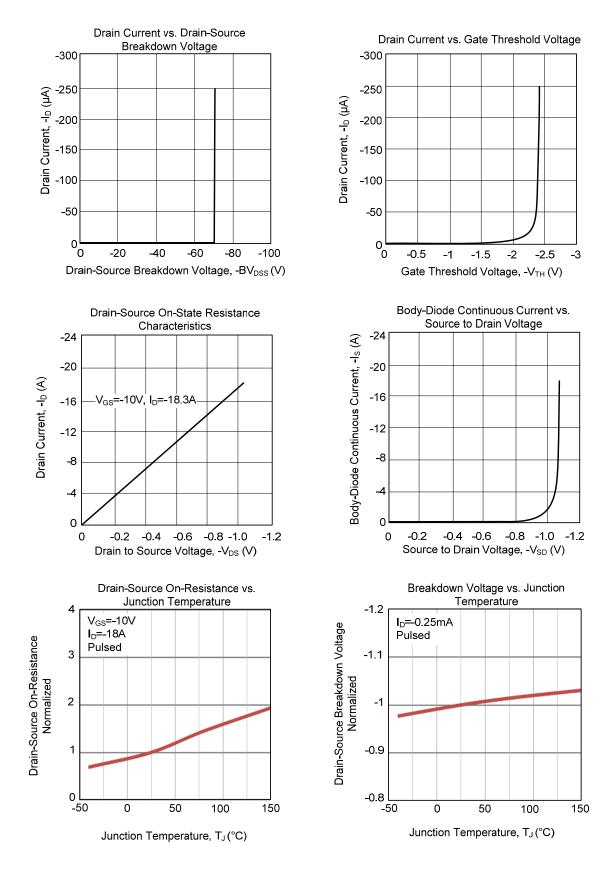




**Unclamped Inductive Switching Waveforms** 

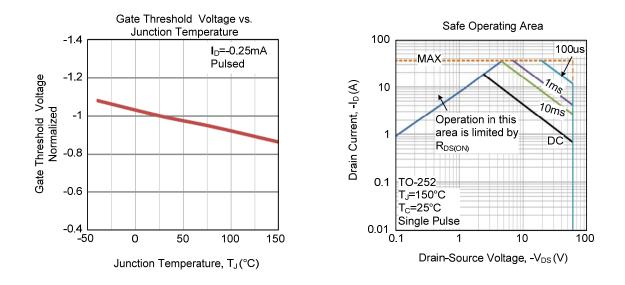


## TYPICAL CHARACTERISTICS





# **TYPICAL CHARACTERISTICS (Cont.)**



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