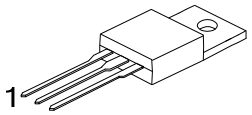
80A, 50V N-CHANNEL POWER MOSFET



TO-220

■ DESCRIPTION

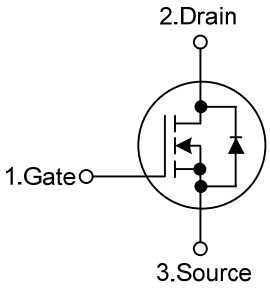
The UTC **UTT80N05** is an N-channel enhancement mode power MOSFET using UTC's advanced technology to provide customers with a minimum on-state resistance, superior switching performance and low gate charge.

The UTC **UTT80N05** is suitable for switching regulators, DC linear mode control, automotive systems, solenoid & motor control, etc.

■ FEATURES

* $R_{DS(ON)}$= 5.1mΩ @ V_{GS}=10V, I_D=80A
* High switching speed

■ SYMBOL



■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UTT80N05L-TA3-T	UTT80N05G-TA3-T	TO-220	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

UTT80N05L-TA3-T

(1)Packing Type — (1) T: Tube

(2)Package Type — (2) TA3: TO-220

(3)Lead Free — (3) G: Halogen Free, L: Lead Free

■ ABSOLUTE MAXIMUM RATINGS (T_J=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage (Note 2)		V_{DSS}	50	V
Gate-Source Voltage		V_{GSS}	±20	V
Drain Current	Continuous (T_C<135°C, V_{GS}=10V)	I_D	80	A
	Pulsed	I_{DM}	320	A
Single Pulsed Avalanche Energy (Note 3)		E_{AS}	860	mJ
Power Dissipation		P_D	312	W
Derate Above 25°C			2.5	W/°C
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-55~+150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Starting T_J=25~150°C
3. Starting T_J=25°C , L = 0.42mH, I_{AS} = 64A

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	62	°C/W
Junction to Case	θ_{JC}	0.4	°C/W

■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	I_D=250μA, V_{GS}=0V	50			V
Drain-Source Leakage Current		I_{DSS}	V_{DS}=50V, V_{GS}=0V			1	μA
Gate-Source Leakage Current	Forward	I_{GSS}	V_{GS}=+20V, V_{DS}=0V			+100	nA
	Reverse		V_{GS}=-20V, V_{DS}=0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=250μA	2	2.8	4	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	V_{GS}=10V, I_D=80A		5.1	7	mΩ
DYNAMIC PARAMETERS							
Input Capacitance		C_{ISS}	V_{GS}=0V, V_{DS}=25V, f=1.0MHz		3565		pF
Output Capacitance		C_{OSS}			1310		pF
Reverse Transfer Capacitance		C_{RSS}			395		pF
SWITCHING PARAMETERS							
Total Gate Charge at 20V		Q_G	V_{DD}=30V, I_D=80A, R_L=0.4Ω		207	269	nC
Gate to Source Charge		Q_{GS}			17.2		nC
Gate to Drain Charge		Q_{GD}			52		nC
Turn-ON Delay Time		$t_{D(ON)}$	V_{DD}=30V, I_D=80A, R_L=0.4Ω, V_{GS}=10V, R_{GS}=2.5 Ω		12		ns
Rise Time		t_R			34		ns
Turn-OFF Delay Time		$t_{D(OFF)}$			37		ns
Fall-Time		t_F			23		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Drain-Source Diode Forward Voltage		V_{SD}	I_{SD}=80A		0.9	1.25	V