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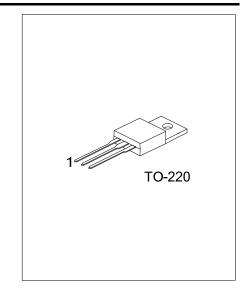
UTT80N05 Preliminary Power MOSFET

80A, 50V N-CHANNEL POWER MOSFET

■ DESCRIPTION

The UTC **UTT80N05** is an N-channel enhancement mode power MOSFET using UTC's advanced technology to provide customers with a minimum on-state resistance, superior switching performance and low gate charge.

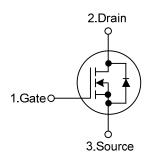
The UTC **UTT80N05** is suitable for switching regulators, DC linear mode control, automotive systems, solenoid & motor control, etc.



■ FEATURES

- * $R_{DS(ON)}$ = 5.1m Ω @ V_{GS} =10V, I_{D} =80A
- * High switching speed

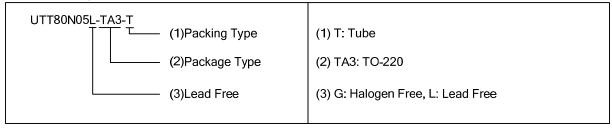
■ SYMBOL



■ ORDERING INFORMATION

Ordering Number		Daalaaaa	Pin Assignment			Daalina	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UTT80N05L-TA3-T	UTT80N05G-TA3-T	TO-220	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source



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■ ABSOLUTE MAXIMUM RATINGS (T_J=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage (Note 2)		V_{DSS}	50	V
Gate-Source Voltage		V_{GSS}	±20	V
Drain Current	Continuous (T _C <135°C, V _{GS} =10V)	I _D	80	Α
	Pulsed	I _{DM}	320	Α
Single Pulsed Avalanche Energy (Note 3)		E _{AS}	860	mJ
Power Dissipation		0	312	W
Derate Above 25°C		P_{D}	2.5	W/°C
Junction Temperature		TJ	+150	°C
Storage Temperature		T _{STG}	-55~+150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Starting T_J=25~150°C
- 3. Starting T_J =25°C , L = 0.42mH, I_{AS} = 64A

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θ_{JA}	62	°C/W	
Junction to Case	θ_{JC}	0.4	°C/W	

■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage		BV_{DSS}	I _D =250μA, V _{GS} =0V				V	
Drain-Source Leakage Current		I_{DSS}	V _{DS} =50V, V _{GS} =0V			1	μΑ	
Gate-Source Leakage Current	Forward	I _{GSS}	V_{GS} =+20V, V_{DS} =0V			+100	nA	
	Reverse		V _{GS} =-20V, V _{DS} =0V			-100	nA	
ON CHARACTERISTICS								
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	2	2.8	4	V	
Static Drain-Source On-State Resistance		R _{DS(ON)}	V_{GS} =10V, I_D =80A		5.1	7	mΩ	
DYNAMIC PARAMETERS								
Input Capacitance		C_{ISS}	V _{GS} =0V, V _{DS} =25V, f=1.0MHz		3565		pF	
Output Capacitance		Coss			1310		pF	
Reverse Transfer Capacitance		C_{RSS}			395		pF	
SWITCHING PARAMETERS								
Total Gate Charge at 20V		Q_{G}			207	269	nC	
Gate to Source Charge		Q_GS	V_{DD} =30V, I_{D} =80A, R_{L} =0.4 Ω		17.2		nC	
Gate to Drain Charge		Q_GD			52		nC	
Turn-ON Delay Time		$t_{D(ON)}$			12		ns	
Rise Time		t_R	V_{DD} =30V, I_{D} =80A, R_{L} =0.4 Ω ,		34		ns	
Turn-OFF Delay Time		t _{D(OFF)}	V _{GS} =10V, R _{GS} =2.5 Ω		37		ns	
Fall-Time		t _F			23		ns	
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS								
Drain-Source Diode Forward Voltage		V_{SD}	I _{SD} =80A		0.9	1.25	V	

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