

N-CHANNEL ENHANCEMENT MODE POWER MOSFET

■ DESCRIPTION

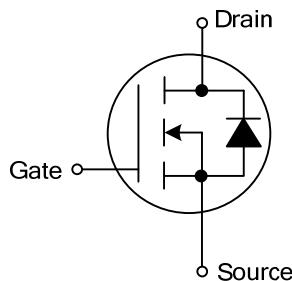
The UTC **25N10** is an N-channel enhancement mode power MOSFET and it uses UTC's perfect technology to provide designers with fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

It is generally suitable for all commercial-industrial applications and DC/DC converters requiring low voltage.

■ FEATURES

- * Single Drive Requirement
- * Low Gate Charge
- * RoHS Compliant

■ SYMBOL



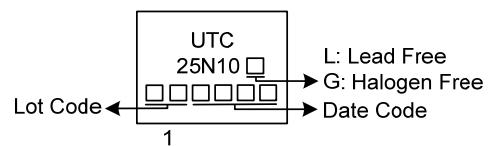
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
25N10L-TA3-T	25N10G-TA3-T	TO-220	G	D	S	Tube
25N10L-TF1-T	25N10G-TF1-T	TO-220F1	G	D	S	Tube
25N10L-TF2-T	25N10G-TF2-T	TO-220F2	G	D	S	Tube
25N10L-TF3-T	25N10G-TF3-T	TO-220F	G	D	S	Tube
25N10L-TM3-T	25N10G-TM3-T	TO-251	G	D	S	Tube
25N10L-TN3-R	25N10G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

25N10G-TA3-T 	(1) T: Tube, R: Tape Reel (2) TA3: TO-220, TF1: TO-220F1, TF2: TO-220F2, TF3: TO-220F, TM3: TO-251, TN3: TO-252 (3) G: Halogen Free and Lead Free, L: Lead Free
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■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_c=25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain Source Voltage		V_{DSS}	100	V
Gate Source Voltage		V_{GSS}	± 20	V
Continuous Drain Current ($V_{GS}=10\text{V}$)	$T_c=25^\circ\text{C}$	I_D	23	A
	$T_c=100^\circ\text{C}$	I_D	14.6	A
Pulsed Drain Current (Note 2)		I_{DM}	80	A
Single Pulsed Avalanche Energy (Note 3)		E_{AS}	480	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.58	V/ns
Power Dissipation	TO-220	P_D	125	W
	TO-220F/TO-220F1		41	
	TO-220F2		50	
	TO-251/TO-252			
Operating Junction Temperature	T_J		-55 ~ +150	°C
Storage Temperature	T_{STG}		-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L=10\text{mH}$, $I_{AS}=9.8\text{A}$, $V_{DD}=50\text{V}$, $R_G=25\ \Omega$, Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 25\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F	θ_{JA}	62.5	°C/W
	TO-220F1/TO-220F2			
	TO-251/TO-252		100	
Junction to Case	TO-220	θ_{JC}	1	°C/W
	TO-220F/TO-220F1		3.04	
	TO-220F2			
	TO-251/TO-252		2.5 (Note)	

Note: The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

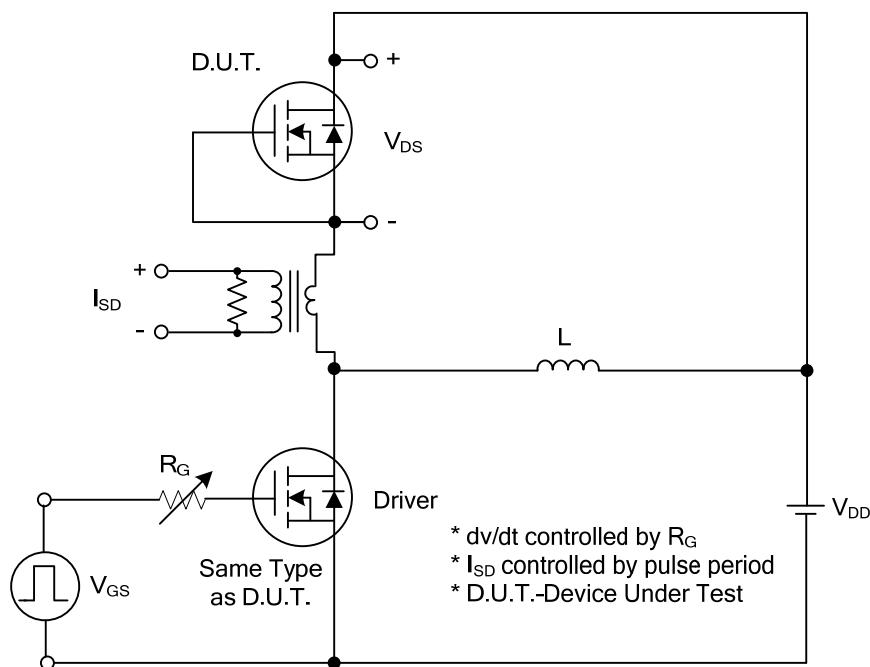
■ ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=1\text{mA}$	100			V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=100\text{V}, V_{\text{GS}}=0\text{V}, T_J=25^\circ\text{C}$			25	μA
		$V_{\text{DS}}=80\text{V}, V_{\text{GS}}=0\text{V}, T_J=150^\circ\text{C}$			100	μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}$			± 100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{\text{GS(TH)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	2.0		4.0	V
Static Drain-Source On-Resistance (Note)	$R_{\text{DS(ON)}}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=16\text{A}$			80	$\text{m}\Omega$
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, f=1.0\text{MHz}$		762		pF
Output Capacitance	C_{OSS}			196		pF
Reverse Transfer Capacitance	C_{RSS}			22		pF
Gate Resistance	R_{G}	$f=1.0\text{MHz}$		1.5	2.3	Ω
SWITCHING PARAMETERS						
Total Gate Charge (Note)	Q_{G}	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=100\text{V}, I_{\text{D}}=25\text{A}$		28.5		nC
Gate Source Charge	Q_{GS}			6.0		nC
Gate Drain Charge	Q_{GD}			7.5		nC
Turn-ON Delay Time ¹	$t_{\text{D(ON)}}$	$V_{\text{DD}}=100\text{V}, I_{\text{D}}=25\text{A}, R_{\text{G}}=25\Omega$ $V_{\text{GS}}=10\text{V}, R_{\text{D}}=4.0\Omega$		16		ns
Turn-ON Rise Time	t_{R}			26		ns
Turn-OFF Delay Time	$t_{\text{D(OFF)}}$			34		ns
Turn-OFF Fall-Time	t_{F}			19		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I_{S}				25	A
Maximum Body-Diode Pulsed Current	I_{SM}				50	A
Drain-Source Diode Forward Voltage (Note)	V_{SD}	$I_{\text{S}}=25\text{A}, V_{\text{GS}}=0\text{V}$			1.3	V
Reverse Recovery Time	t_{rr}	$I_{\text{S}}=25\text{A}, V_{\text{GS}}=0\text{V},$ $dI/dt=100\text{A}/\mu\text{s}$		96		ns
Reverse Recovery Charge	Q_{rr}			342		nC

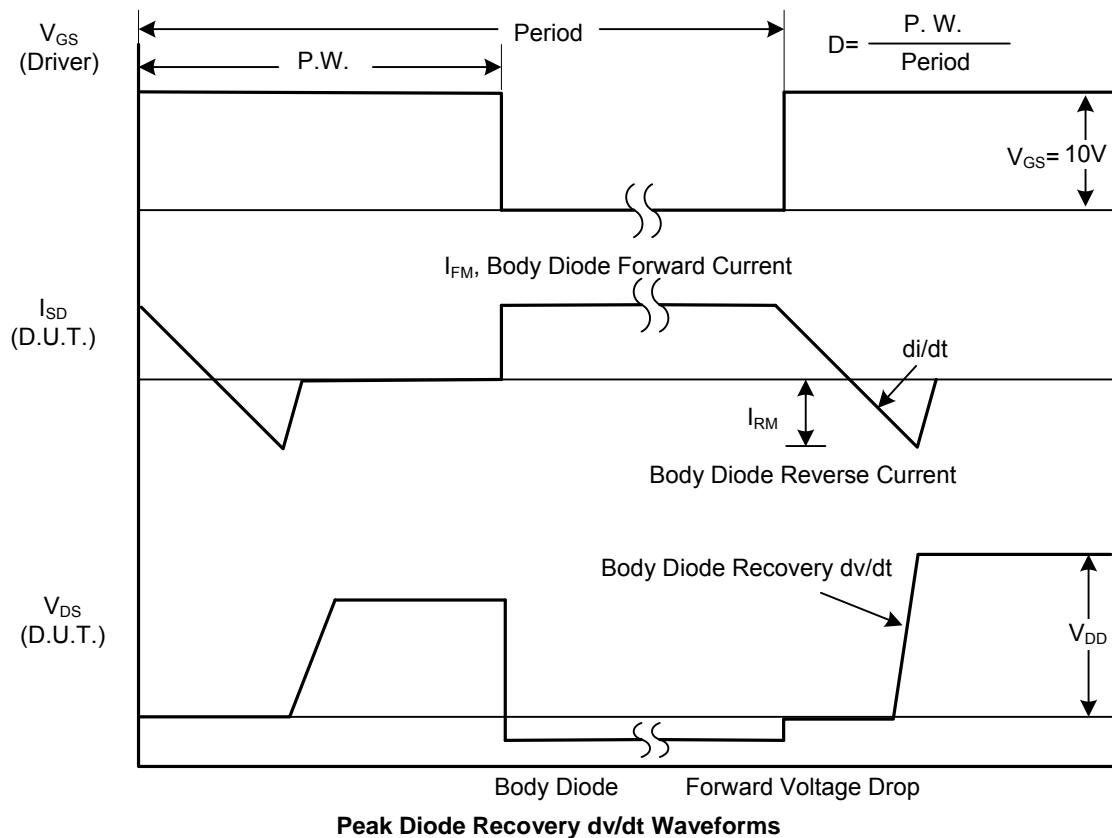
Notes: 1. Pulse Test : Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating ambient temperature.

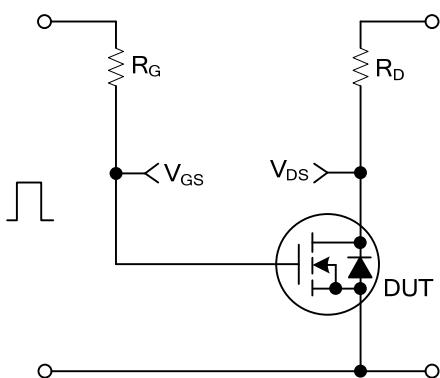
■ TEST CIRCUITS AND WAVEFORMS



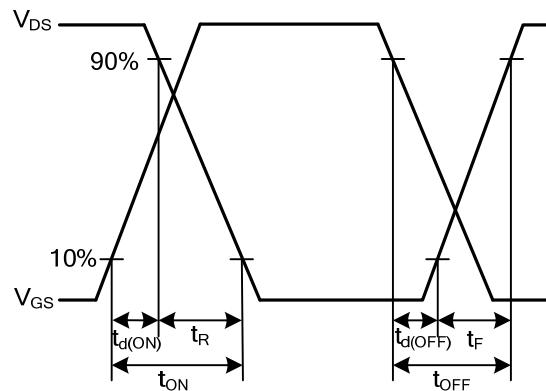
Peak Diode Recovery dv/dt Test Circuit



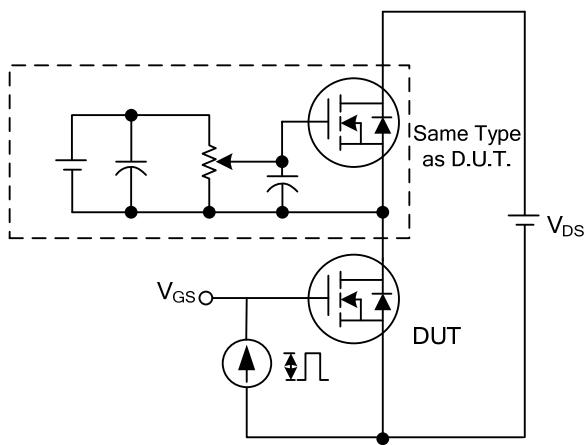
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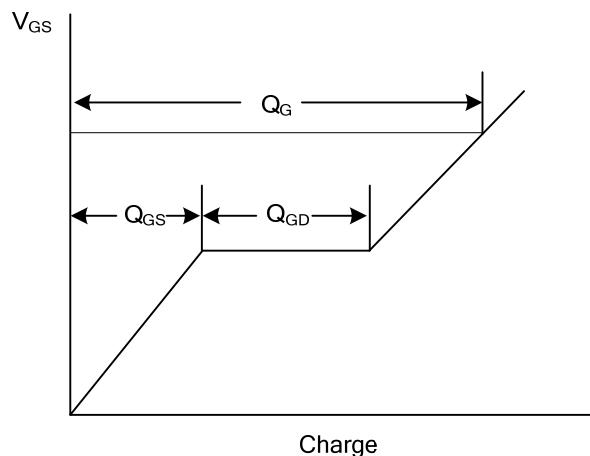
Latching Test Circuit



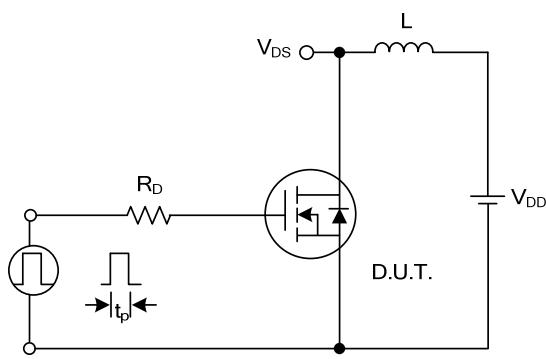
Switching Waveforms



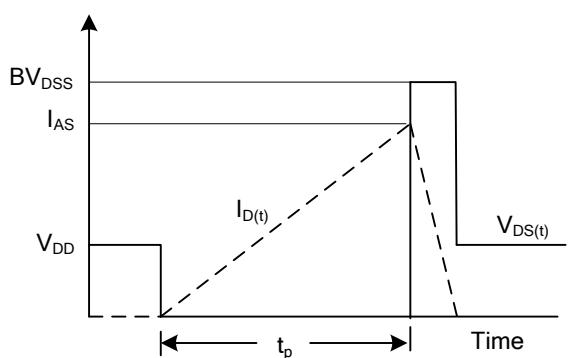
Gate Charge Test Circuit



Gate Charge Waveform

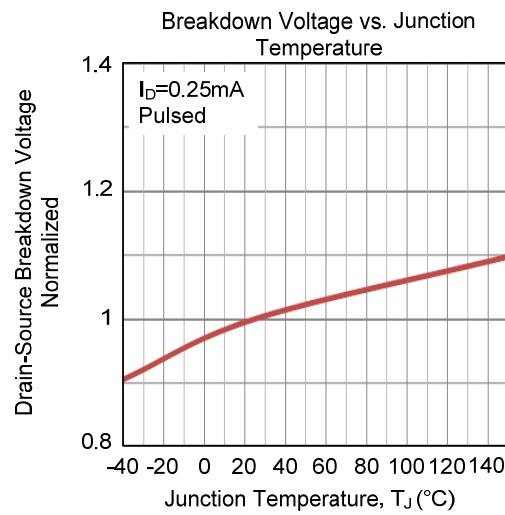
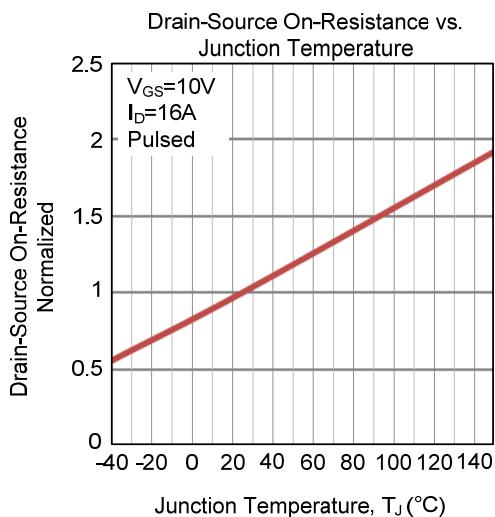
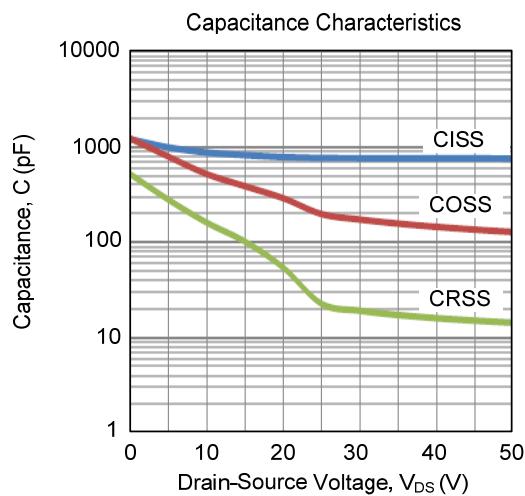
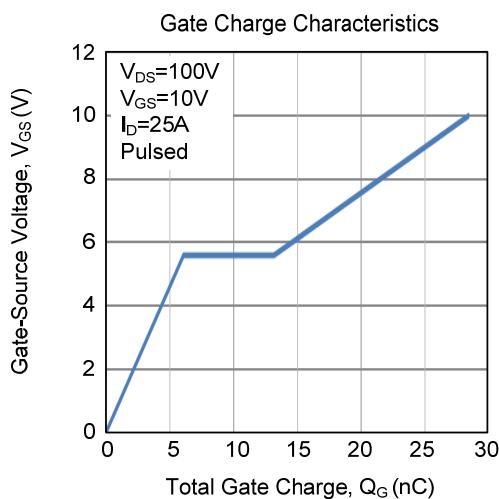
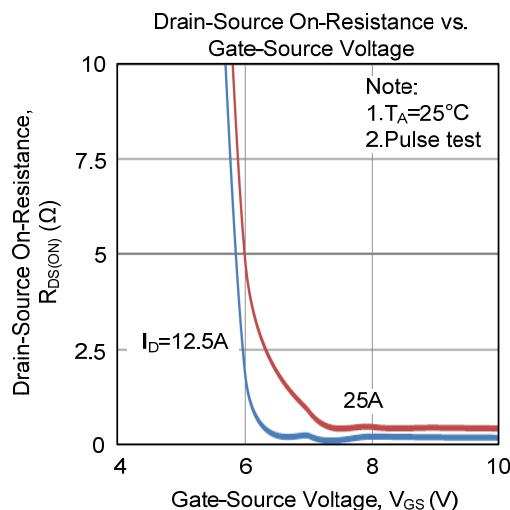
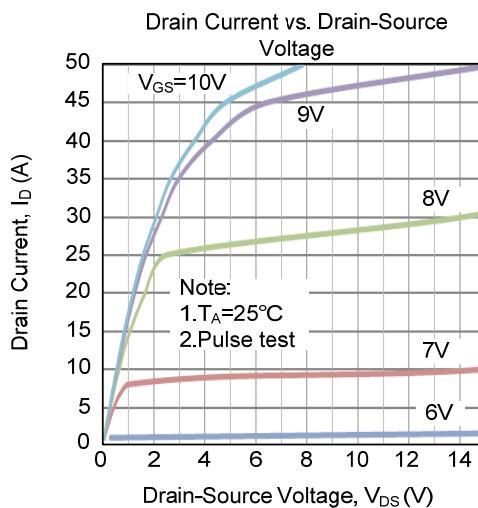


Unclamped Inductive Switching Test Circuit

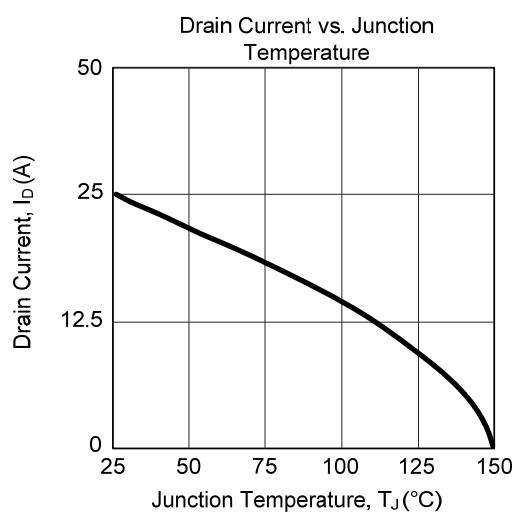
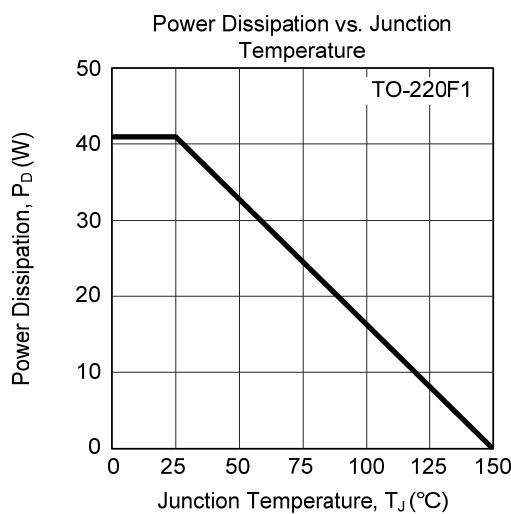
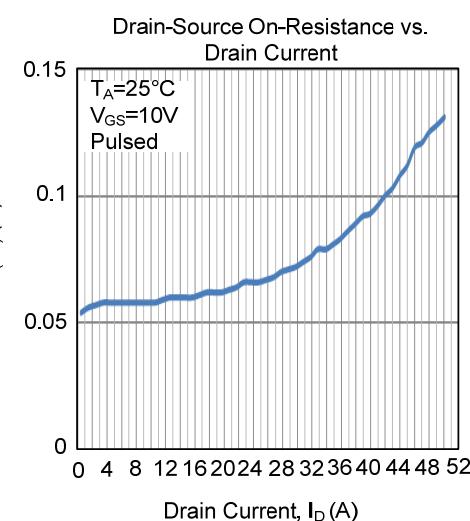
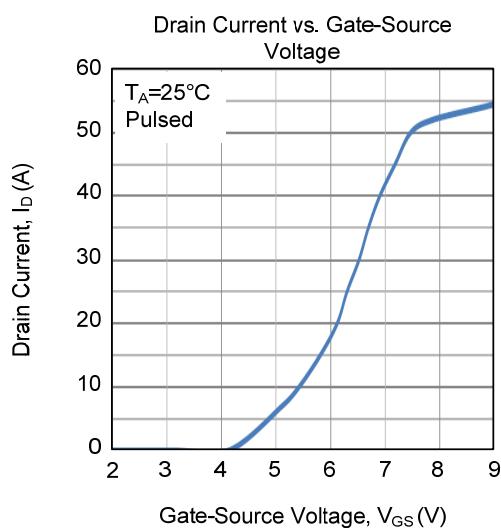
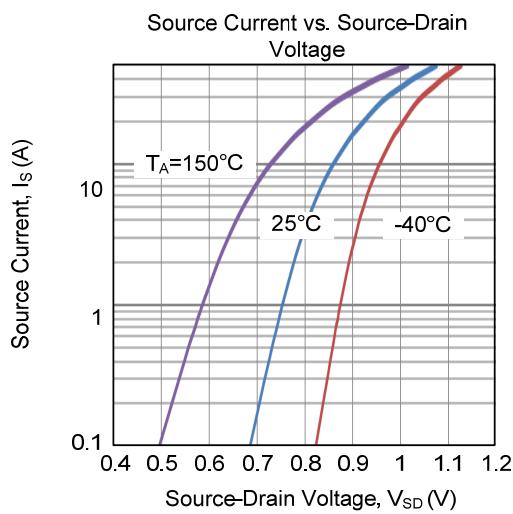
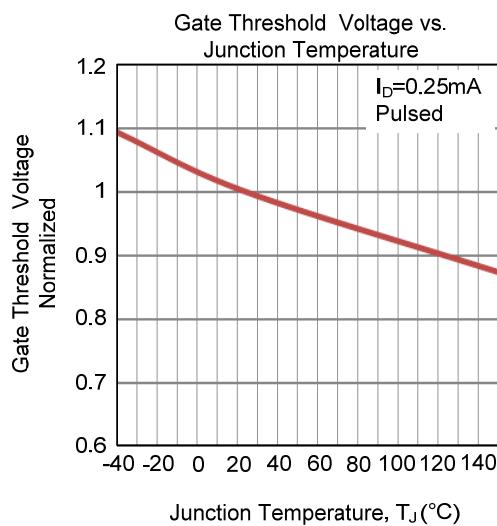


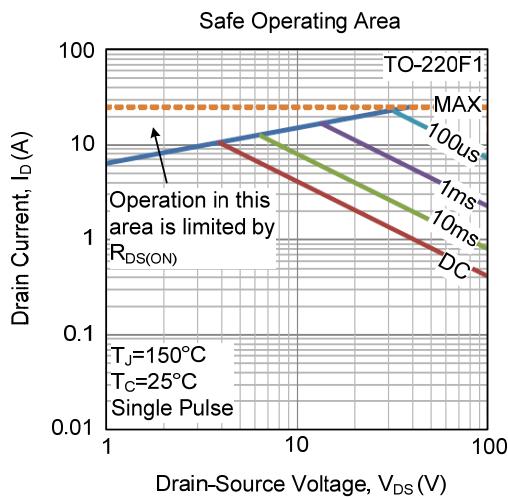
Unclamped Inductive Switching Waveforms

■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)

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