



## 7P20

Power MOSFET

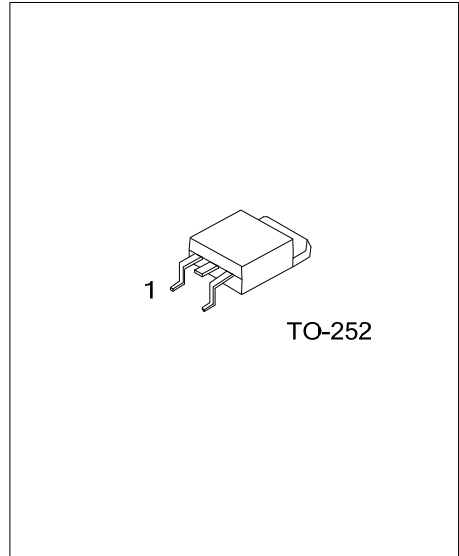
### -7.0A, -200V P-CHANNEL POWER MOSFET

#### DESCRIPTION

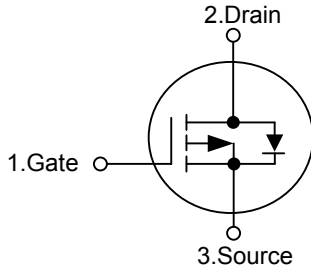
The **7P20** uses advanced proprietary, planar stripe, DMOS technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with low gate voltages. This device is suitable for use as a load switch or in PWM applications. They are also well suited for high efficiency switching DC/DC converters.

#### FEATURES

- \*  $R_{DS(ON)} \leq 0.69 \Omega @ V_{GS} = -10 V, I_D = -3.5A$
- \* Fast Switching Capability
- \* Avalanche Energy Specified
- \* Improved dv/dt Capability, High Ruggedness



#### SYMBOL



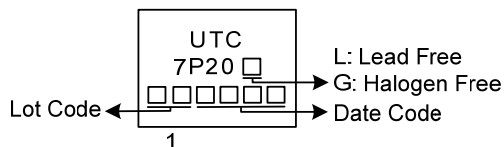
#### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
7P20L-TN3-R	7P20G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>7P20G-TN3-R</p> <p>(1)Packing Type</p> <p>(2)Package Type</p> <p>(3)Green Package</p>	<p>(1) R: Tape Reel</p> <p>(2) TN3: TO-252</p> <p>(3) G: Halogen Free and Lead Free L: Lead Free</p>
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#### MARKING



■ ABSOLUTE MAXIMUM RATINGS ( $T_C = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	$V_{DSS}$	-200	V
Gate-Source Voltage	$V_{GSS}$	$\pm 30$	V
Continuous Drain Current	$I_D$	-7.0	A
Pulsed Drain Current (Note 2)	$I_{DM}$	-14	A
Single Pulsed Avalanche Energy (Note 3)	$E_{AS}$	205	mJ
Peak Diode Recovery dv/dt (Note 4)	dv/dt	7.6	V/ns
Power Dissipation	$T_A = 25^\circ\text{C}$	$P_D$	1.25
	$T_C = 25^\circ\text{C}$		50
Junction Temperature	$T_J$	+150	$^\circ\text{C}$
Storage Temperature	$T_{STG}$	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3.  $L=30\text{mH}$ ,  $I_{AS}=-3.7\text{A}$ ,  $V_{DD}=-50\text{V}$ ,  $R_G=25\Omega$ , Starting  $T_J=25^\circ\text{C}$ .

4.  $I_{SD}\leq 7.0\text{A}$ ,  $di/dt\leq 200\text{A}/\mu\text{s}$ ,  $V_{DD}\leq BV_{DSS}$ , Starting  $T_J=25^\circ\text{C}$ .

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	$\theta_{JA}$	100	$^\circ\text{C}/\text{W}$
Junction to Case	$\theta_{JC}$	2.5	$^\circ\text{C}/\text{W}$

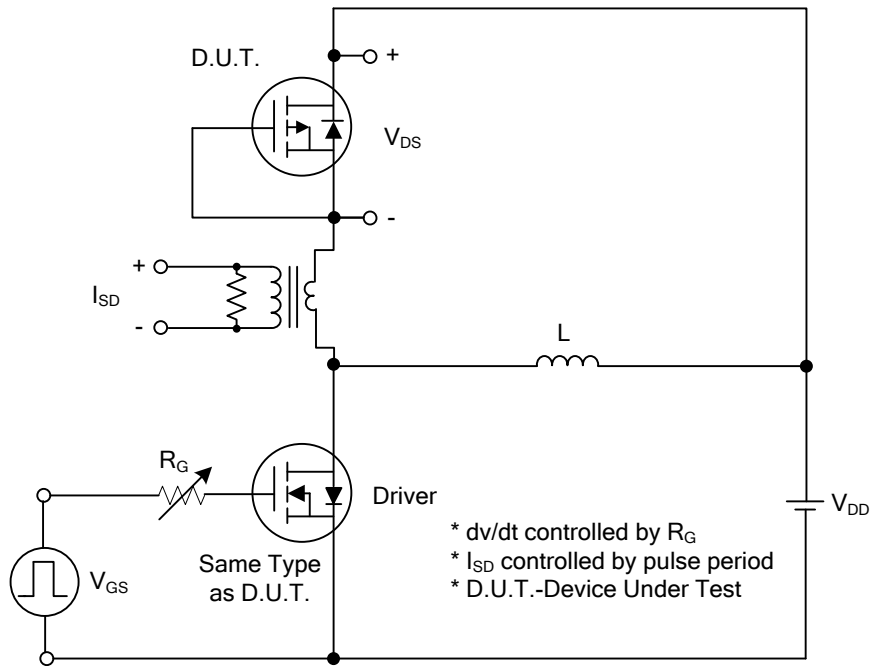
■ ELECTRICAL CHARACTERISTICS ( $T_C=25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0\text{V}$ , $I_D=-250\mu\text{A}$	-200			V
Drain-Source Leakage Current	$I_{DSS}$	$V_{DS}=-200\text{V}$ , $V_{GS}=0\text{V}$			-1	$\mu\text{A}$
Gate-Source Leakage Current	$I_{GSS}$	$V_{DS}=0\text{V}$ , $V_{GS}=\pm 30\text{V}$			$\pm 100$	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}$ , $I_D=-250\mu\text{A}$	-2.0		-4.0	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}=-10\text{V}$ , $I_D=-3.5\text{A}$		0.54	0.69	$\Omega$
<b>DYNAMIC PARAMETERS</b>						
Input Capacitance	$C_{ISS}$	$V_{DS}=-25\text{V}$ , $V_{GS}=0\text{V}$ , $f=1.0\text{MHz}$		941		pF
Output Capacitance	$C_{OSS}$			140		pF
Reverse Transfer Capacitance	$C_{RSS}$			24.8		pF
<b>SWITCHING PARAMETERS</b>						
Total Gate Charge	$Q_G$	$V_{DS}=-160\text{V}$ , $V_{GS}=-10\text{V}$ , $I_D=-7.0\text{A}$ (Note 1, 2)		22.9		nC
Gate Source Charge	$Q_{GS}$			5.2		nC
Gate Drain Charge	$Q_{GD}$			8		nC
Turn-ON Delay Time	$t_{D(ON)}$	$V_{DD}=-100\text{V}$ , $I_D=-7.0\text{A}$ , $R_G=25\Omega$ (Note 1, 2)		12		ns
Turn-ON Rise Time	$t_R$			22		ns
Turn-OFF Delay Time	$t_{D(OFF)}$			64		ns
Turn-OFF Fall-Time	$t_F$			28		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Maximum Body-Diode Continuous Current	$I_S$				-7	A
Maximum Pulsed Drain-Source Diode Forward Current	$I_{SM}$				-14	A
Diode Forward Voltage	$V_{SD}$	$I_S=-7.0\text{A}$ , $V_{GS}=0\text{V}$			-5.0	V
Body Diode Reverse Recovery Time	$t_{rr}$	$V_{GS}=0\text{V}$ , $I_S=-7.0\text{A}$		122		ns
Body Diode Reverse Recovery Charge	$Q_{rr}$	$di_F/dt=100\text{A/s}$ (Note 1)		0.59		$\mu\text{C}$

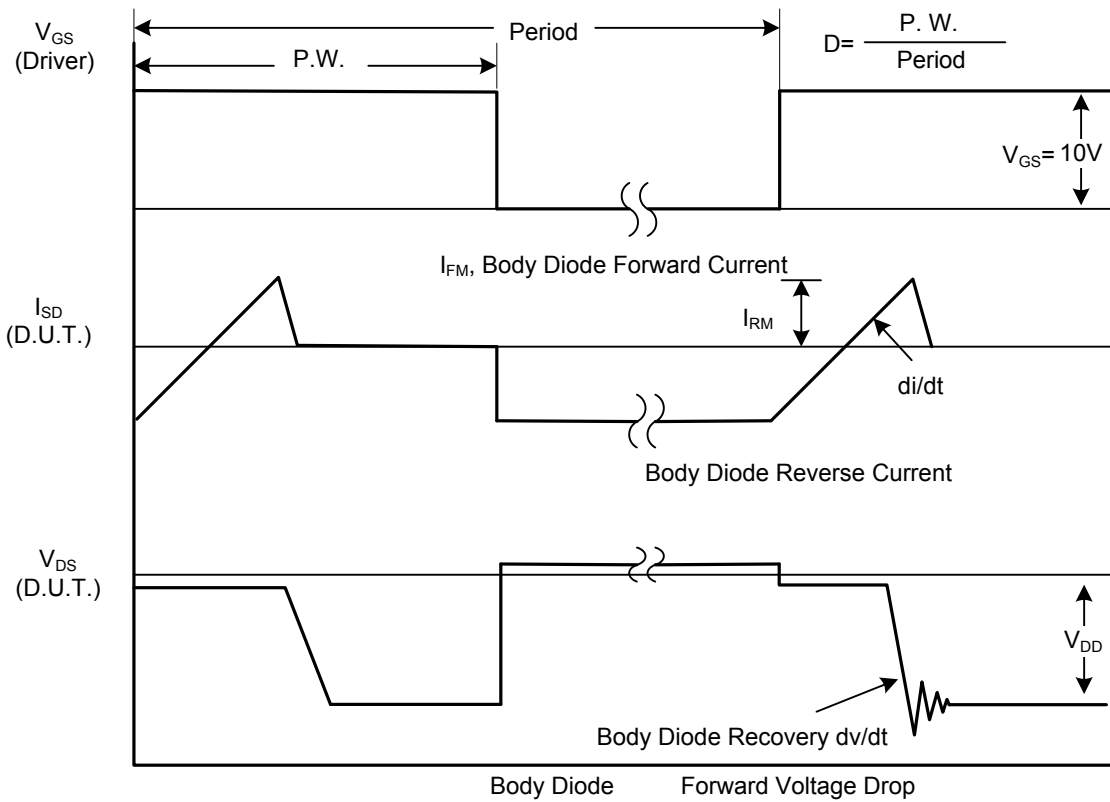
Notes: 1. Pulse Test : Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$ .

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS



Peak Diode Recovery  $dv/dt$  Test Circuit



Peak Diode Recovery  $dv/dt$  Waveforms

■ TEST CIRCUITS AND WAVEFORMS (Cont.)

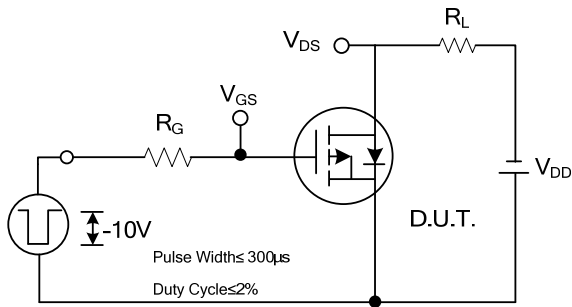


Fig. 2A Switching Test Circuit

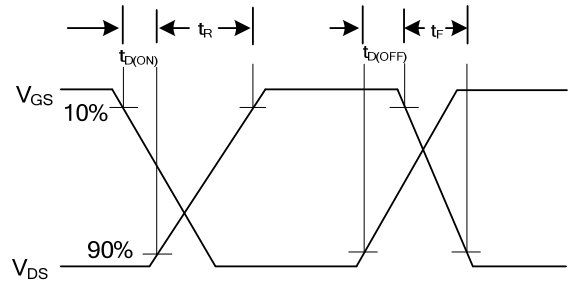


Fig. 2B Switching Waveforms

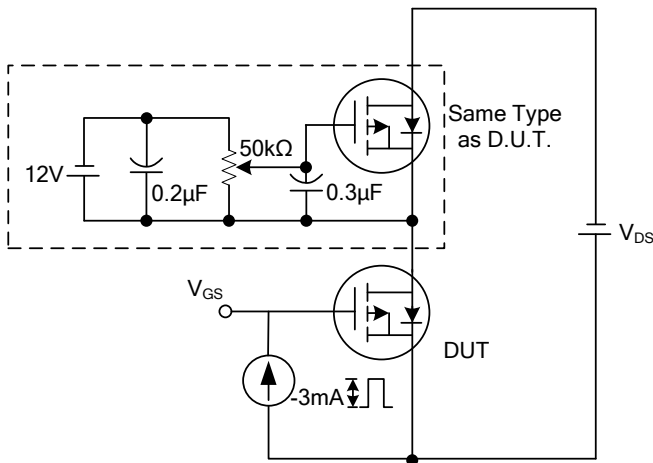


Fig. 3A Gate Charge Test Circuit

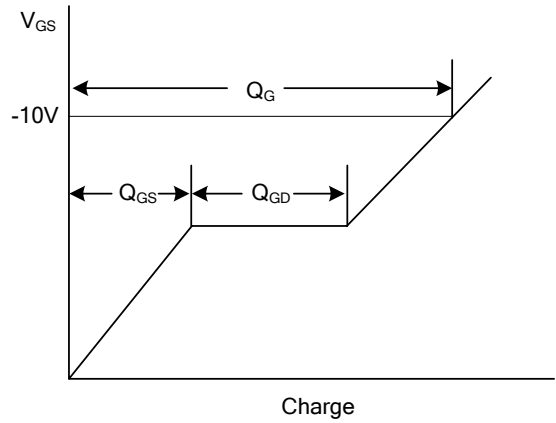


Fig. 3B Gate Charge Waveform

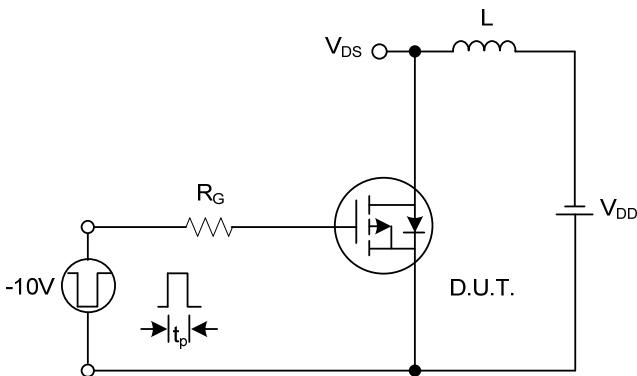


Fig. 4A Unclamped Inductive Switching Test Circuit

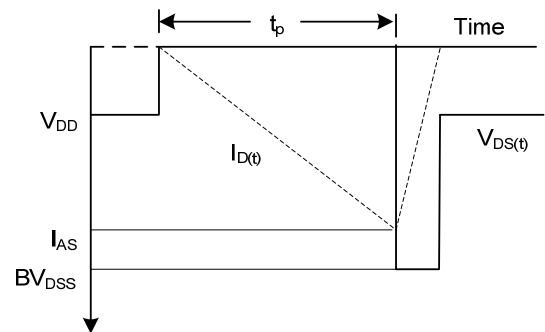
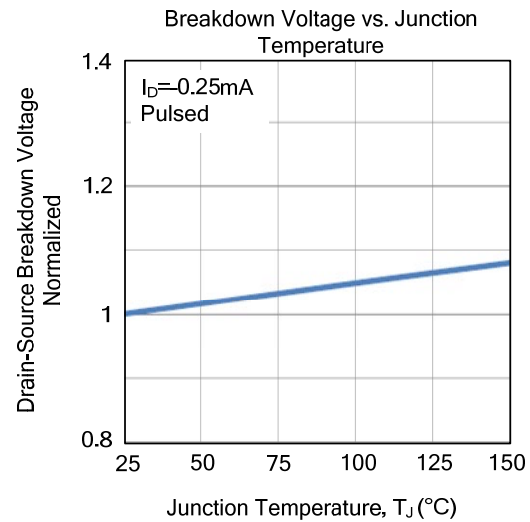
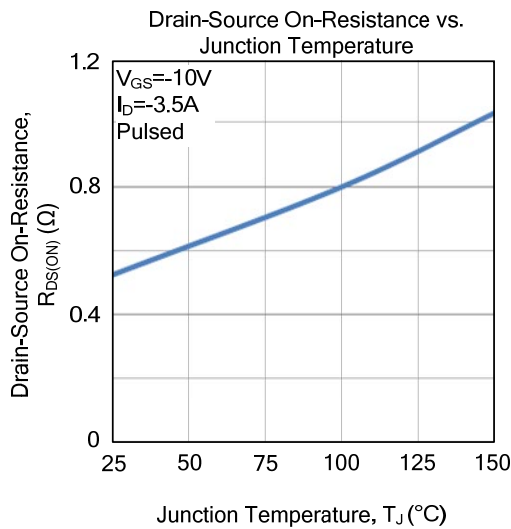
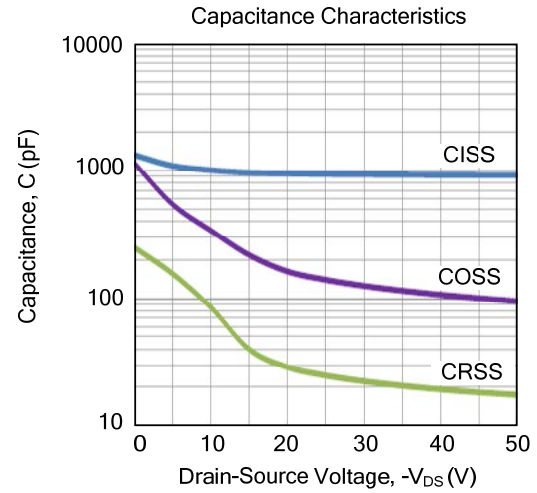
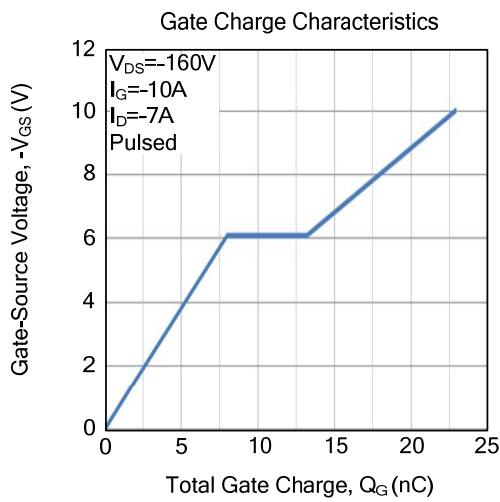
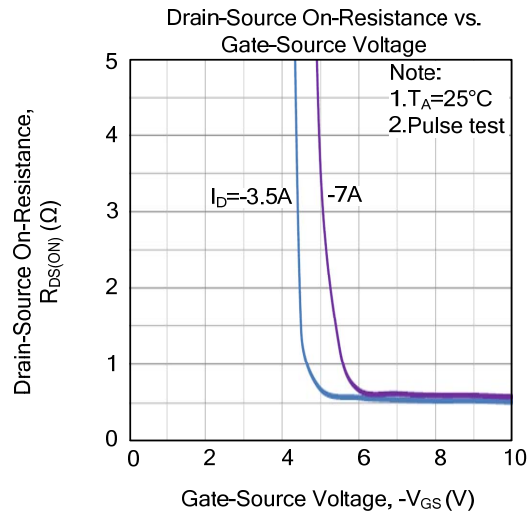
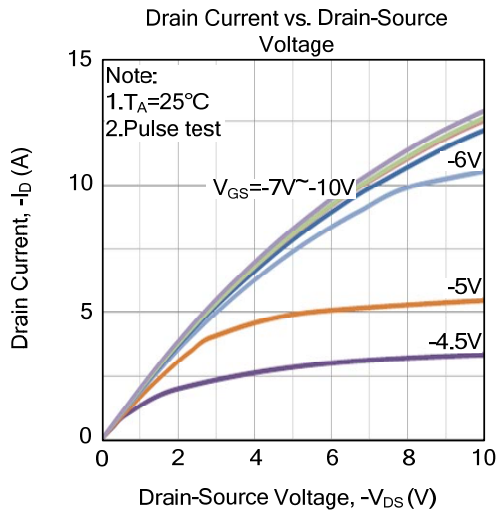
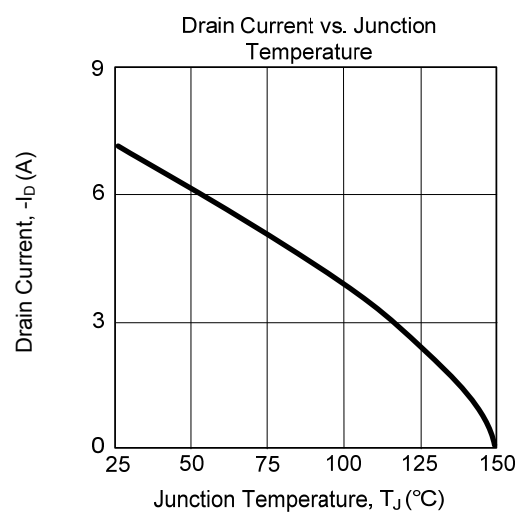
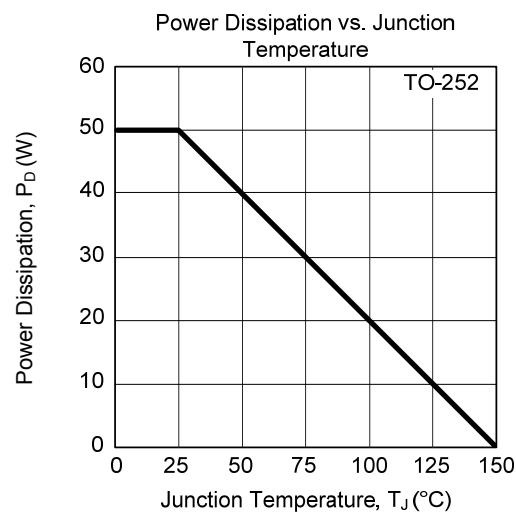
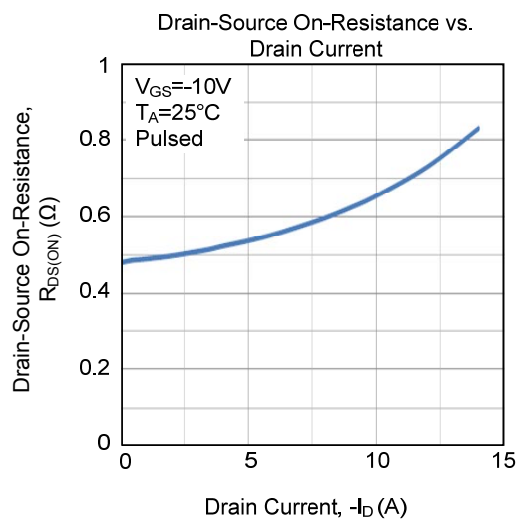
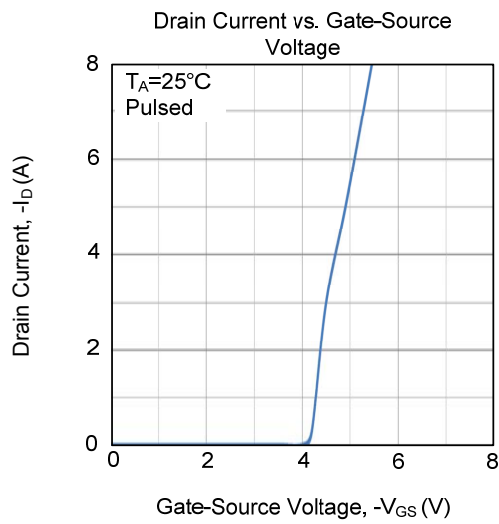
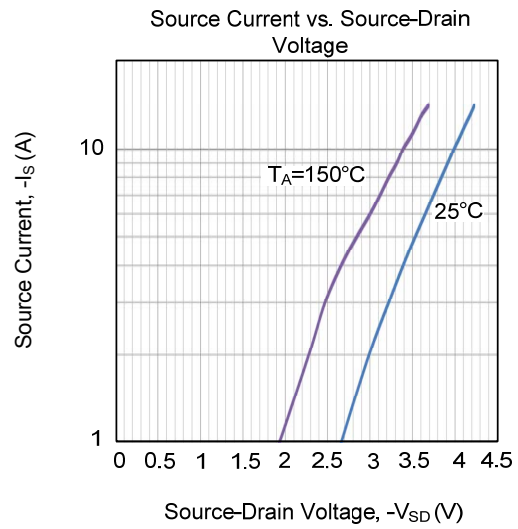
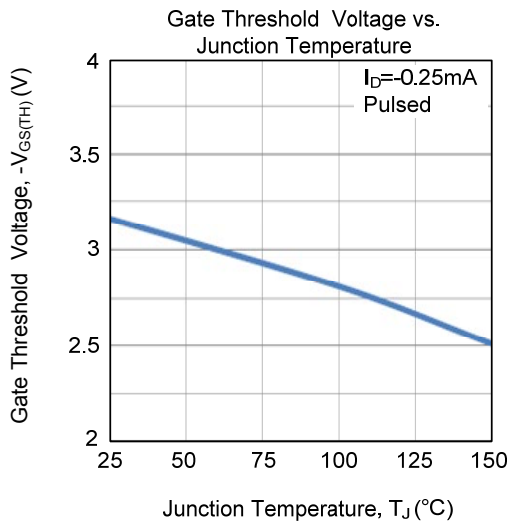


Fig. 4B Unclamped Inductive Switching Waveforms

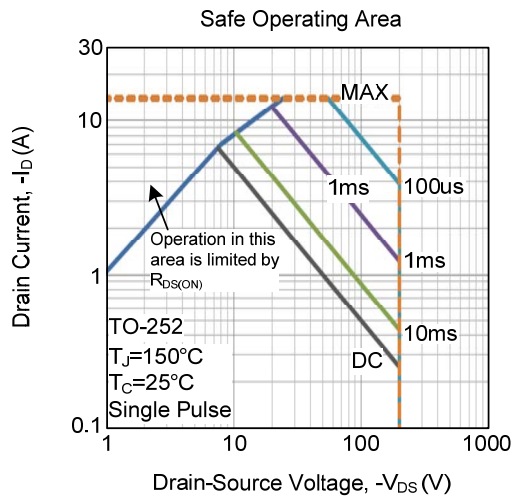
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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