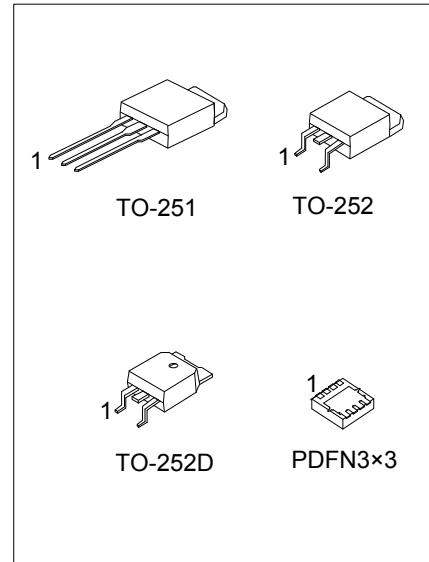
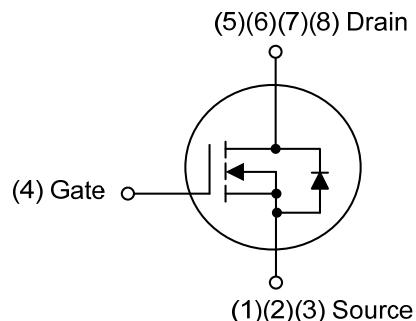
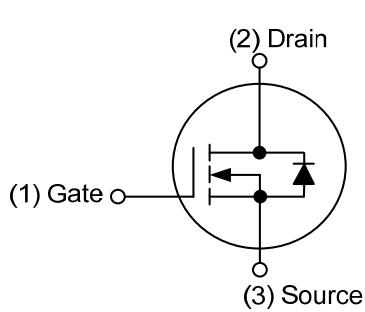


UT50N03**Power MOSFET****50A, 30V N-CHANNEL
POWER MOSFET****■ FEATURES**

- * $R_{DS(ON)} \leq 14 \text{ m}\Omega$ @ $V_{GS}=10\text{V}$, $I_D=30\text{A}$
- * Low capacitance
- * Optimized gate charge
- * Fast switching capability
- * Avalanche energy specified

**■ SYMBOL**

TO-251 / TO-252 / TO-252D

PDFN3x3

■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
UT50N03L-TM3-T	UT50N03G-TM3-T	TO-251	G	D	S	-	-	-	-	-	Tube
UT50N03L-TN3-R	UT50N03G-TN3-R	TO-252	G	D	S	-	-	-	-	-	Tape Reel
UT50N03L-TND-R	UT50N03G-TND-R	TO-252D	G	D	S	-	-	-	-	-	Tape Reel
UT50N03L-P3030-R	UT50N03G-P3030-R	PDFN3x3	S	S	S	G	D	D	D	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

UT50N03G-TM3-T	(1)Packing Type (2)Package Type (3)Green Package 	(1) T: Tube, R: Tape Reel (2) TM3: TO-251, TN3: TO-252, TND: TO-252D, P3030: PDFN3x3 (3) G: Halogen Free and Lead Free, L: Lead Free
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■ MARKING

TO-251 / TO-252 / TO-252D	PDFN3×3
<p>Diagram illustrating marking for TO-251, TO-252, and TO-252D packages. The marking area contains: - Top row: UTC (top left), UT50N03 (center), and a small square (top right). - Middle row: A series of six small squares representing a lot code. - Bottom row: A single digit '1' (left) and a date code consisting of four squares (right). Annotations indicate: - 'Lot Code' points to the middle row of squares. - 'Date Code' points to the bottom row of squares. - 'L: Lead Free' and 'G: Halogen Free' are noted near the top right square.</p>	<p>Diagram illustrating marking for PDFN3×3 package. The marking area contains: - Top row: UT (top left), 50N03 (center), and a small square (top right). - Middle row: A single dot (left) and a date code consisting of four squares (right). - Annotations indicate: - 'Date Code' points to the bottom row of squares.</p>

■ ABSOLUTE MAXIMUM RATINGS ($T_J = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	30	V
Gate-Source Voltage		V_{GSS}	± 20	V
Continuous Drain Current	TO-251/TO-252	I_D	50	A
	TO-252D		30	
	PDFN3x3			
Pulsed Drain Current (Note 2)		I_{DM}	100	A
Single Pulsed Avalanche Energy (Note 3)		E_{AS}	66	mJ
Power Dissipation	TO-251/TO-252	P_D	40	W
	TO-252D		24	W
	PDFN3x3			
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. L = 0.05mH, $I_{AS} = 51\text{A}$, $V_{DD} = 25\text{V}$, $R_G = 25 \Omega$, Starting $T_J = 25^\circ\text{C}$.

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient (Note 3)	TO-251/TO-252	θ_{JA}	50	$^\circ\text{C/W}$
	TO-252D		60	$^\circ\text{C/W}$
	PDFN3x3			
Junction to Case	TO-251/TO-252	θ_{JC}	3.13	$^\circ\text{C/W}$
	TO-252D		5.2	$^\circ\text{C/W}$
	PDFN3x3			

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

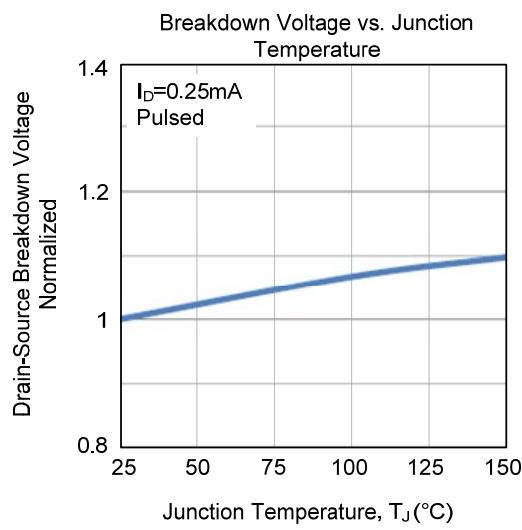
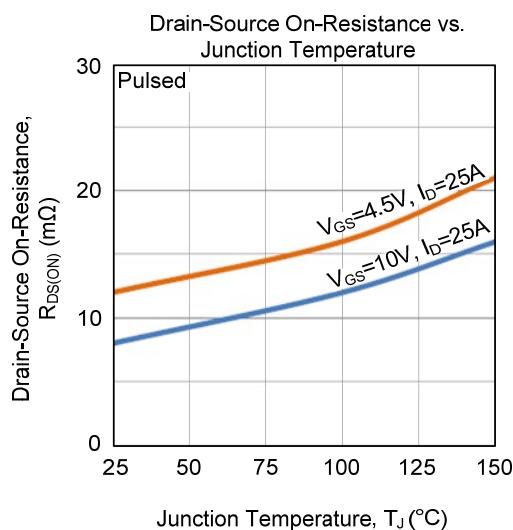
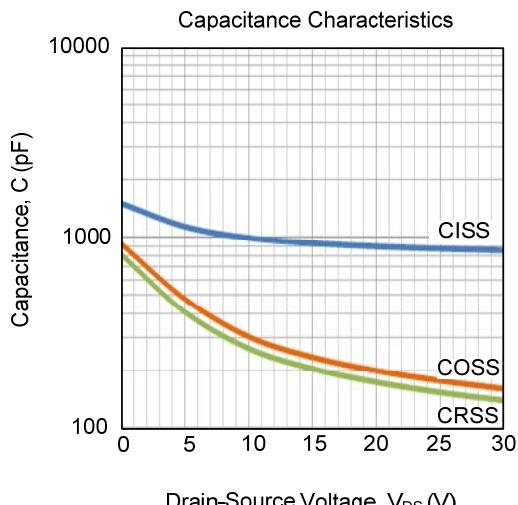
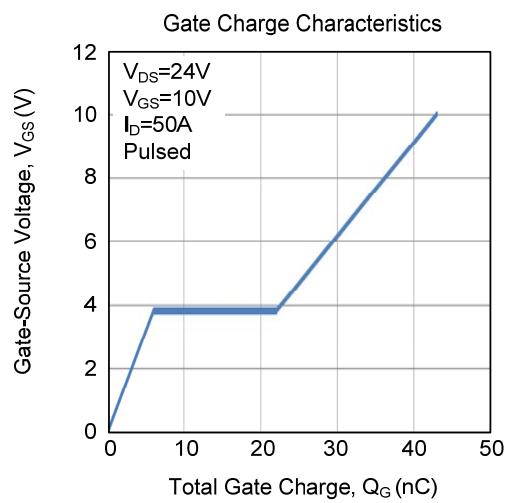
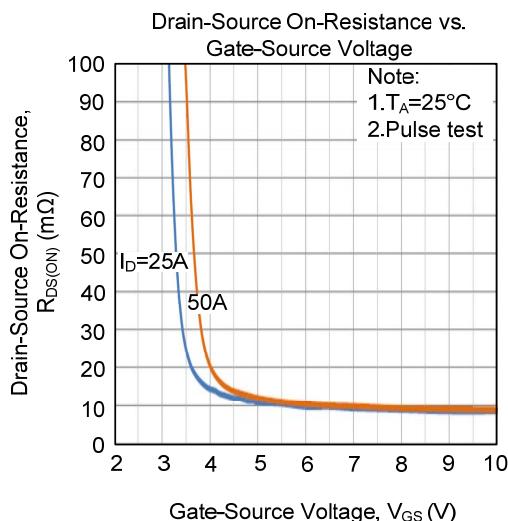
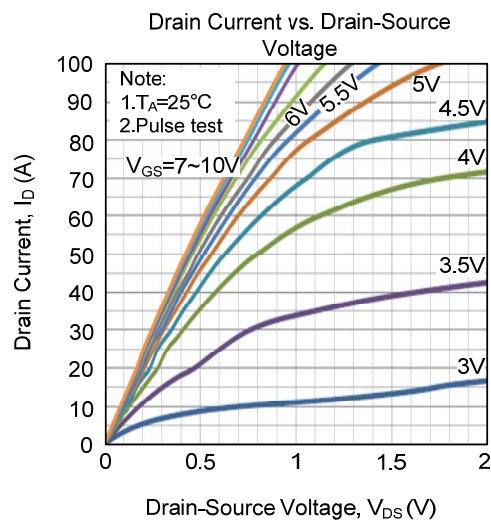
■ ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	30			V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}$			1.5	μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=\pm20\text{V}$			±100	nA
ON CHARACTERISTICS						
Gate-Threshold Voltage	$V_{\text{GS(TH)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.0	1.7	2.5	V
Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=25\text{A}$			10	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=25\text{A}$			15	$\text{m}\Omega$
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$		940		pF
Output Capacitance	C_{OSS}			235		pF
Reverse Transfer Capacitance	C_{RSS}			200		pF
SWITCHING PARAMETERS						
Total Gate Charge	Q_G	$V_{\text{DS}}=24\text{V}, V_{\text{GS}}=10\text{V}, I_{\text{D}}=50\text{A}$		43		nC
Gate-to-Source Charge	Q_{GS}			6		nC
Gate-to-Drain Charge	Q_{GD}			16		nC
Turn-ON Delay Time	$t_{\text{D(ON)}}$	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=15\text{V}, I_{\text{D}}=50\text{A}, R_G=3\Omega$		8		ns
Turn-ON Rise Time	t_R			17		ns
Turn-OFF Delay Time	$t_{\text{D(OFF)}}$			25		ns
Turn-OFF Fall-Time	t_F			23		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Continuous Drain-Source Diode Forward Current	I_S				45	A
Drain-Source Diode Forward Voltage	V_{SD}	$I_S=50\text{A}, V_{\text{GS}}=0\text{V}$			1.4	V
Reverse Recovery Time	t_{rr}	$I_S=30\text{A}, V_{\text{GS}}=0\text{V},$		164		ns
Reverse Recovery Charge	Q_{rr}	$dI/dt=100\text{A}/\mu\text{s}$		300		nC

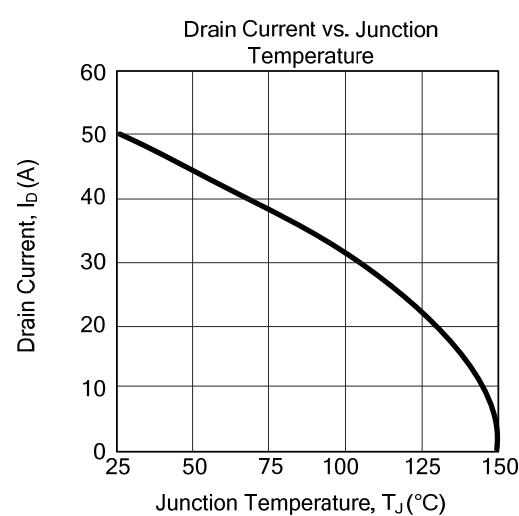
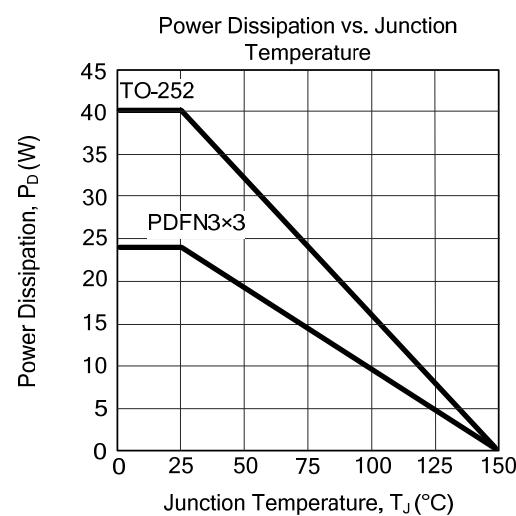
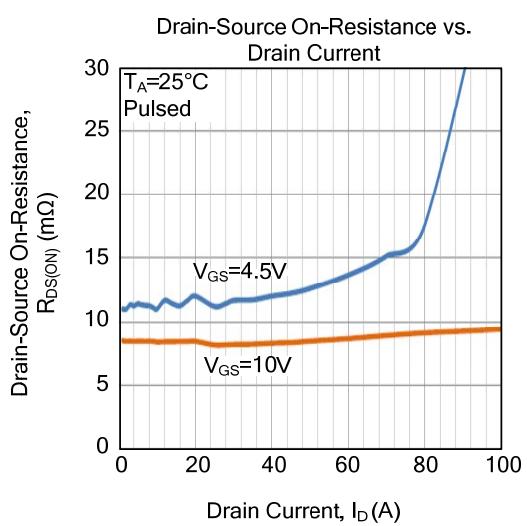
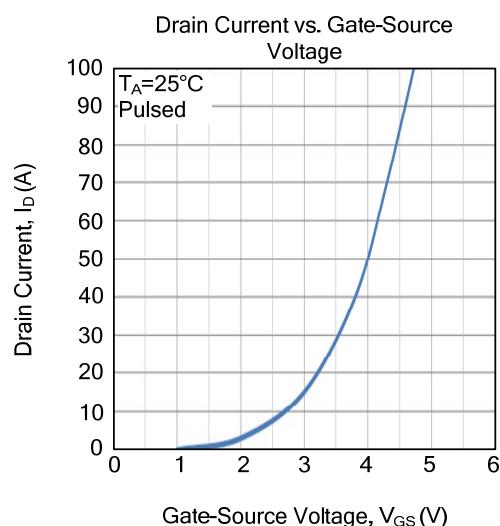
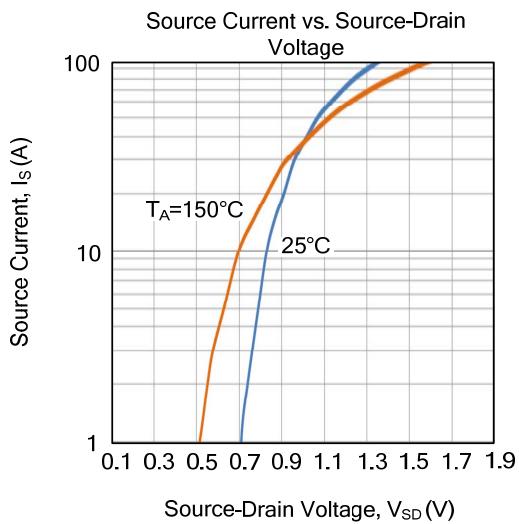
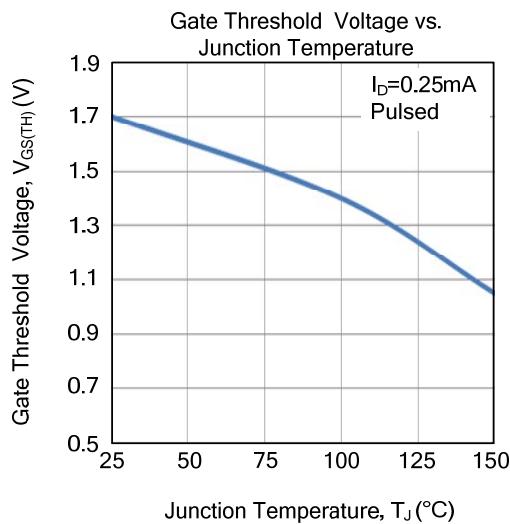
Notes: 1. Pulse width limited by $T_{\text{J(MAX)}}$

2. Pulse Test: Pulse Width≤300μs, Duty Cycle≤2%

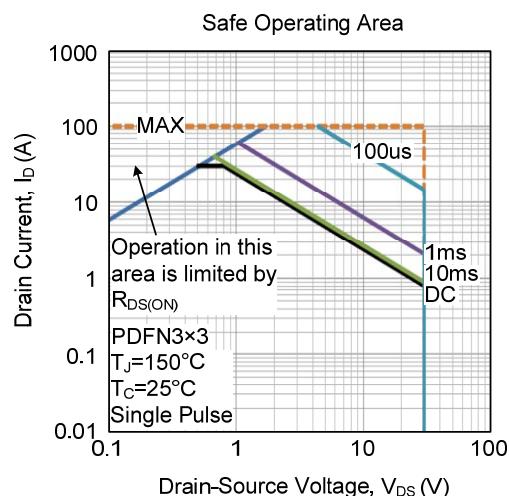
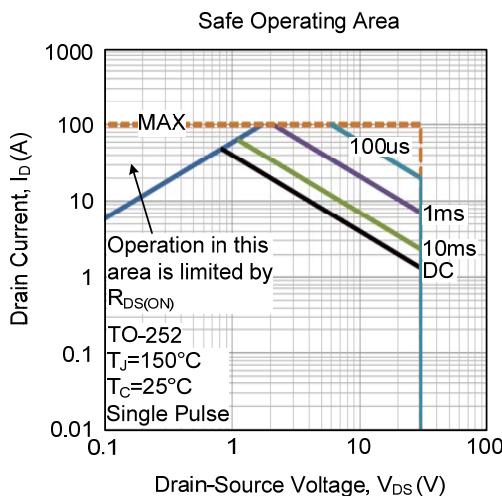
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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