



UTT220N03

Power MOSFET

**220A, 30V N-CHANNEL
ENHANCEMENT MODE POWER
MOSFET**

■ DESCRIPTION

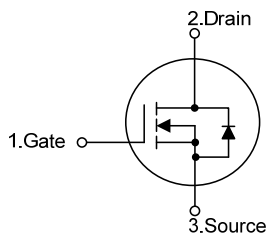
The UTC **UTT220N03** is a N-channel MOSFET, using UTC's advanced technology to provide customers with a minimum on-state resistance and superior switching performance.

The UTC **UTT220N03** is generally applied in DC to DC convertor or synchronous rectification

■ FEATURES

- * $R_{DS(ON)} < 2.4m\Omega @ V_{GS}=10V, I_D=80A$
- * Low Gate Charge (Typical 84nC)
- * Fast Switching
- * 100% Avalanche Tested
- * High Power and Current Handling Capability
- * RoHS Compliant

■ SYMBOL

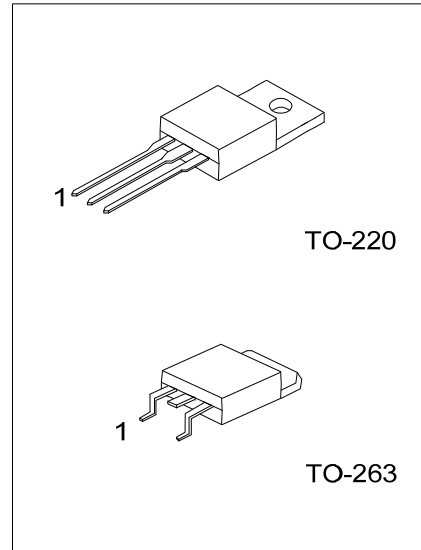


■ ORDERING INFORMATION

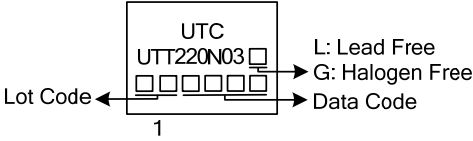
Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UTT220N03L-TA3-T	UTT220N03G-TA3-T	TO-220	G	D	S	Tube
UTT220N03L-TQ2-T	UTT220N03G-TQ2-T	TO-263	G	D	S	Tube
UTT220N03L-TQ2-R	UTT220N03G-TQ2-R	TO-263	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>UTT220N03G-TA3-T</p>	<p>(1) T: Tube, R: Tape Reel</p> <p>(2) TA3: TO-220, TQ2: TO-263</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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MARKING



■ ABSOLUTE MAXIMUM RATINGS (Note 6) ($T_C=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	V_{DSS}	30	V
Gate-Source Voltage	V_{GSS}	± 20	V
Drain Current	Continuous ($T_C=25^\circ\text{C}$)	I_D	220
	$T_C=100^\circ\text{C}$		170
	Pulsed (Note 1)	I_{DM}	876
Single Pulsed Avalanche Energy (Note 2)	E_{AS}	864	mJ
Peak Diode Recovery dv/dt (Note 3)	dv/dt	6.0	V/ns
Power Dissipation	$T_C=25^\circ\text{C}$	P_D	214
	$T_C=100^\circ\text{C}$		100
	Derate above 25°C		1.43
Junction Temperature	T_J	-55 ~ +175	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55 ~ +175	$^\circ\text{C}$

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.
 Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	62.5	$^\circ\text{C/W}$
Junction to Case	θ_{JC}	0.7	$^\circ\text{C/W}$

■ ELECTRICAL CHARACTERISTICS ($T_C=25^\circ\text{C}$, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D=250\mu\text{A}$, $V_{GS}=0\text{V}$, $T_C=25^\circ\text{C}$	30			V
Breakdown Voltage Temperature Coefficient	$\Delta BV_{DSS}/\Delta T_J$	Reference to 25°C , $I_D=250\mu\text{A}$		30		$\text{mV}/^\circ\text{C}$
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=32\text{V}$, $V_{GS}=0\text{V}$			10	μA
Gate- Source Leakage Current	Forward	$V_{GS}=+20\text{V}$, $V_{DS}=0\text{V}$			+100	nA
	Reverse	$V_{GS}=-20\text{V}$, $V_{DS}=0\text{V}$			-100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$	1.0		3.0	V
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10\text{V}$, $I_D=80\text{A}$		2.0	2.4	$\text{m}\Omega$
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{GS}=0\text{V}$, $V_{DS}=25\text{V}$, $f=1.0\text{MHz}$		5490	7300	pF
Output Capacitance	C_{OSS}			1220	1620	pF
Reverse Transfer Capacitance	C_{RSS}			155	233	pF
SWITCHING PARAMETERS						
Total Gate Charge	Q_G	$V_{GS}=10\text{V}$, $V_{DS}=30\text{V}$, $I_D=1.3\text{A}$ (Note 4, 5)		200	220	nC
Gate to Source Charge	Q_{GS}			19		nC
	Q_{GS2}			9.5		nC
Gate to Drain Charge	Q_{GD}			12		nC
Turn-ON Delay Time	$t_{D(ON)}$	$V_{DD}=30\text{V}$, $I_D=0.5\text{A}$, $R_{GEN}=4.7\Omega$, $V_{GS}=10\text{V}$ (Note 4, 5)		58	70	ns
Rise Time	t_R			260	310	ns
Turn-OFF Delay Time	$t_{D(OFF)}$			1810	1860	ns
Fall-Time	t_F			987	1160	ns
Equivalent Series Resistance (G-S)	ESR			1.1		Ω
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I_S				219	A
Maximum Body-Diode Pulsed Current	I_{SM}				876	A
Drain-Source Diode Forward Voltage	V_{SD}	$I_S=80\text{A}$, $V_{GS}=0\text{V}$			1.3	V
Body Diode Reverse Recovery Time	t_{rr}	$I_S=80\text{A}$, $V_{GS}=0\text{V}$, $di_F/dt=100\text{A}/\mu\text{s}$		54		ns
Body Diode Reverse Recovery Charge	Q_{rr}	(Note 4)		49		nC

Notes: 1. Repetitive Rating: Pulse width limited by maximum junction temperature

2. $L = 3\text{mH}$, $I_{AS} = 24\text{A}$, $V_{DD} = 30\text{V}$, $R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$

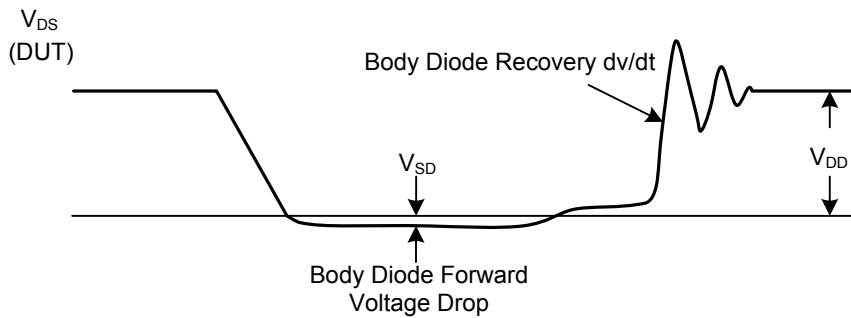
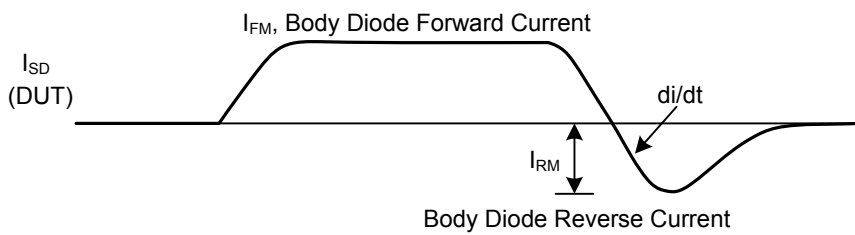
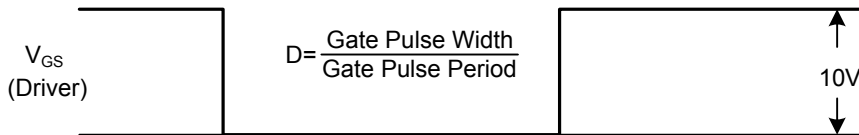
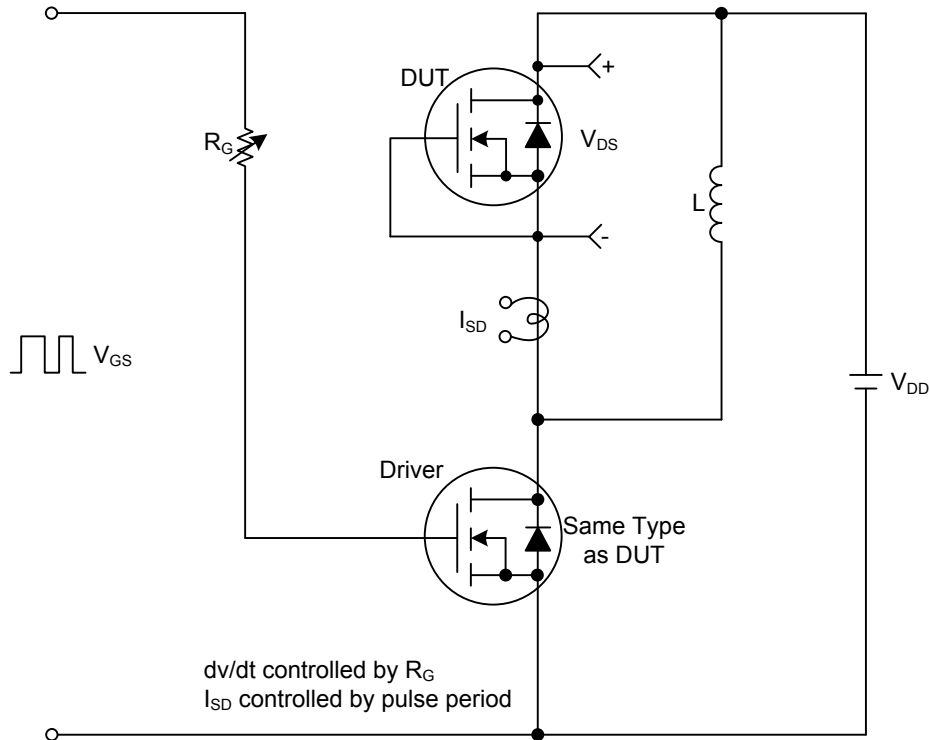
3. $I_{SD} \leq 80\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

4. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$

5. Essentially independent of operating temperature.

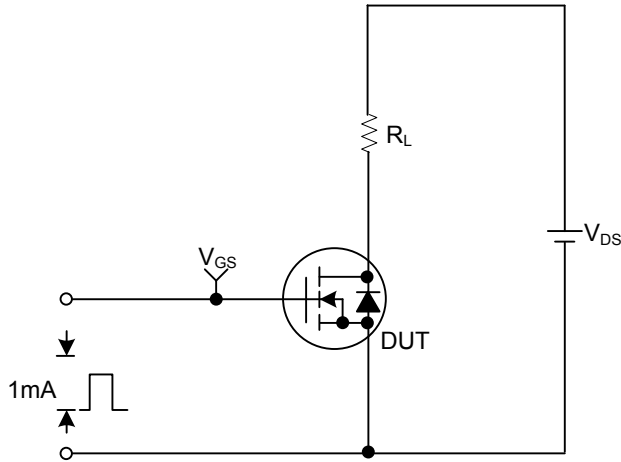
■ TEST CIRCUITS AND WAVEFORMS

Peak Diode Recovery dv/dt Test Circuit & Waveforms

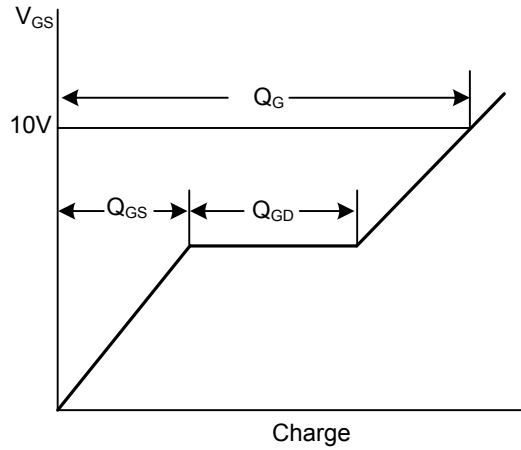


■ TEST CIRCUITS AND WAVEFORMS(Cont.)

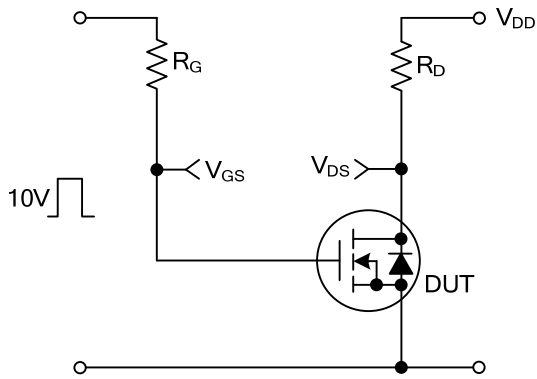
Gate Charge Test Circuit



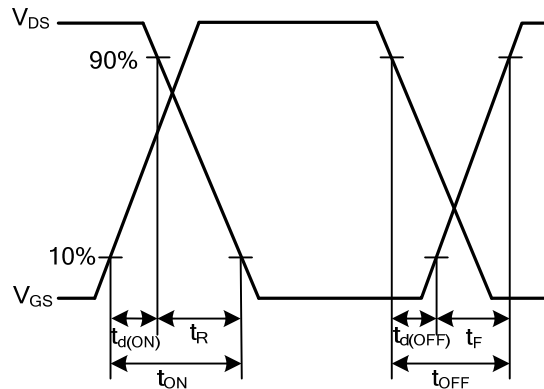
Gate Charge Waveforms



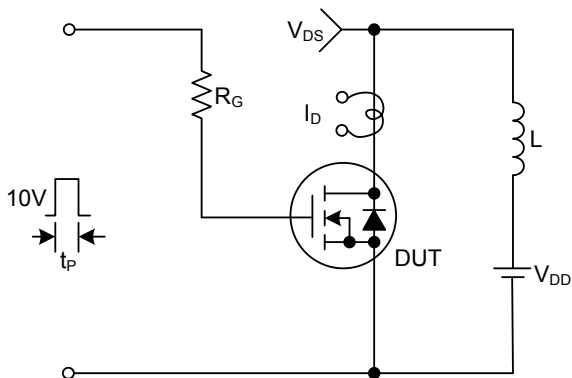
Resistive Switching Test Circuit



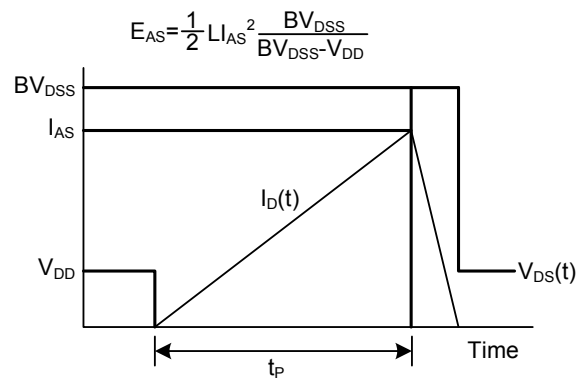
Resistive Switching Waveforms



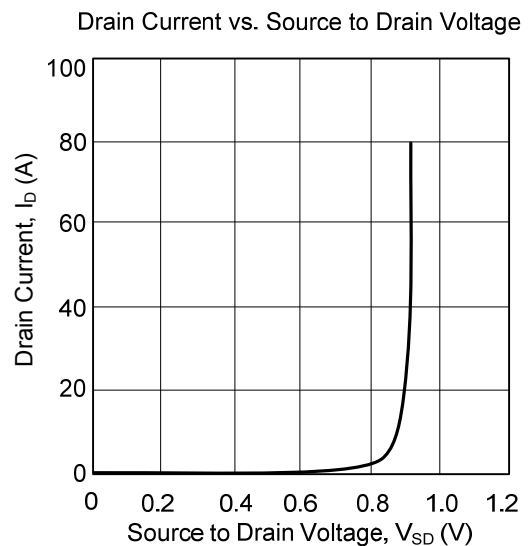
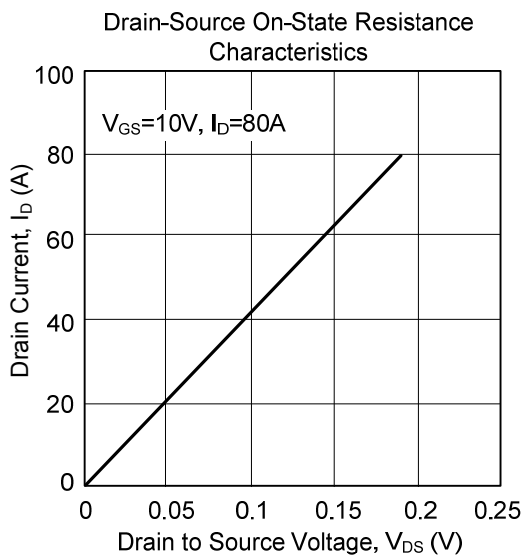
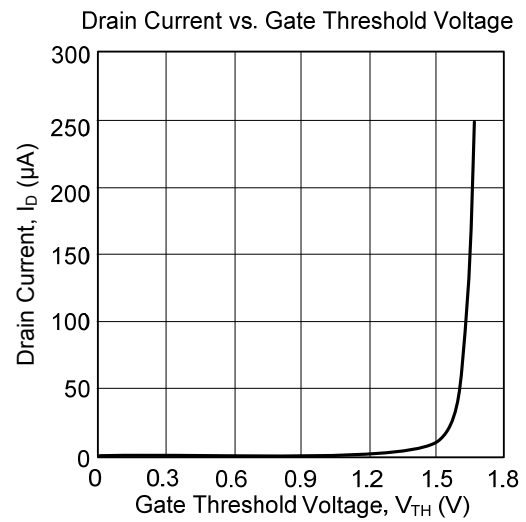
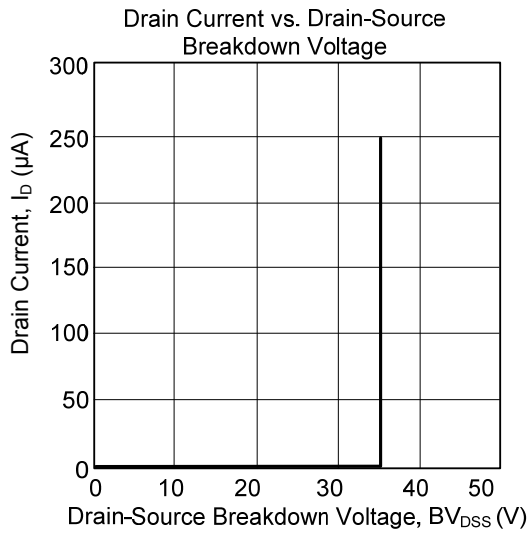
Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms



■ TYPICAL CHARACTERISTICS



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